MJ N-Channel Super Trench II Power MOSFET

Description

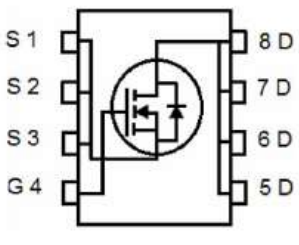
The MJXP065N10AGU uses Super Trench II technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.

General Features

- V_{DS}=100V,I_D=90A
  $R_{DS(ON)}$=5.3mΩ (typical) @ V_{GS}=10V
  $R_{DS(ON)}$=6.5mΩ (typical) @ V_{GS}=4.5V
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150°C operating temperature
- Pb-free lead plating

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic Diagram

Top View

Bottom View

DFN 5X6

100% UIS TESTED! 100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P065N10AGU	MJXP065N10AGU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	90	A
Drain Current-Continuous(T_C =100℃)	$I_{D(100℃)}$	61	A
Pulsed Drain Current	I_{DM}	360	A
Maximum Power Dissipation	P_D	110	W
Derating factor		0.88	W/℃
Single pulse avalanche energy (Note 5)	E_{AS}	320	mJ
Operating Junction and Storage Temperature Range	T_J ,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case (Note 2)	$R_{\theta JC}$	1.14	℃/W

Electrical Characteristics (T_C=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=100V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{DS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	1.2	1.8	2.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V,I_D=45A	-	5.3	6.0	mΩ
		V_{GS}=4.5V,I_D=45A	-	6.5	7.0	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=45A	-	60	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{lss}	V_{DS}=50V,V_{GS}=0V F=1.0MHz	-	4680	-	PF
Output Capacitance	C_{oss}		-	316	-	PF
Reverse Transfer Capacitance	C_{rss}		-	14.5	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=50V,I_D=45A V_{GS}=10V,R_G=3Ω	-	10	-	nS
Turn-on Rise Time	t_r		-	6	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	51	-	nS
Turn-Off Fall Time	t_f		-	9	-	nS
Total Gate Charge	Q_g	V_{DS}=50V,I_D=45A V_{GS}=10V	-	76	-	nC
Gate-Source Charge	Q_{gs}		-	15.3	-	nC
Gate-Drain Charge	Q_{gd}		-	17.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=45A	-	-	1.2	V
Diode Forward Current (Note 2)	I_S		-	-	90	A
Reverse Recovery Time	t_{rr}	T_J=25°C,I_F=45A di/dt= 100A/μs (Note 3)	-	55	-	nS
Reverse Recovery Charge	Q_{rr}		-	135	-	nC

Notes:

① Repetitive Rating: Pulse width limited by maximum junction temperature.
② Surface Mounted on FR4 Board, t ≤ 10 sec.
③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
④ Guaranteed by design, not subject to production
⑤ EAS condition : T_J=25℃,V_{DD}=50V,V_G=10V,L=0.5mH,Rg=25Ω

Typical Electrical and Thermal Characteristics

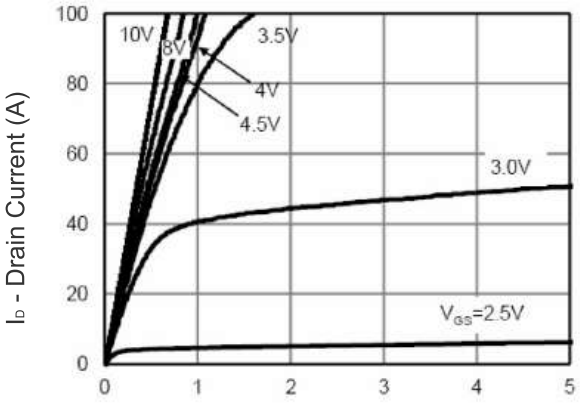


Figure 1 Output Characteristics

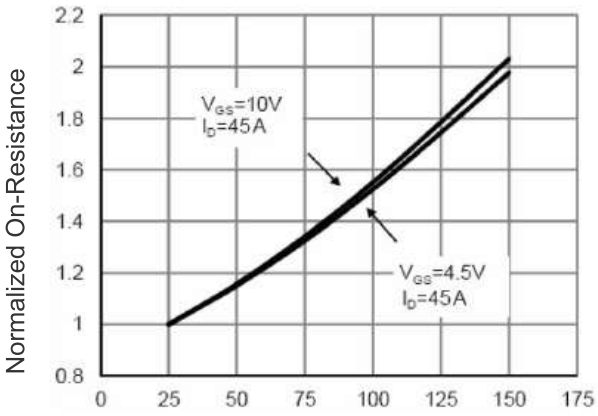


Figure 4 Rdson-Junction Temperature

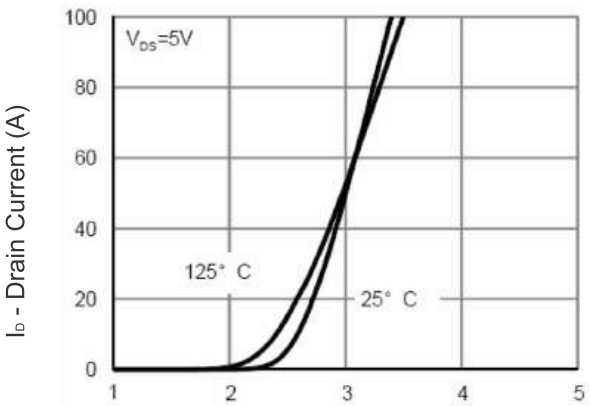


Figure 2 Transfer Characteristics

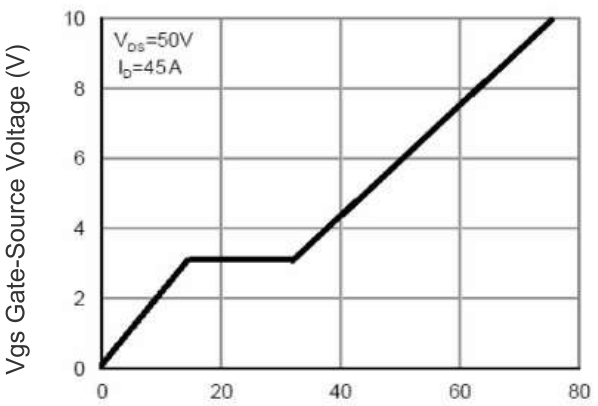


Figure 5 Gate Charge

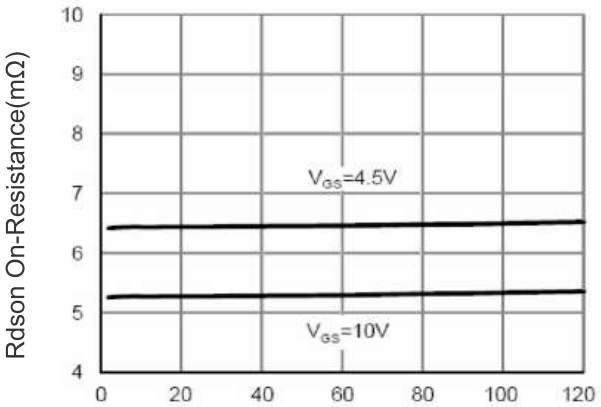


Figure 3 Rdson- Drain Current

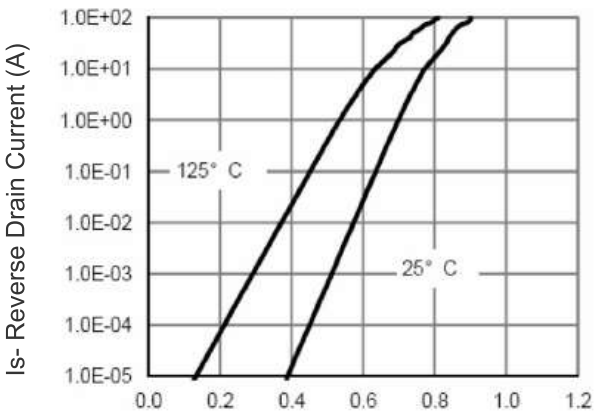
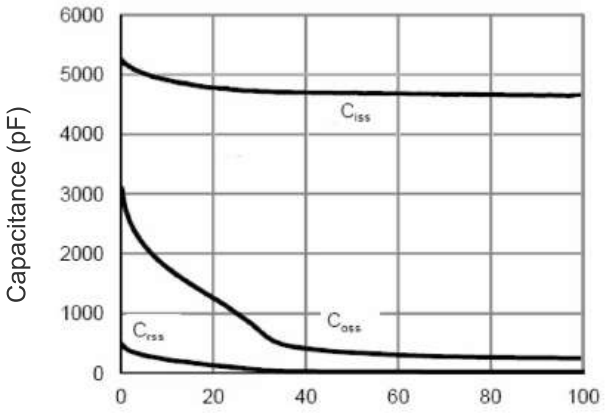
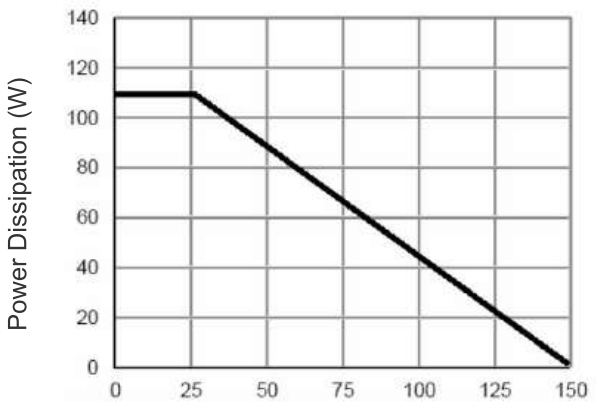


Figure 6 Source- Drain Diode Forward



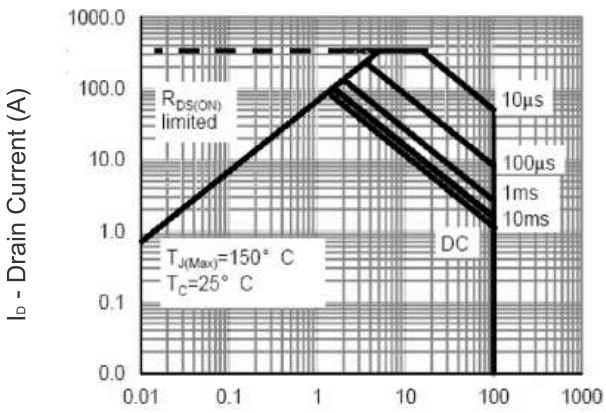
Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



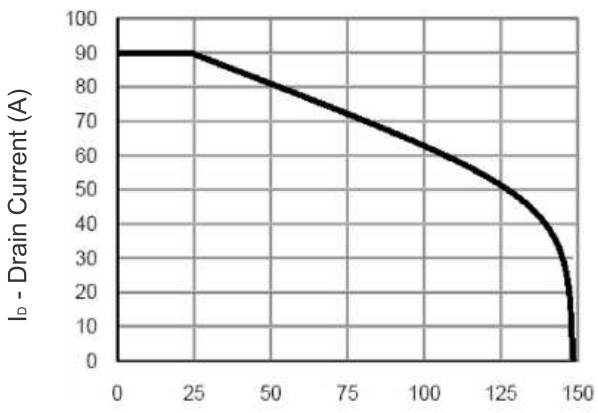
TJ-Junction Temperature(℃)

Figure 9 Power De-rating



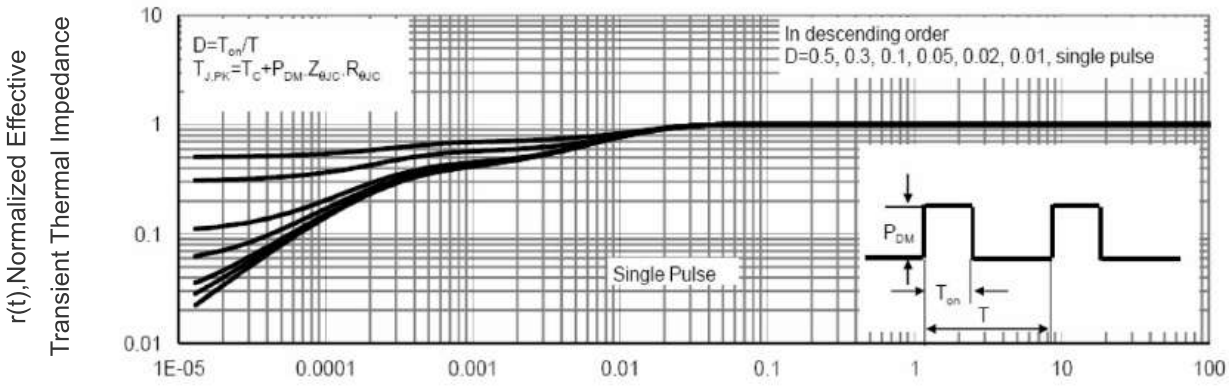
Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



TJ-Junction Temperature(℃)

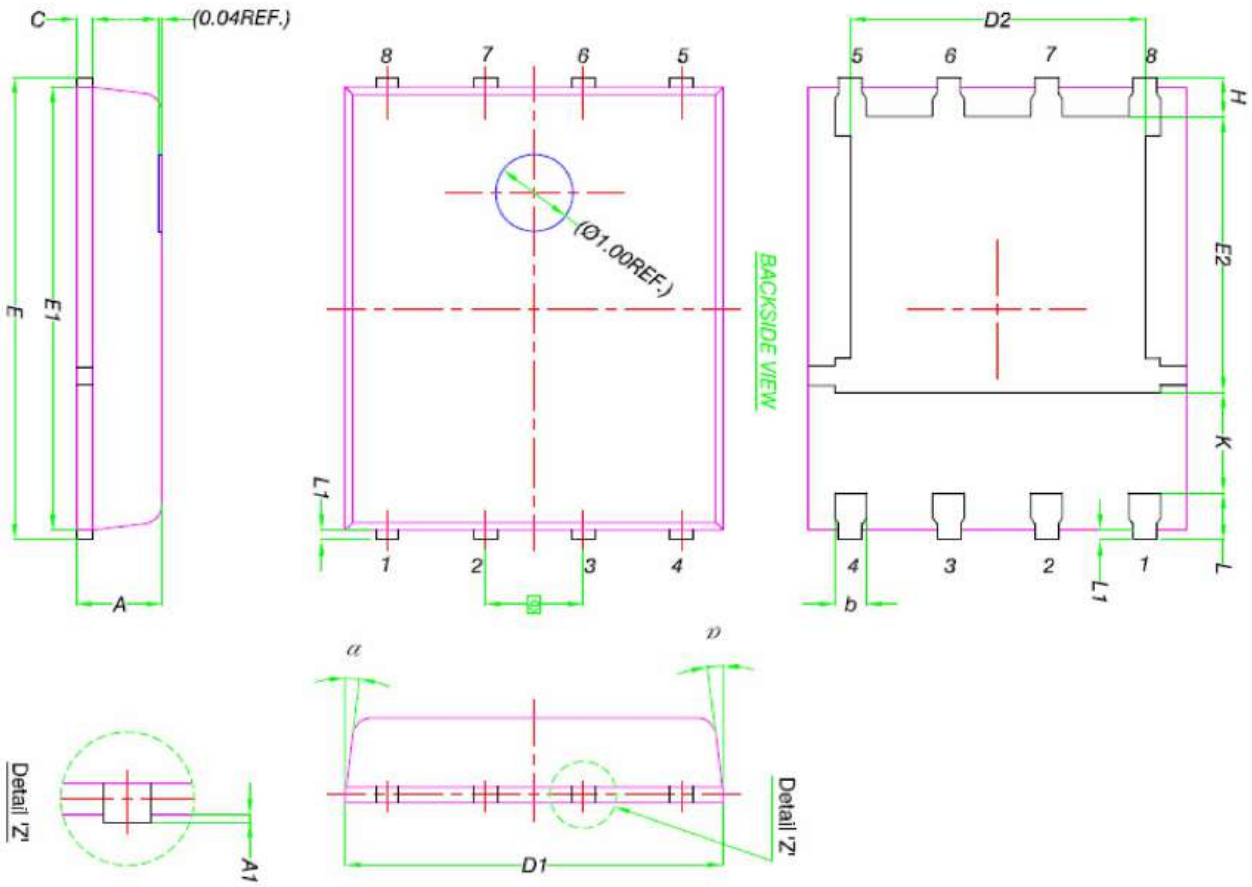
Figure 10 Current De-rating



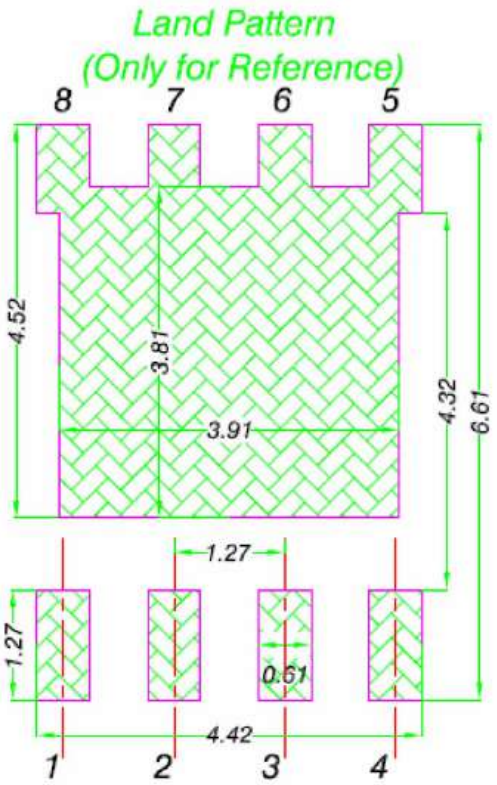
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
α	0°	-	12°



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