



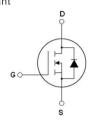
N-Channel Super Junction Power MOSFET II

General Description

The series of devices use advanced super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- New technology for high voltage device
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
 ROHS compliant





Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

Vds	800	V
RDS(ON) TYP	1000	mΩ
lo	5	A

Schematic diagram

TO-252

Package Marking And Ordering Information

Device	Device Package	Marking
MJ80R1K2K	TO-252	MJ80R1K2K

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Ves=0V)	Vds	800	V
Gate-Source Voltage (VDS=0V)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	Id (dc)	5	А
Continuous Drain Current at Tc=100°C	Id (dc)	3	А
Pulsed drain current (Note 1)	DM (pluse)	15	А
Maximum Power Dissipation (Tc=25°C)	Po	81	W
Derate above 25°C	Po	0.65	W/°C
Single pulse avalanche energy (Note 2)	Eas	75	mJ
Avalanche current (Note 1)	lar	2.5	А
Repetitive Avalanche energy, t_{AR} limited by T_{jmax} (Note 1)	Ear	0.4	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, V⊳s ≤480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	5	V/ns
Operating Junction and Storage Temperature Range	Tj,Tstg	-55+150	°C

* limited by maximum junction temperature





Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	1.54	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	62	°C/W

Table 3. Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
On/off states							
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V Id=250µA	800	-	-	V	
Zero Gate Voltage Drain Current (Tc=25°C)	loss	VDS=800V,VGS=0V	_	-	1	μA	
Zero Gate Voltage Drain Current (Tc=125℃)	IDSS	VDS=800V,VGS=0V	-	-	100	μA	
Gate-Body Leakage Current	lgss	Vgs=±30V,Vds=0V	-	-	±100	nA	
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250µA	2.5	3	3.5	V	
Drain-Source On-State Resistance	Rds(ON)	Vgs=10V,Id=2.5A	-	1000	1200	mΩ	
Dynamic Characteristics				1	1		
Forward Transconductance	g⊧s	V _{DS} =20V,I _D =2.5A	-	5.5	-	S	
Input Capacitance	Cies		-	680	-	PF	
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	55	_	PF	
Reverse Transfer Capacitance	Crss	-	-	3.5	-	PF	
Total Gate Charge	Qg		-	14.5	22	nC	
Gate-Source Charge	Qgs	Vds=480V,Id=5A Vgs=10V	-	2.8	_	nC	
Gate-Drain Charge	Qgd	-	-	5.5	-	nC	
Intrinsic gate resistance	Rg	f=1 MHz open drain	-	2	_	Ω	
Switching times							
Turn-on Delay Time	td(on)		-	7	-	nS	
Turn-on Rise Time	tr	VDD=480V,ID=2.5A	-	5	_	nS	
Turn-Off Delay Time	td(off)	R _G =15Ω,V _{GS} =10V	-	70	85	nS	
Turn-Off Fall Time	tr	-	-	9	15	nS	
Source- Drain Diode Characteristics				1	<u> </u>	1	
Source-drain current (Body Diode)	Isd		-	-	5	A	
Pulsed Source-drain current (Body Diode)	Isdm	− Tc=25°C	-	-	15	А	
Forward On Voltage	Vsd	Tj=25°C,Isd=5A,Vgs=0V	-	0.85	1.2	V	
Reverse Recovery Time	trr		_	240		nS	
Reverse Recovery Charge	Qrr	Tj=25°C,I⊧=5A di/dt=100A/µs	-	2.2	-	uC	
Peak reverse recovery Current	Irrm		_	16		A	



0.5

0

5

ID, Drain Current (A)

Figure 5 Static drain-source

on resistance

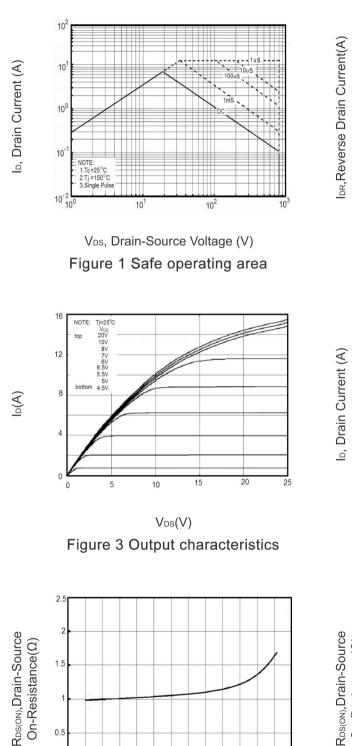


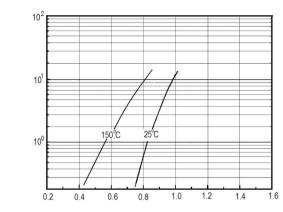
Notes

1.Repetitive Rating: Pulse width limited by maximum junction temperature

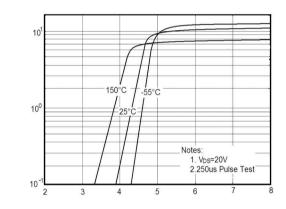
2.Tj=25°C,VDD=50V,VG=10V, RG=25Ω

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

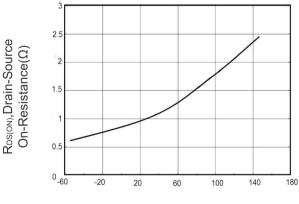




Vsp,Source-Drain Voltage(V) Figure 2 Source-Drain Diode **Forward Voltage**



V_{GS}, Gate-Source Voltage (V) Figure 4 Transfer characteristics



T_J, Junction Temperature (°C) Figure 6 RDS(ON) vs Junction Temperature

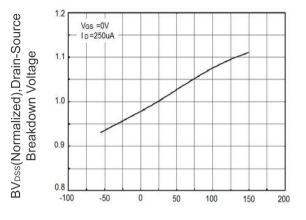
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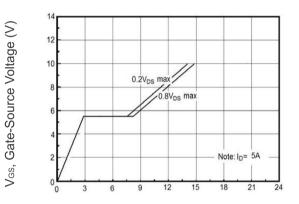


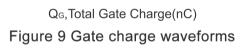
I_D, Drain Current (A)

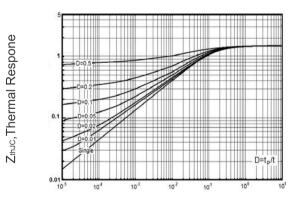
Capacitances(pF)



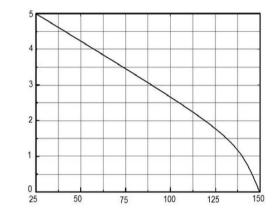
TJ, Junction Temperature (°C) Figure 7 BVDss vs Junction Temperature



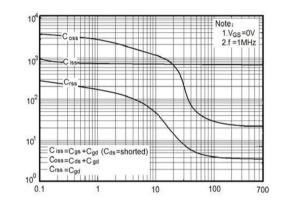




t_₽,Square Wave Pulse(S) Figure 11 Transient Thermal Impedance



Tc, Case Temperature (°C) Figure 8 Maximum I⊵ vs Junction Temperature

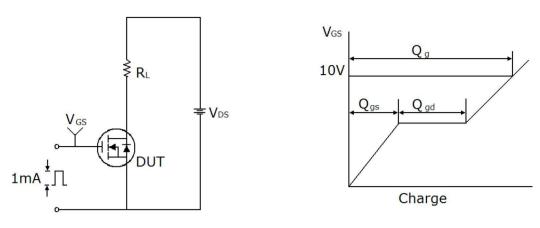


V_{DS}, Drain-Source Voltage (V) Figure 10 Capacitance

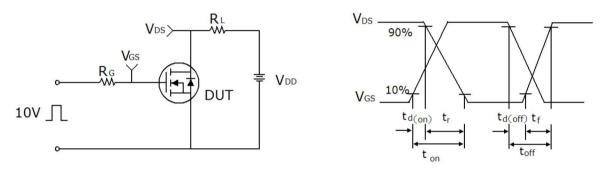




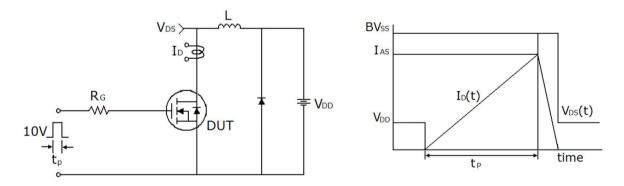
Test circuit



Gate charge test circuit & Waveform





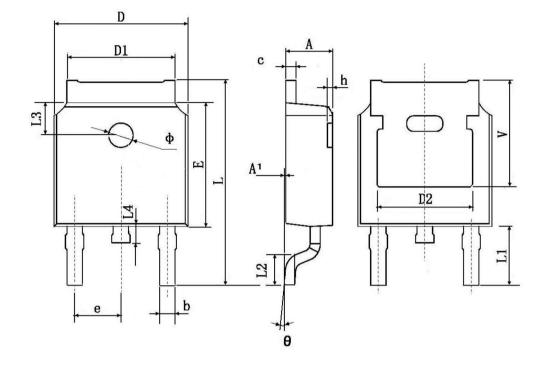


Unclamped Inductive Switching Test Circuit & Waveforms





TO-252 Package Information



Cump hal	Dimensions	In Millimeters	Dimension	s In Inches	
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
e	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350) TYP.	0.211 TYP.		





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