



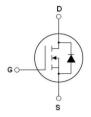
# N-Channel Super Junction Power MOSFET III

### **General Description**

The series of devices use advanced super junction technology and design to provide excellent R<sub>DS(ON)</sub> with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

### **Features**

- ◆ New technology for high voltage device
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ROHS compliant





Schematic diagram

TO-251

### **Application**

- ◆ Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)

VDS	700	V
Rds(on)TYP	820	mΩ
ID	5	А

### Package Marking And Ordering Information

Device	Device Package	Marking		
MJ70T900I	TO-251	MJ70T900I		

### Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vcs=0V)	VDS	700	V
Gate-Source Voltage (V <sub>DS</sub> =0V) ,AC (f>1 Hz)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	ID (DC)	5	А
Continuous Drain Current at Tc=100°C	ID (DC)	3	А
Pulsed drain current (Note 1)	IDM (pluse)	20	А
Maximum Power Dissipation (Tc=25°C)	Po	46	W
Derate above 25°C	Po	0.37	W/°C
Single pulse avalanche energy (Note 2)	Eas	52	mJ
Avalanche current (Note 1)	lar	0.9	А
Repetitive Avalanche energy, tar limited by T <sub>jmax</sub> (Note 1)	Ear	0.14	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, V <sub>DS</sub> ≤480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V <sub>DS</sub> ≤480 V,I <sub>SD</sub> <i<sub>D</i<sub>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	TJ,Tsтg	-55+150	°C





### Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	2.72	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	75	°C/W

# Table 3. Electrical Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BVDSS	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	700	-	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	loss	V <sub>DS</sub> =700V,V <sub>GS</sub> =0V	-	_	1	μΑ
Zero Gate Voltage Drain Current (Tc=125°C)	Ipss	V <sub>DS</sub> =700V,V <sub>GS</sub> =0V	-	-	50	μΑ
Gate-Body Leakage Current	lgss	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	3	-	4	V
Drain-Source On-State Resistance	RDS(ON)	V <sub>GS</sub> =10V,I <sub>D</sub> =2.5A	-	820	950	mΩ
Dynamic Characteristics	1					
Input Capacitance	Cies		-	370	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V F=1.0MHz	-	25	-	pF
Reverse Transfer Capacitance	Crss		-	0.5	-	pF
Total Gate Charge	Qg		-	10.5	15	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =480V,I <sub>D</sub> =5A V <sub>GS</sub> =10V	-	2.6	-	nC
Gate-Drain Charge	Qgd		-	5.3	-	nC
Switching times				ı	l	l
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =420V,I <sub>D</sub> =3A	-	4	-	nS
Turn-Off Delay Time	td(off)	R <sub>G</sub> =5Ω,V <sub>G</sub> <sub>S</sub> =10V	-	55	-	nS
Turn-Off Fall Time	tf		-	11	-	nS
Source- Drain Diode Characteristics	<u> </u>			l		
Source-drain current (Body Diode)	Isp		-	_	5	А
Pulsed Source-drain current (Body Diode)	Isdm	Tc=25°C	-	_	20	А
Forward On Voltage	Vsp	Tj=25°C,IsD=5A,Vgs=0V	-	0.9	1.2	V
Reverse Recovery Time	t <sub>rr</sub>		-	210	-	nS
Reverse Recovery Charge	Qrr	T <sub>j</sub> =25°C,I <sub>F</sub> =2.5A		-	uC	
Peak reverse recovery Current	Irrm		_	6.5	_	Α

lor, Reverse Drain Current(A)

lb, Drain Current (A)

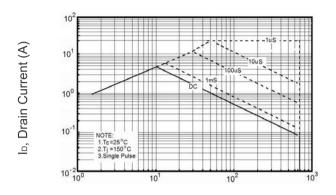


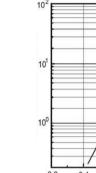


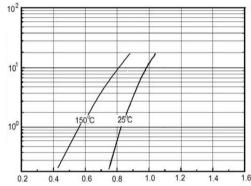
#### **Notes**

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- $2.T_j=25^{\circ}C,V_{DD}=50V,V_{G}=10V,R_{G}=25\Omega$

### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)





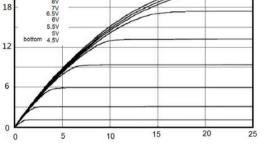


Vsp,Source-Drain Voltage(V) Figure 2 Source-Drain Diode

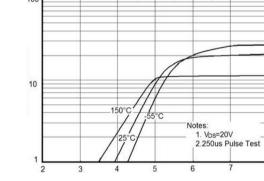
V<sub>DS</sub>, Drain-Source Voltage (V)

Figure 1 Safe operating area

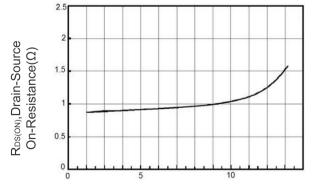
Forward Voltage 100



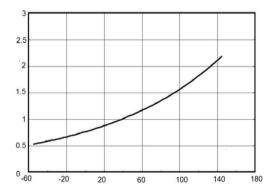
V<sub>DS</sub>(V) Figure 3 Output characteristics



V<sub>GS</sub>, Gate-Source Voltage (V) Figure 4 Transfer characteristics



R<sub>DS(ON)</sub>, Drain-Source On-Resistance(Ω)



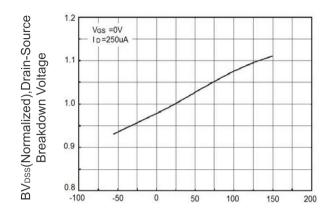
ID, Drain Current (A) Figure 5 Static drain-source on resistance

T<sub>J</sub>, Junction Temperature (°C) Figure 6 RDS(ON) vs Junction Temperature

lp, Drain Current (A)

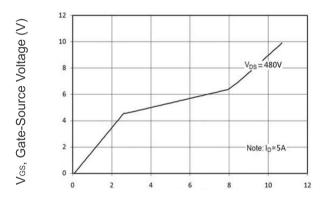
Capacitances(pF)





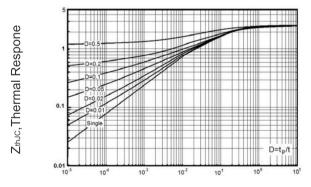
T<sub>J</sub>, Junction Temperature (°C)

Figure 7 BVDSS vs Junction Temperature



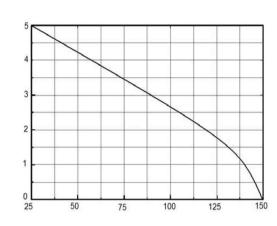
Q<sub>G</sub>,Total Gate Charge(nC)

Figure 9 Gate charge waveforms



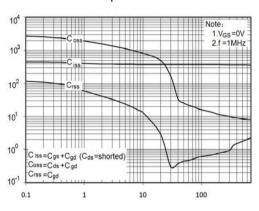
tp,Square Wave Pulse(S)

Figure 11 Transient Thermal Impedance



Tc, Case Temperature (°C)

Figure 8 Maximum ID vs Junction Temperature

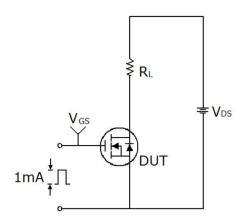


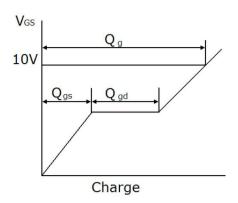
V<sub>DS</sub>, Drain-Source Voltage (V)

Figure 10 Capacitance

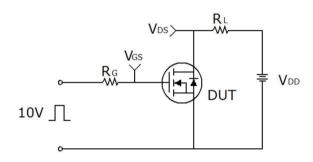


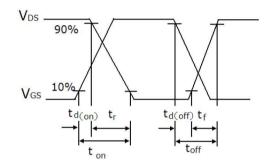
## Test circuit



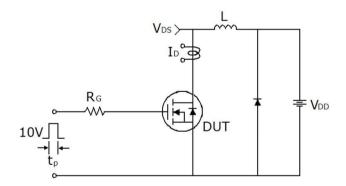


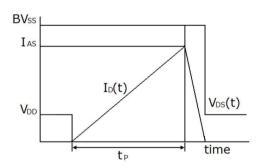
Gate charge test circuit & Waveform





Switch Time Test Circuit



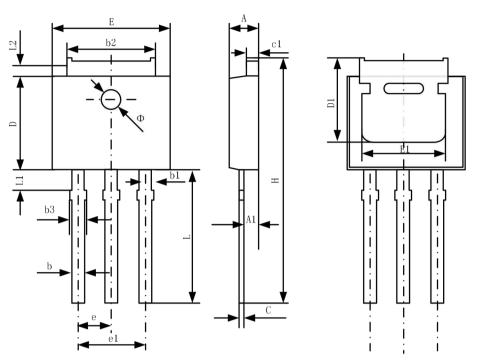


Unclamped Inductive Switching Test Circuit & Waveforms





## TO-251 Package Information



	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
А	2.20	2.35	0.087	0.093
A1	0.90	1.10	0.035	0.043
b	0.56	0.69	0.022	0.027
b1	0.77	0.90	0.030	0.035
b2	5.23	5.43	0.206	0.214
b3		1.05	0.000	0.041
С	0.46	0.59	0.018	0.023
c1	0.46	0.59	0.018	0.023
D	6.00	6.20	0.236	0.244
D1	5.20		0.205	
E	6.50	6.70	0.256	0.264
E1	4.60	5.00	0.181	
e	2.24	2.34	0.088	0.092
e1	4.47	4.67	0.176	0.184
Н	16.18	16.78	0.637	0.661
L	9.00	9.60	0.354	0.378
L1	0.95	1.35	0.037	0.053
L2	0.90	1.25	0.035	0.049





#### Attention:

Any and all MJ power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MJ power representative nearest you before using any MJ power products described or contained herein in such applications.

MJ power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MJ power products described or contained herein.

Specifications of any and all MJ power products described or contained herein stipulate the erformance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

MJ power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all MJ power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or therwise, without the prior written permission of MJ power Semiconductor CO.,LTD.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MJ power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MJ power product that you intend to use.

This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.