



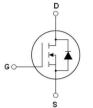
N-Channel Super Junction Power MOSFET II

General Description

The series of devices use advanced super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- New technology for high voltage device
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant





Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

Vds	700	V
Rds(on)typ.	1200	mΩ
lo	4	А

Schematic diagram

TO-220

Package Marking And Ordering Information

Device	Device Package	Marking
MJ70R1K2	TO-220	MJ70R1K2

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	MJ70R1K2	Unit
Drain-Source Voltage (Ves=0V)	Vds	700	V
Gate-Source Voltage (VDs=0V)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	ID (DC)	4	А
Continuous Drain Current at Tc=100°C	Id (dc)	2.5	А
Pulsed drain current (Note 1)	DM (pluse)	12	А
Maximum Power Dissipation (Tc=25℃)	Po	46	W
Derate above 25°C	Po	0.37	W/°C
Single pulse avalanche energy (Note 2)	Eas	130	mJ
Avalanche current (Note 1)	lar	2	А
Repetitive Avalanche energy, t_{AR} limited by T_{jmax} (Note 1)	Ear	0.2	mJ

Parameter	Symbol	MJ70R1K2	Unit
Drain Source voltage slope, V⊳s ≤480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	Tj,Tstg	-55+150	°C

* limited by maximum junction temperature





Table 2. Thermal Characteristic

Parameter	Symbol	MJ70R1K2	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	2.7	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	62	°C/W

Table 3. Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states	·					
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250µA	700	-	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	IDSS	VDS=700V,VGS=0V	-	-	1	μA
Zero Gate Voltage Drain Current (Tc=125°C)	IDSS	VDS=700V,VGS=0V	-	-	50	μA
Gate-Body Leakage Current	lgss	Vgs=±30V,Vps=0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250µA	2.5	3	3.5	V
Drain-Source On-State Resistance	Rds(on)	Vgs=10V,Id=2.5A	-	1200	1400	mΩ
Dynamic Characteristics				1	1	1
Forward Transconductance	grs	VDS=20V,ID=2.5A	-	4	-	S
Input Capacitance	Cies		-	280	-	PF
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	26	-	PF
Reverse Transfer Capacitance	Crss	-	-	2.3	-	PF
Total Gate Charge	Qg		-	6.5	10	nC
Gate-Source Charge	Qgs	V _{DS} =480V,I _D =4A V _{GS} =10V	-	1.3	-	nC
Gate-Drain Charge	Qgd	-	-	2.5	-	nC
Intrinsic gate resistance	Rg	f=1 MHz open drain	-	2.5	-	Ω
Switching times	I	1		1		1
Turn-on Delay Time	td(on)		-	6	-	nS
Turn-on Rise Time	tr	Vdd=380V,Id=2.5A	-	3	-	nS
Turn-Off Delay Time	td(off)	R _G =20Ω,V _{GS} =10V	-	48	60	nS
Turn-Off Fall Time	tr	-	-	8	15	nS
Source- Drain Diode Characteristics				1	1	1
Source-drain current (Body Diode)	Isp		-	-	4	A
Pulsed Source-drain current (Body Diode)	Isdm	Tc=25°C	-	-	12	А
Forward On Voltage	Vsd	Tj=25°C,Isd=4A,Vgs=0V	-	1	1.3	V
Reverse Recovery Time	trr		-	150	-	nS
Reverse Recovery Charge	Qrr	Tj=25°C,I⊧=4A di/dt=100A/µs	-	0.85	-	uC
Peak reverse recovery current	Irrm		_	11	_	A

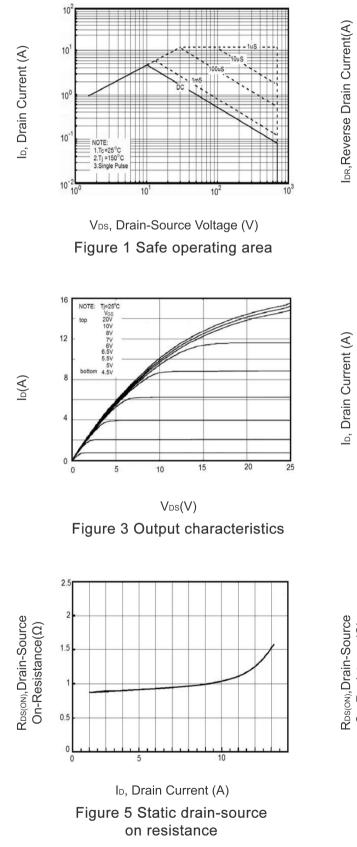


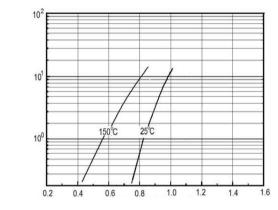


Notes

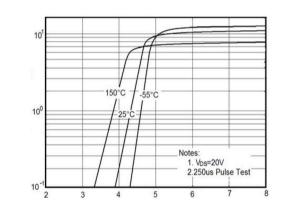
1.Repetitive Rating: Pulse width limited by maximum junction temperature 2.Tj=25°C,VDD=50V,VG=10V, RG=25 Ω

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

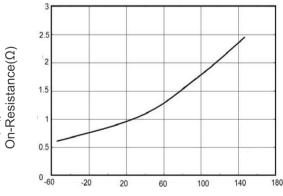




V_{SD},Source-Drain Voltage(V) Figure 2 Source-Drain Diode Forward Voltage



V_{GS}, Gate-Source Voltage (V) Figure 4 Transfer characteristics



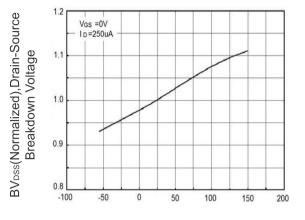
TJ, Junction Temperature (°C) Figure 6 RDS(ON) vs Junction Temperature



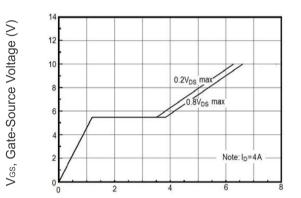


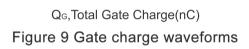
I_D, Drain Current (A)

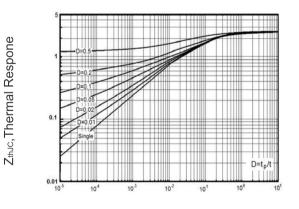
Capacitances(pF)



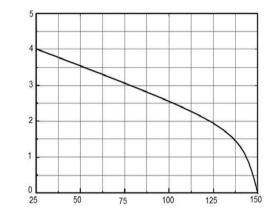
TJ, Junction Temperature (°C) Figure 7 BVDss vs Junction Temperature



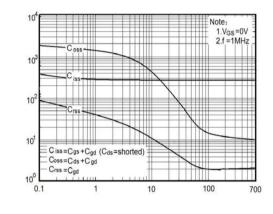




t_P,Square Wave Pulse(S) Figure 11 Transient Thermal Impedance



Tc, Case Temperature (°C) Figure 8 Maximum I⊳ vs Junction Temperature



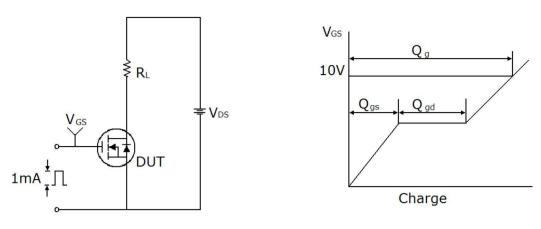
V_{DS}, Drain-Source Voltage (V) Figure 10 Capacitance



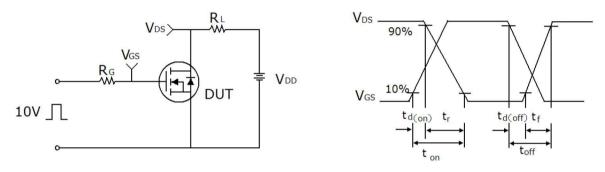




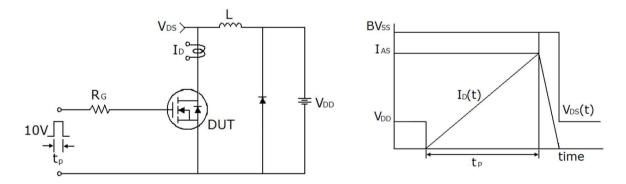
Test circuit



Gate charge test circuit & Waveform



Switch Time Test Circuit

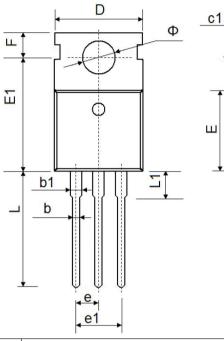


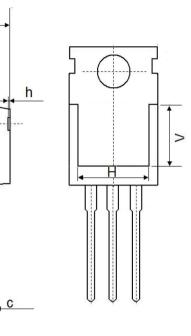
Unclamped Inductive Switching Test Circuit & Waveforms





TO-220-3L-C Package Information





Symphol	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
А	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
е	2.540	2.540 TYP.		TYP.
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
Н	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295	REF.
Ф	3.400	3.800	0.134	0.150

A1





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