



MJ N-Channel Enhancement Mode Power MOSFET

Description

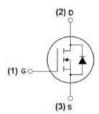
The MJ6890D uses advanced trench technology and design to provide excellent $R_{\text{DS(ON)}}$ with low gate charge. It can be used in a wide variety of applications.

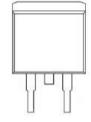
General Features

- ♦ $V_{DS} = 68V, I_{D} = 90A$ $R_{DS(ON)} < 7.5mΩ @ V_{GS} = 10V$ (Typ:6.5mΩ)
- ◆ Special process technology for high ESD capability
- ♦ High density cell design for ultra low Rdson
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ Excellent package for good heat dissipation

Application

- ◆ Power switching application
- ◆ Hard switched and High frequency circuits
- ◆ Uninterruptible power supply







Schematic diagram

Marking and pin assignment

TO-263-2L top view

100% UIS TESTED! 100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ6890D	MJ6890D	TO-263-2L	ii ii	-	-

Absolute Maximum Ratings (Tc=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	68	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	90	Α
Drain Current-Continuous(Tc =100°C)	ID(100°C)	63	Α
Pulsed Drain Current	Ірм	320	А
Maximum Power Dissipation	Po	130	W
Derating factor		0.86	W/°C
Single pulse avalanche energy (Note 5)	Eas	380	mJ
Operating Junction and Storage Temperature Range	TJ,TsTG	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	1.15	°C/W
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Electrical Characteristics (Tc=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250µA	68	73	-	V
Zero Gate Voltage Drain Current	loss	Vps=68V,Vgs=0V	-	-	1	μA
Gate-Body Leakage Current	lgss	V _{DS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250µA	2	3	4	V
Drain-Source On-State Resistance	Rds(ON)	V _{GS} =10V, I _D =30A	-	6.5	7.5	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =20A	20	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	Clss		-	3300	-	PF
Output Capacitance	Coss	V _{DS} =30V,V _{GS} =0V F=1.0MHz	_	450	-	PF
Reverse Transfer Capacitance	Crss		-	170	-	PF
Switching Characteristics (Note 4)	-	1				
Turn-on Delay Time	t _{d(on)}		-	15	-	nS
Turn-on Rise Time	tr	VDD=30V,ID=30A	-	94	-	nS
Turn-Off Delay Time	t _{d(off)}	$V_{GS}=30V,R_{GEN}=6\Omega$	-	46	- nS - nS	
Furn-Off Fall Time			_	32	-	nS
otal Gate Charge			-	35	-	nC
Gate-Source Charge	Qgs	V _{DS} =30V,I _D =20A V _{GS} =10V	-	11	-	nC
Gate-Drain Charge	Qgd		-	9	-	nC
Drain-Source Diode Characteristics		I				1
Diode Forward Voltage (Note 3)	Vsp	V _{GS} =0V,I _S =90A	_	-	1.2	V
Diode Forward Current (Note 2)	Is		_	_	90	А
Reverse Recovery Time			nS			
IJ=25°C, IF=90A		di/dt=100A/µs (Note 3)	-	51	-	nC
Forward Turn-On Time ton Intrinsic turn-on time is negligible(turn-on is dominated by) S+I D			

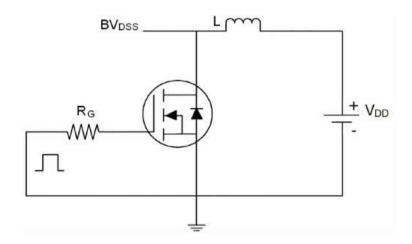
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, $t \le 10$ sec.
- ③ Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production
- § EAS condition: Tj=25°C,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω

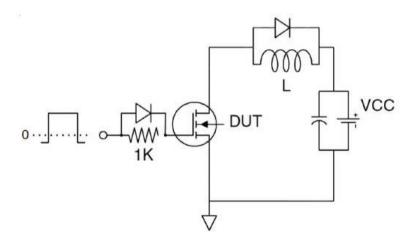




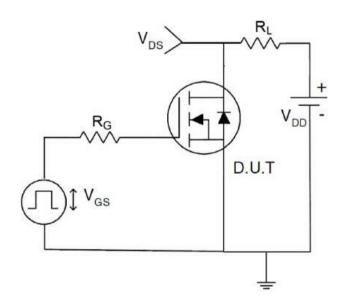
Test circuit



Eas test Circuit



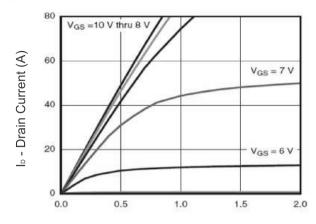
Gate charge test Circuit

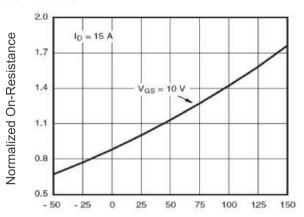


Switch Time Test Circuit



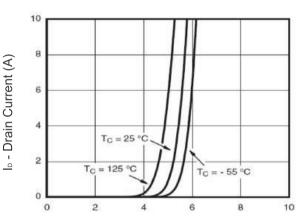
Typical Electrical and Thermal Characteristics (Curves)



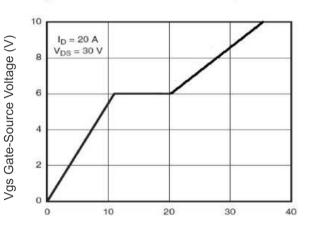


Vds Drain-Source Voltage (V)

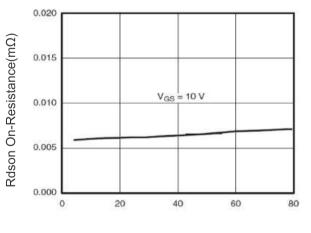
Figure 1 Output Characteristics



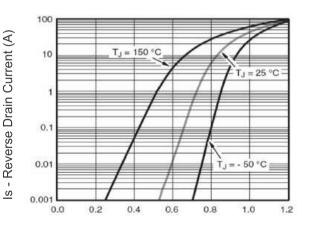
T_J -Junction Temperature(°C) Figure 4 Rdson-Junction Temperature



Vgs Gate-Source Voltage (V) Figure 2 Transfer Characteristics



Qg Gate Charge (nC) Figure 5 Gate Charge



I_D - Drain Current (A)

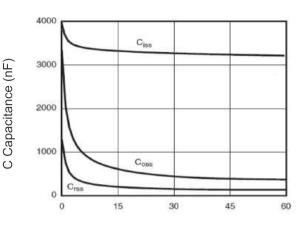
Figure 3 Rdson- Drain Current

Vsd Source-Drain Voltage (V) Figure 6 Source- Drain Diode Forward

BVDSS (normalized)



I_D - Drain Current (A)



74 I₀=250μA 70 66 62 -50 -25 0 25 50 75 100 125 150

Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds

1000

R_{DS(ON)}

limited

10

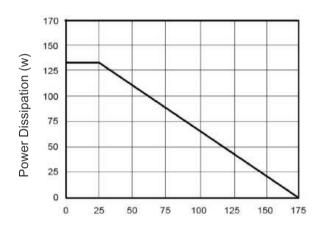
T_{J(Max)}=175°C

T_c=25°C

1

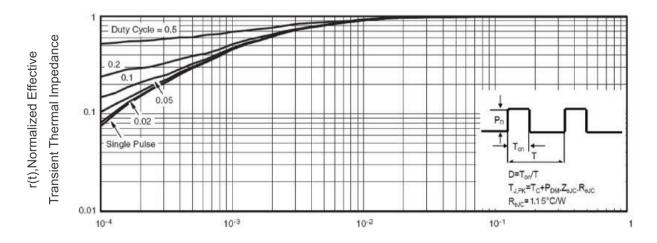
1
1
1
10
100

TJ -Junction Temperature(°C)
Figure 9 BVpss vs Junction Temperature



Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area

T_J -Junction Temperature(°C) Figure 10 Power De-rating



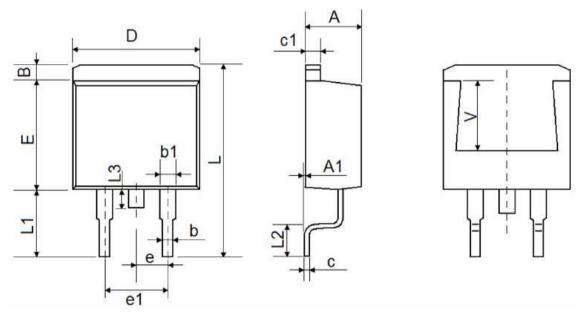
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance





TO-263-2L Package Information



Complete	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
c	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540	TYP.	0.100	TYP.	
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.600 REF		0.220	REF	





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