

N-Channel Super Junction Power MOSFET III

General Description

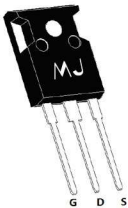
The series of devices use advanced super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- ◆ Optimized body diode reverse recovery performance
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ◆ ROHS compliant



Schematic diagram



TO-247

Application

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)
- ◆ LLC Half-bridge

V_{DS}	650	V
$R_{DS(ON)TYP}$	110	mΩ
I_D	28	A

□ Intrinsic fast-recovery body diode

Package Marking And Ordering Information

Device	Device Package	Marking
MJ65TF130T	TO-247	MJ65TF130T

Table 1. Absolute Maximum Ratings ($T_c=25^{\circ}C$)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	650	V
Gate-Source Voltage ($V_{DS}=0V$) AC ($f>1\text{ Hz}$)	V_{GS}	± 30	V
Continuous Drain Current at $T_c=25^{\circ}C$	I_D (DC)	28	A
Continuous Drain Current at $T_c=100^{\circ}C$	I_D (DC)	18	A
Pulsed drain current ^(Note 1)	I_{DM} (pluse)	112	A
Maximum Power Dissipation ($T_c=25^{\circ}C$)	P_D	260	W
Derate above $25^{\circ}C$	P_D	2.08	W/ $^{\circ}C$
Single pulse avalanche energy ^(Note 2)	E_{AS}	676	mJ
Avalanche current ^(Note 1)	I_{AR}	5.2	A
Repetitive Avalanche energy, t_{AR} limited by T_{jmax} ^(Note 1)	E_{AR}	3.2	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, $V_{DS} \leq 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt , $V_{DS} \leq 480\text{ V}, I_{SD} < I_D$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	$-55...+150$	$^{\circ}C$

* limited by maximum junction temperature

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	0.48	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C/W

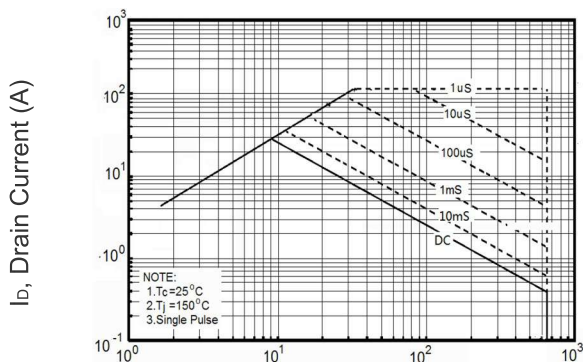
Table 3. Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	650	-	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	1	μA
Zero Gate Voltage Drain Current (Tc=125°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	100	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	3	3.5	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V,I _D =14A	-	110	140	mΩ
Dynamic Characteristics						
Input Capacitance	C _{ies}	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	2070	-	pF
Output Capacitance	C _{oss}		-	120	-	pF
Reverse Transfer Capacitance	C _{rss}		-	0.5	-	pF
Total Gate Charge	Q _g	V _{DS} =480V,I _D =28A V _{GS} =10V	-	37.5	-	nC
Gate-Source Charge	Q _{gs}		-	13	-	nC
Gate-Drain Charge	Q _{gd}		-	11.5	-	nC
Intrinsic gate resistance	R _G	f=1 MHz open drain	-	10	-	Ω
Switching times						
Turn-on Delay Time	t _{d(on)}	V _{DD} =380V,I _D =14A R _G =2.3Ω,V _{GS} =10V	-	14	-	nS
Turn-on Rise Time	t _r		-	12	-	nS
Turn-Off Delay Time	t _{d(off)}		-	65	-	nS
Turn-Off Fall Time	t _f		-	11	-	nS
Source- Drain Diode Characteristics						
Source-drain current (Body Diode)	I _{SD}	T _C =25°C	-	-	28	A
Pulsed Source-drain current (Body Diode)	I _{SDM}		-	-	112	A
Forward On Voltage	V _{SD}	T _J =25°C,I _{SD} =28A,V _{GS} =0V	-	0.9	1.2	V
Reverse Recovery Time	t _{rr}	T _J =25°C,I _F =14A di/dt=100A/μs	-	190	-	nS
Reverse Recovery Charge	Q _{rr}		-	2	-	uC
Peak reverse recovery Current	I _{rrm}		-	21	-	A

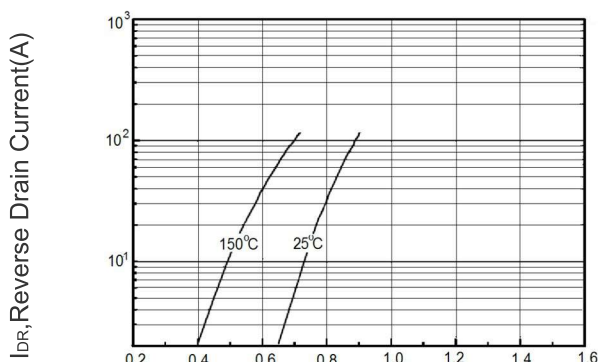
Notes

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
2. $T_J=25^{\circ}\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$

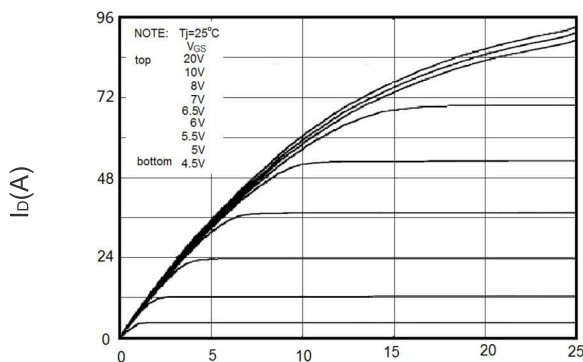
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)



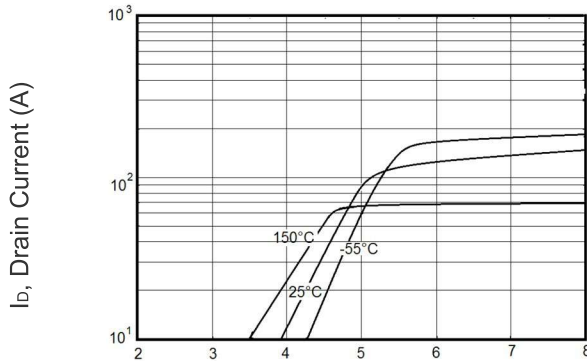
V_{DS} , Drain-Source Voltage (V)
Figure 1 Safe operating area



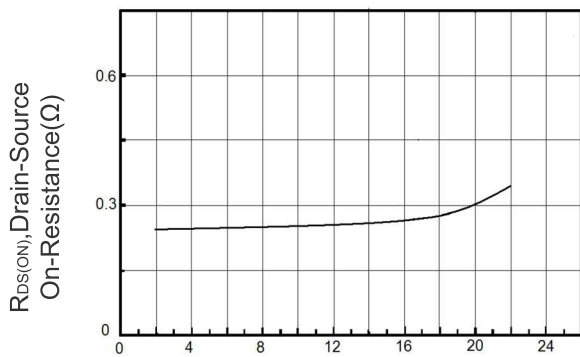
V_{SD} , Source-Drain Voltage(V)
Figure 2 Source-Drain Diode Forward Voltage



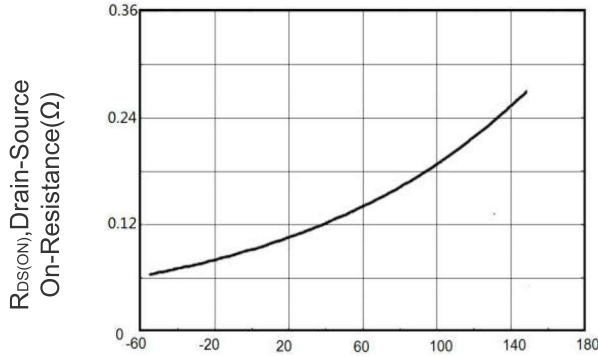
V_{DS} (V)
Figure 3 Output characteristics



V_{GS} , Gate-Source Voltage (V)
Figure 4 Transfer characteristics



I_D , Drain Current (A)
Figure 5 Static drain-source on resistance



T_J , Junction Temperature ($^{\circ}\text{C}$)
Figure 6 $R_{DS(ON)}$ vs Junction Temperature

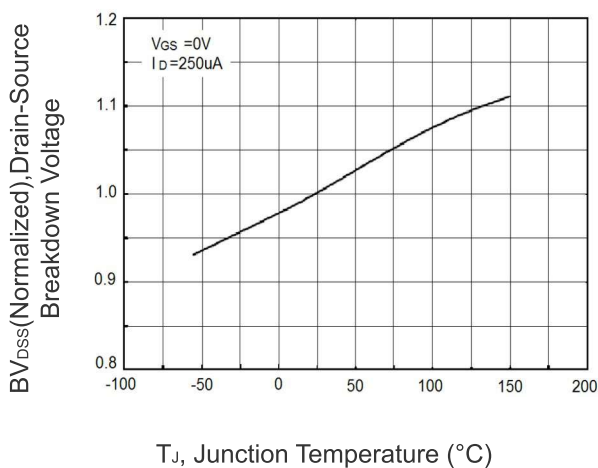


Figure 7 BV_{DSS} vs Junction Temperature

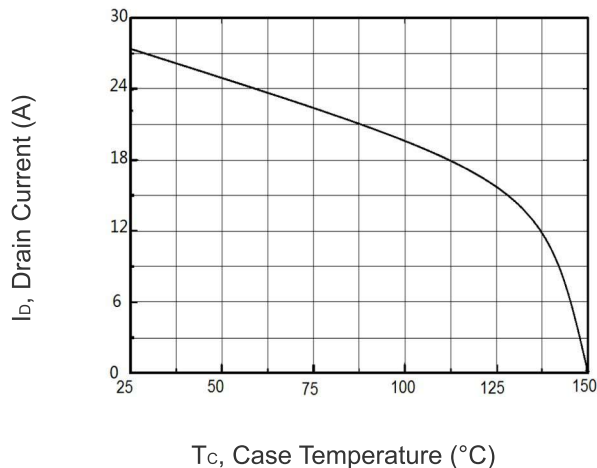


Figure 8 Maximum I_D vs Junction Temperature

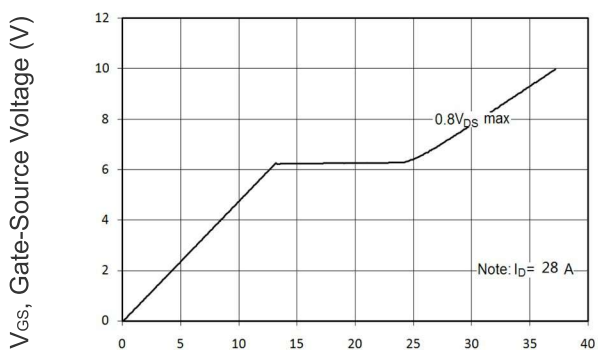


Figure 9 Gate charge waveforms

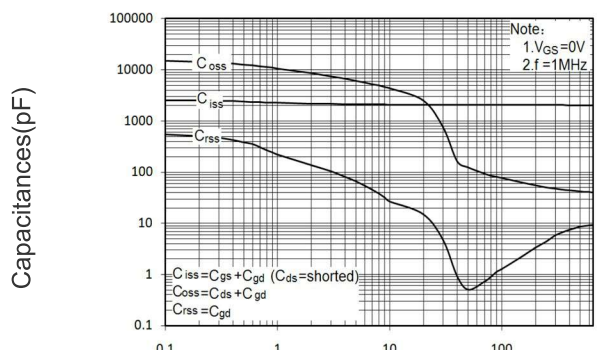


Figure 10 Capacitance

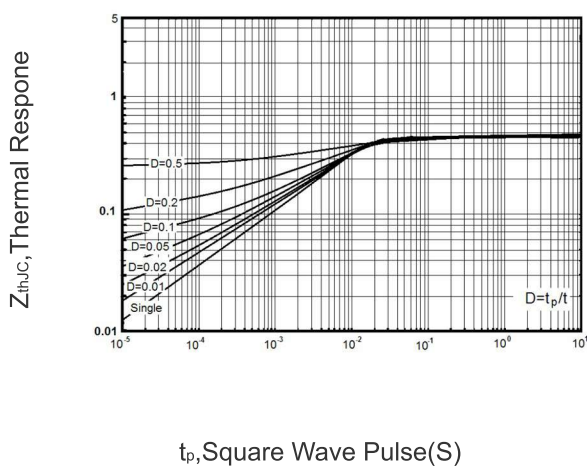
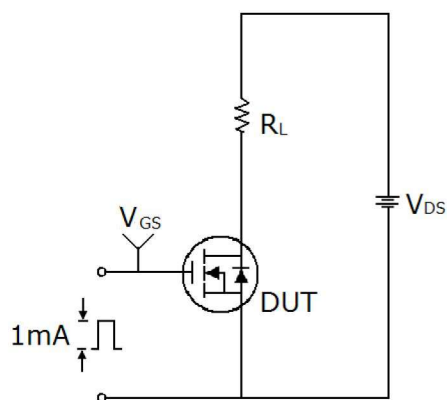
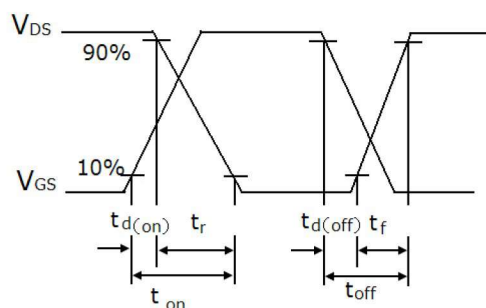


Figure 11 Transient Thermal Impedance

Test circuit



Gate charge test circuit & Waveform

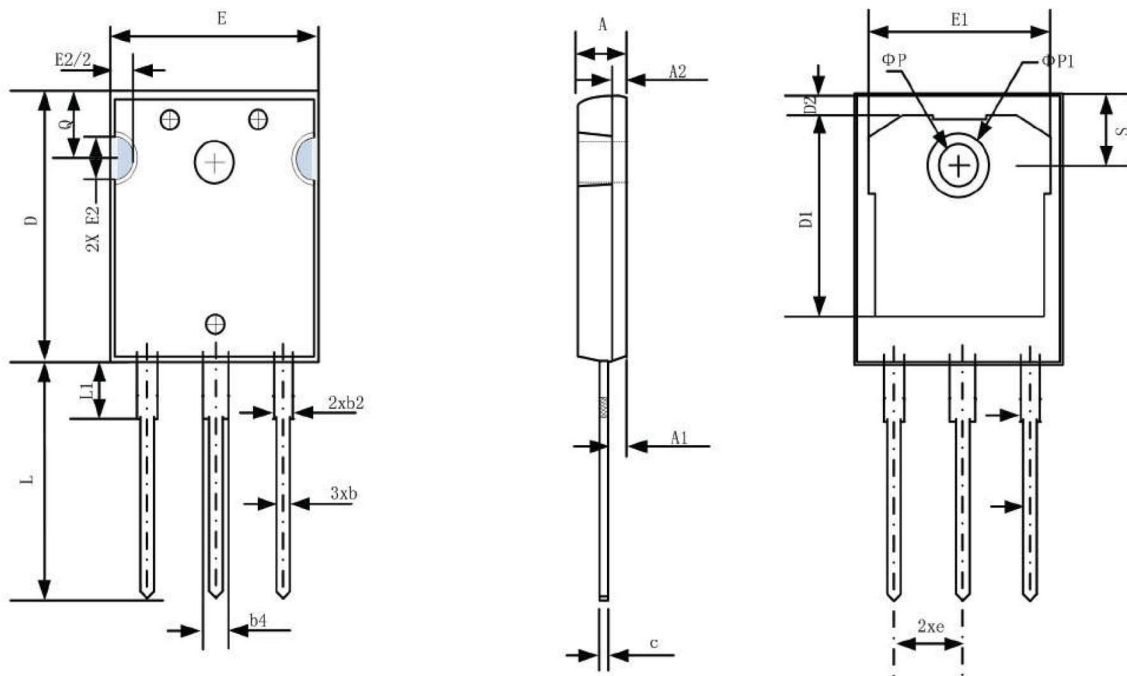


Switch Time Test Circuit



Unclamped Inductive Switching Test Circuit & Waveforms

O-247-3L (GM) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.83	5.21	0.190	0.205
A1	2.29	2.55	0.090	0.100
A2	1.50	2.49	0.059	0.098
b	1.12	1.33	0.044	0.052
b2	1.91	2.39	0.075	0.094
b4	2.87	3.22	0.113	0.127
c	0.55	0.69	0.022	0.027
D	20.80	21.10	0.819	0.831
D1	16.25	17.65	0.640	0.695
D2	0.51	1.35	0.020	0.053
E	15.75	16.13	0.620	0.635
E1	13.46	14.16	0.530	0.557
E2	4.32	5.49	0.170	0.216
e	5.44 BSC		0.214 BSC	
L	19.81	20.32	0.780	0.800
L1	4.10	4.40	0.161	0.173
ΦP	3.56	3.65	0.140	0.144
ΦP1	7.19 REF		0.283 REF	
Q	5.39	6.20	0.212	0.244
S	6.04	6.30	0.238	0.248

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