

N-Channel Super Junction Power MOSFET III

General Description

The series of devices use advanced super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- ◆ New technology for high voltage device
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ◆ ROHS compliant



Schematic diagram



TO-252

Application

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)

V_{DS}	650	V
$R_{DS(ON)TYP}$	600	mΩ
I_D	7	A

Package Marking And Ordering Information

Device	Device Package	Marking
MJ65T680K	TO-252	MJ65T680K

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	650	V
Gate-Source Voltage ($V_{DS}=0V$) AC ($f>1\text{ Hz}$)	V_{GS}	±30	V
Continuous Drain Current at $T_c=25^{\circ}C$	I_D (DC)	7	A
Continuous Drain Current at $T_c=100^{\circ}C$	I_D (DC)	4.5	A
Pulsed drain current ^(Note 1)	I_{DM} (pluse)	28	A
Maximum Power Dissipation ($T_c=25^{\circ}C$)	P_D	60	W
Derate above 25℃	P_D	0.48	W/℃
Single pulse avalanche energy ^(Note 2)	E_{AS}	101	mJ
Avalanche current ^(Note 1)	I_{AR}	1.5	A
Repetitive Avalanche energy, t_{AR} limited by T_{jmax} ^(Note 1)	E_{AR}	0.28	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, $V_{DS} \leq 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt , $V_{DS} \leq 480\text{ V}, I_{SD} < I_D$	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55...+150	℃

* limited by maximum junction temperature

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	2.08	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	°C/W

Table 3. Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	650	-	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	1	μA
Zero Gate Voltage Drain Current (Tc=125°C)	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	100	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	3	-	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V,I _D =3.5A	-	600	680	mΩ
Dynamic Characteristics						
Input Capacitance	C _{ies}	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	435	-	PF
Output Capacitance	C _{OSS}		-	28	-	PF
Reverse Transfer Capacitance	C _{rss}		-	3.3	-	PF
Total Gate Charge	Q _g	V _{DS} =480V,I _D =7A V _{GS} =10V	-	11	-	nC
Gate-Source Charge	Q _{gs}		-	3.5	-	nC
Gate-Drain Charge	Q _{gd}		-	5	-	nC
Switching times						
Turn-on Delay Time	t _{d(on)}	V _{DD} =380V,I _D =3.5A R _G =4.7Ω,V _{GS} =10V	-	8	-	nS
Turn-on Rise Time	t _r		-	7	-	nS
Turn-Off Delay Time	t _{d(off)}		-	58	75	nS
Turn-Off Fall Time	t _f		-	9	15	nS
Source- Drain Diode Characteristics						
Source-drain current (Body Diode)	I _{SD}	T _C =25°C	-	-	7	A
Pulsed Source-drain current (Body Diode)	I _{SDM}		-	-	28	A
Forward On Voltage	V _{SD}	T _J =25°C,I _{SD} =7A,V _{GS} =0V	-	0.9	1.2	V
Reverse Recovery Time	t _{rr}	T _J =25°C,I _F =3.5A di/dt=100A/μs	-	210	-	nS
Reverse Recovery Charge	Q _{rr}		-	0.85	-	uC
Peak reverse recovery Current	I _{rrm}		-	8	-	A

Notes

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
2. $T_J=25^{\circ}\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

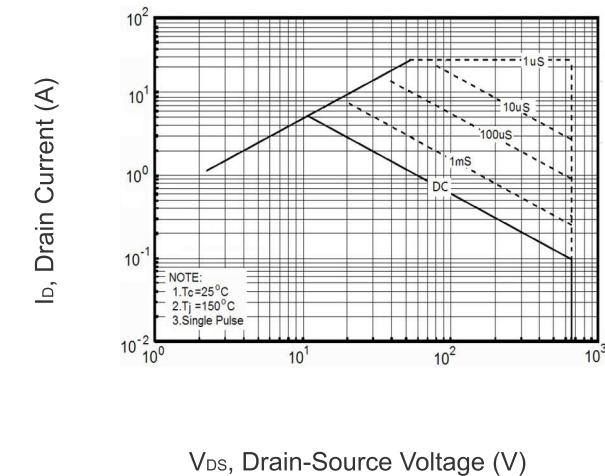


Figure 1 Safe operating area

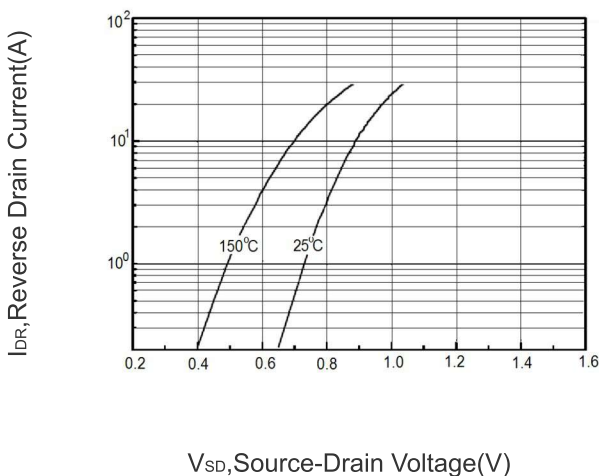


Figure 2 Source-Drain Diode Forward Voltage

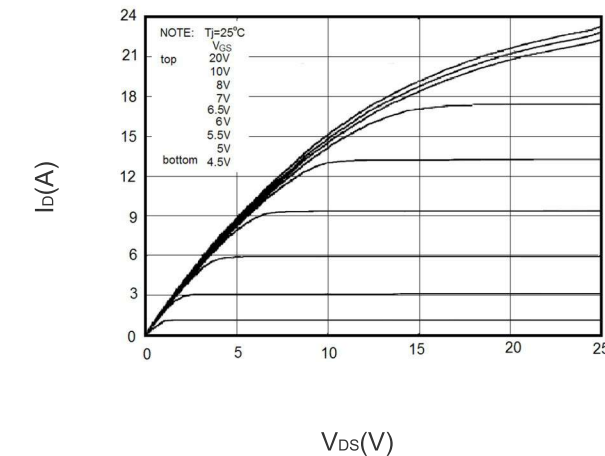


Figure 3 Output characteristics

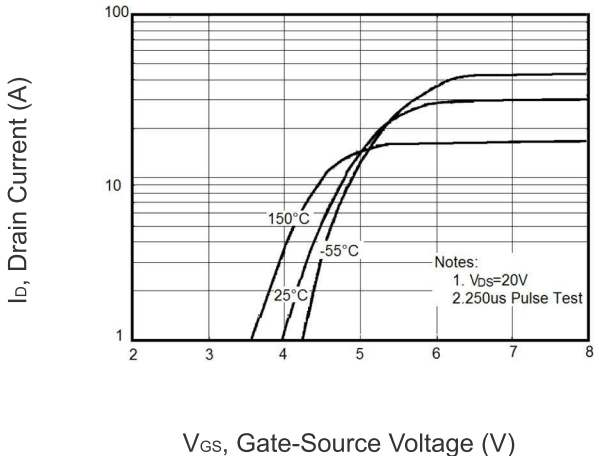


Figure 4 Transfer characteristics

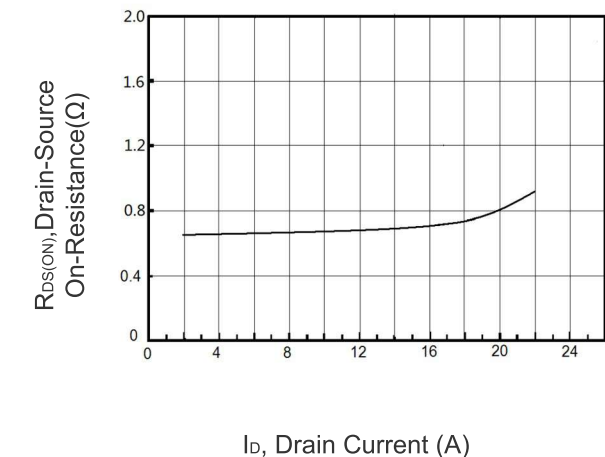


Figure 5 Static drain-source on resistance

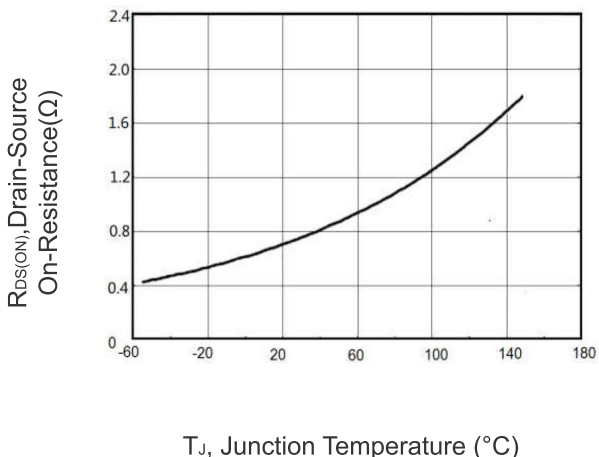


Figure 6 $R_{DS(ON)}$ vs Junction Temperature

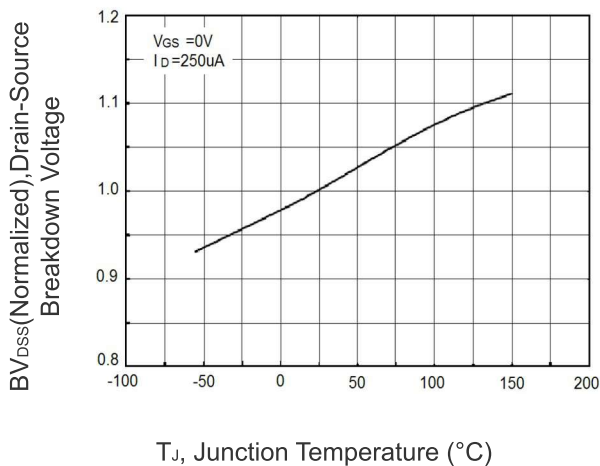


Figure 7 BV_{DSS} vs Junction Temperature

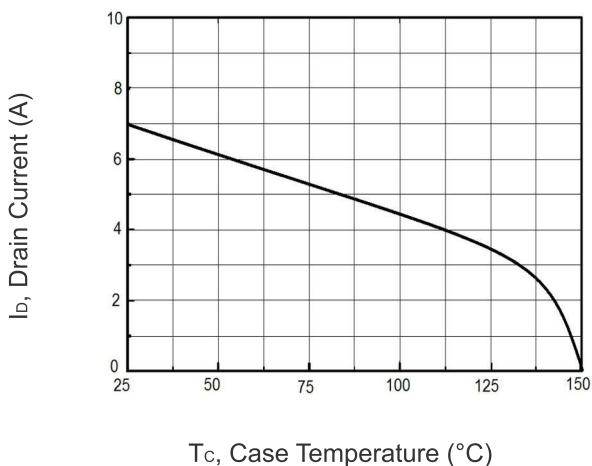


Figure 8 Maximum I_D vs Junction Temperature

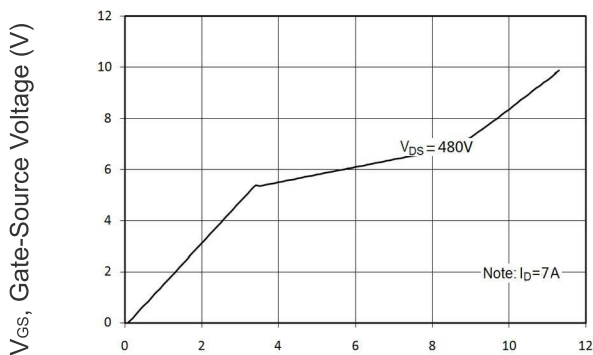


Figure 9 Gate charge waveforms

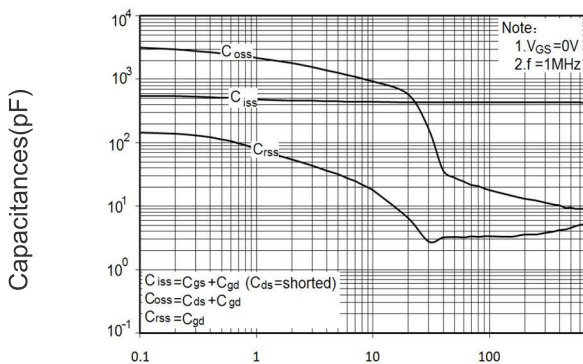


Figure 10 Capacitance

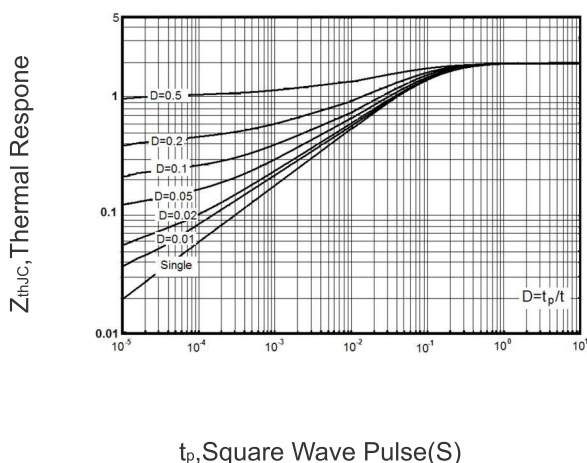
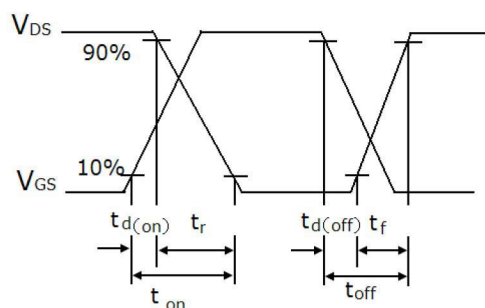


Figure 11 Transient Thermal Impedance

Test circuit



Gate charge test circuit & Waveform

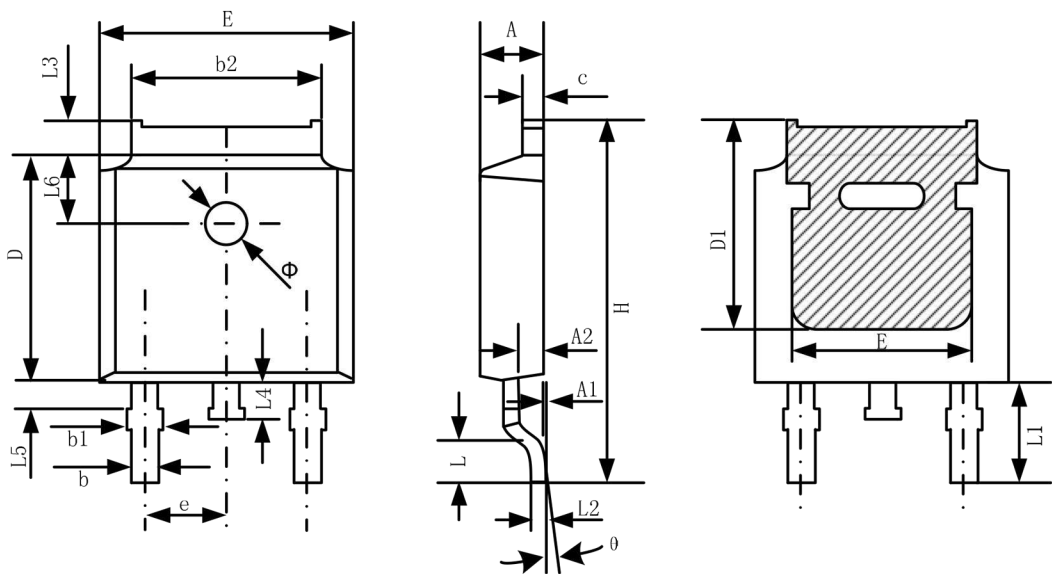


Switch Time Test Circuit



Unclamped Inductive Switching Test Circuit & Waveforms

TO-252-2 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.38	0.087	0.094
A1	0.00	0.10	0.000	0.004
A2	0.90	1.10	0.035	0.043
b	0.72	0.85	0.028	0.033
b1	0.72	0.90	0.028	0.035
b2	5.13	5.46	0.202	0.215
c	0.47	0.60	0.019	0.024
D	6.00	6.20	0.236	0.244
D1	5.25	--	0.207	--
E	6.50	6.70	0.256	0.264
E1	4.70	--	0.185	--
e	2.19	2.39	0.086	0.094
H	9.80	10.40	0.386	0.409
L	1.40	1.70	0.055	0.067
L1	2.90 REF		0.114 REF	
L2	0.508 BSC		0.020 BSC	
L3	0.90	1.25	0.035	0.049
L4	0.60	1.00	0.024	0.039
L5	0.15	0.75	0.006	0.030
L6	1.80 REF		0.071 REF	
Φ	1.20	1.40	0.047	0.055
θ	0°	8°	0°	8°

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