



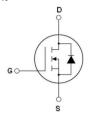
N-Channel Super Junction Power MOSFET III

General Description

The series of devices use advanced super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- ◆ New technology for high voltage device
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ROHS compliant





Schematic diagram

TO-252

Application

- ◆ Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)

VDS	650	V
Rds(on)TYP	1600	mΩ
ID	3	А

Package Marking And Ordering Information

Device	Device Package	Marking
MJ65T1K9K	TO-252	MJ65T1K9K

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vcs=0V)	VDS	650	V
Gate-Source Voltage (Vps=0V) ,AC (f>1 Hz)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	ID (DC)	3	А
Continuous Drain Current at Tc=100°C	ID (DC)	1.85	А
Pulsed drain current (Note 1)	IDM (pluse)	12	А
Maximum Power Dissipation (Tc=25°C)	Po	22	W
Derate above 25°C	Po	0.176	W/°C
Single pulse avalanche energy (Note 2)	Eas	16	mJ
Avalanche current (Note 1)	lar	0.4	А
Repetitive Avalanche energy, tar limited by T _{jmax} (Note 1)	Ear	0.1	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, V _{DS} ≤480 V	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V,I _{SD} <i<sub>D</i<sub>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	TJ,Tsтg	-55+150	°C





Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	5.68	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	62	°C/W

Table 3. Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250μA	650	-	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	1	μΑ
Zero Gate Voltage Drain Current (Tc=125°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	50	μA
Gate-Body Leakage Current	lgss	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	3	-	4	V
Drain-Source On-State Resistance	RDS(ON)	Vgs=10V,Ip=1.5A	-	1600	1900	mΩ
Dynamic Characteristics	,					
Input Capacitance	Cies		-	130	-	PF
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	10	-	PF
Reverse Transfer Capacitance	Crss	-	-	0.6	-	PF
Total Gate Charge	Qg		-	9	-	nC
Gate-Source Charge	Qgs	V _{DS} =480V,I _D =3A V _{GS} =10V	-	2.5	-	nC
Gate-Drain Charge	Qgd	-	-	4	-	nC
Switching times						
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	tr	Vpp=380V,Ip=1.5A	-	9	-	nS
Turn-Off Delay Time	t _{d(off)}	R _G =4.7Ω,V _{GS} =10V	-	56	-	nS
Turn-Off Fall Time	tf	-	-	11	-	nS
Source- Drain Diode Characteristics						
Source-drain current (Body Diode)	Isp		-	-	4	А
Pulsed Source-drain current (Body Diode)	Isdm	Tc=25°C	-	-	16	Α
Forward On Voltage	Vsb	T _j =25°C,I _{SD} =3A,V _{GS} =0V	-	0.9	1.2	V
Reverse Recovery Time	trr		-	190	-	nS
Reverse Recovery Charge	Qrr	T _j =25°C,I _F =1.5A di/dt=100A/µs	-	0.5	-	uC
Peak reverse recovery current	Irrm	-	-	5	-	А

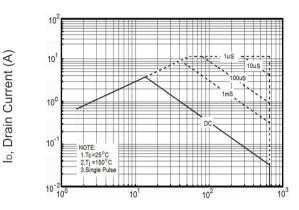


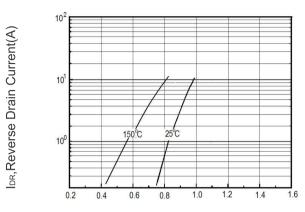


Notes

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- $2.T_j = 25^{\circ}C, V_{DD} = 50V, V_{G} = 10V, R_{G} = 25\Omega$

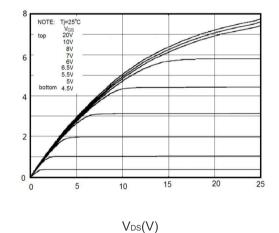
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)





V_{DS}, Drain-Source Voltage (V)
Figure 1 Safe operating area

Vsb,Source-Drain Voltage(V)
Figure 2 Source-Drain Diode
Forward Voltage



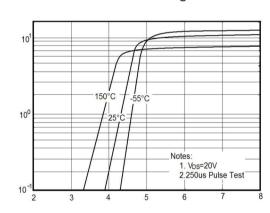
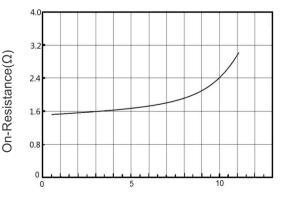
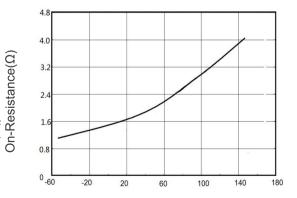


Figure 3 Output characteristics

V_{GS}, Gate-Source Voltage (V)
Figure 4 Transfer characteristics



RDS(ON), Drain-Source



I_D, Drain Current (A)

Figure 5 Static drain-source
on resistance

TJ, Junction Temperature (°C)
Figure 6 RDS(ON) vs Junction
Temperature

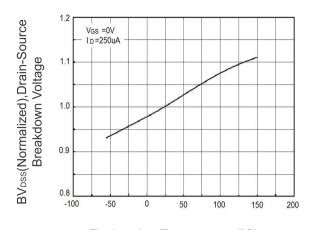
lb, Drain Current (A)

ADS(ON), Drain-Source

lo, Drain Current (A)

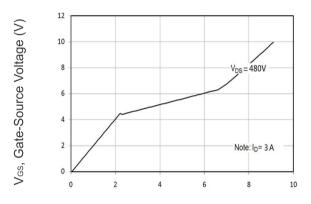
Capacitances(pF)





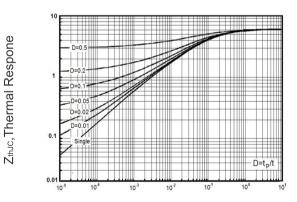
T_J, Junction Temperature (°C)

Figure 7 BVDSS vs Junction Temperature



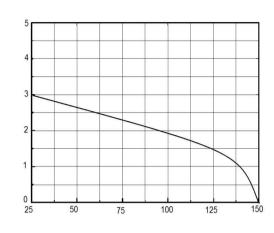
Q_G,Total Gate Charge(nC)

Figure 9 Gate charge waveforms



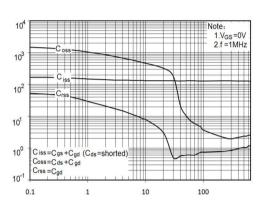
tp,Square Wave Pulse(S)

Figure 11 Transient Thermal Impedance



Tc, Case Temperature (°C)

Figure 8 Maximum ID vs Junction Temperature



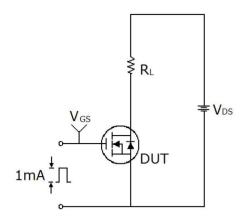
V_{DS}, Drain-Source Voltage (V)

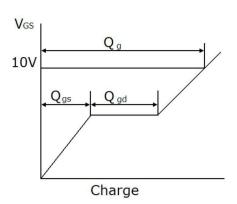
Figure 10 Capacitance



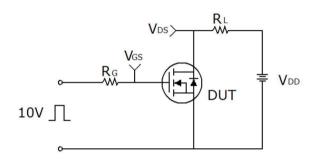


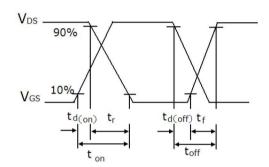
Test circuit



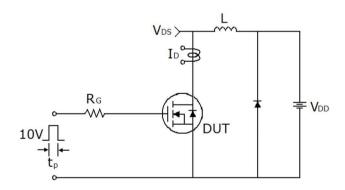


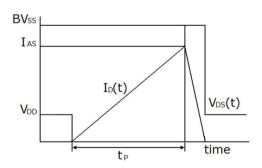
Gate charge test circuit & Waveform





Switch Time Test Circuit



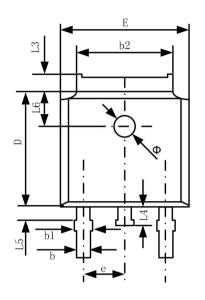


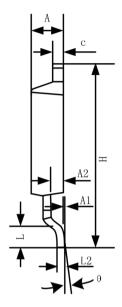
Unclamped Inductive Switching Test Circuit & Waveforms

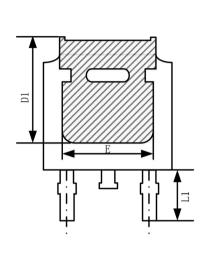




TO-252-2 Package Information

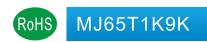






Comple of	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.20	2.38	0.087	0.094	
A1	0.00	0.10	0.000	0.004	
A2	0.90	1.10	0.035	0.043	
b	0.72	0.85	0.028	0.033	
b1	0.72	0.90	0.028	0.035	
b2	5.13	5.46	0.202	0.215	
С	0.47	0.60	0.019	0.024	
D	6.00	6.20	0.236	0.244	
D1	5.25		0.207		
E	6.50	6.70	0.256	0.264	
E1	4.70		0.185		
e	2.19	2.39	0.086	0.094	
Н	9.80	10.40	0.386	0.409	
L	1.40	1.70	0.055	0.067	
L1	2.90	REF	0.114	REF	
L2	0.508 BSC		0.020 BSC		
L3	0.90	1.25	0.035	0.049	
L4	0.60	1.00	0.024	0.039	
L5	0.15	0.75	0.006	0.030	
L6	1.80 REF		0.071 REF		
Ф	1.20	1.40	0.047	0.055	
θ	0°	8°	0°	8°	





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