



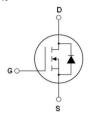
N-Channel Super Junction Power MOSFET II

General Description

The series of devices use advanced super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- ◆ New technology for high voltage device
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ◆ ROHS compliant





Schematic diagram

TO-252

Application

- ◆ Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)

VDS	650	V
R _{DS(ON)}	900	mΩ
ID	5	А

Package Marking And Ordering Information

Device	Device Package	Marking	
MJ65R900K	TO-252	MJ65R900K	

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (Vcs=0V)	VDS	650	V
Gate-Source Voltage (Vps=0V)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	ID (DC)	5	А
Continuous Drain Current at Tc=100°C	ID (DC)	3	А
Pulsed drain current (Note 1)	IDM (pluse)	15	А
Drain Source voltage slope, V _{DS} = 480 V, I _D =5 A, T _j = 125 °C	dv/dt	48	V/ns
Maximum Power Dissipation (Tc=25°C)	Po	49	W
Derate above 25°C	Po	0.39	W/°C
Single pulse avalanche energy (Note 2)	Eas	135	mJ
Avalanche current (Note 1)	lar	2.5	Α

Parameter	Symbol	Value	Unit
Repetitive Avalanche energy , tar limited by Tjmax (Note 1)	Ear	0.4	mJ
Operating Junction and Storage Temperature Range	T _J ,Тsтg	-55+150	°C

^{*} limited by maximum junction temperature



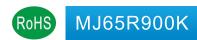


Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	2.55	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	75	°C/W

Table 3. Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250µA	650	_	-	V
Zero Gate Voltage Drain Current (Tc=25°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	1	μA
Zero Gate Voltage Drain Current (Tc=125°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	50	μΑ
Gate-Body Leakage Current	lgss	Vgs=±30V,Vps=0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2.5	3	3.5	V
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =10V,I _D =2.5A	-	780	900	mΩ
Dynamic Characteristics						
Forward Transconductance	grs	V _{DS} =20V,I _D =3A	-	4.8	_	S
Input Capacitance	Cies		-	460	_	PF
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	45	-	PF
Reverse Transfer Capacitance	Crss		-	3.5	-	PF
Total Gate Charge	Qg		-	10	20	nC
Gate-Source Charge	Qgs	V _{DS} =480V,I _D =5A V _{GS} =10V	-	1.6	-	nC
Gate-Drain Charge	Qgd	-	-	4	-	nC
Intrinsic gate resistance	Rg	f=1 MHz open drain	-	2.5	-	Ω
Switching times						
Turn-on Delay Time	t _{d(on)}		-	6	-	nS
Turn-on Rise Time	tr	VDD=380V,ID=3A	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	R _G =18Ω,V _{GS} =10V	-	50	60	nS
Turn-Off Fall Time	tr		-	9	15	nS
Source- Drain Diode Characteristics						
Source-drain current (Body Diode)	Isp		-	_	5	А
Pulsed Source-drain current (Body Diode)	Isdм	− Tc=25°C	-	-	15	А
Forward On Voltage	Vsp	Tj=25°C,IsD=5A,Vgs=0V	-	1	1.3	V
Reverse Recovery Time	trr		-	250	_	nS
Reverse Recovery Charge	Qrr	Tj=25°C,I⊧=5A di/dt=100A/µs	-	2.2	_	uC
Peak reverse recovery current	Irrm	-	_	15	_	А

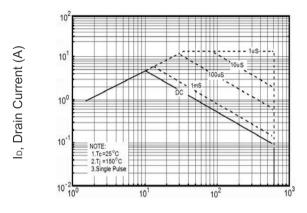




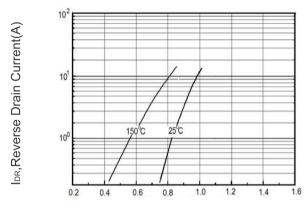
Notes

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- $2.T_j = 25^{\circ}C, V_{DD} = 50V, V_{G} = 10V, R_{G} = 25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)







V_{SD},Source-Drain Voltage(V)
Figure 2 Source-Drain Diode
Forward Voltage

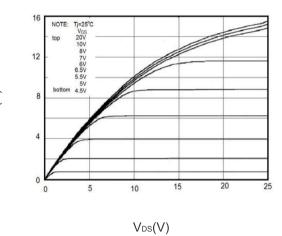
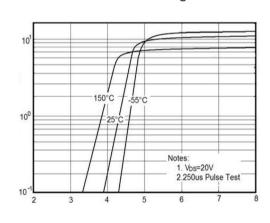
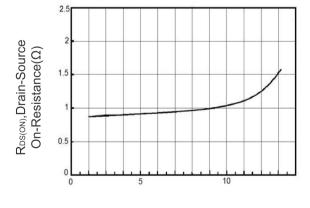


Figure 3 Output characteristics

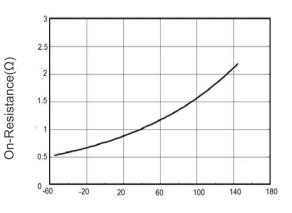


V_{GS}, Gate-Source Voltage (V)
Figure 4 Transfer characteristics



I_D, Drain Current (A)

Figure 5 Static drain-source
on resistance



T_J, Junction Temperature (°C)
Figure 6 R_{DS(ON)} vs Junction
Temperature

lb, Drain Current (A)

ADS(ON), Drain-Source



Vgs =0V Ip=250uA

-50

BVpss(Normalized), Drain-Source

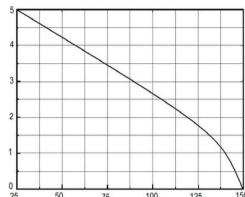
Ves, Gate-Source Voltage (V)

Breakdown Voltage

1.0

0.8 -100 lb, Drain Current (A)

Capacitances(pF)



Tc, Case Temperature (°C) Figure 8 Maximum ID vs Junction Temperature

1.V_{GS}=0V 2.f =1MHz

100 150 75

.8V_{DS} max

50

T_J, Junction Temperature (°C)

Figure 7 BVDSS vs Junction Temperature

100

150

200

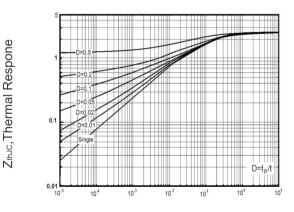
103 10 101 C iss=Cgs+Cgd (Cds=shorted) Coss=Cds+Cgd Crss = Cgd 10⁰

0.1

V_{DS}, Drain-Source Voltage (V) Figure 10 Capacitance

10

Q_G,Total Gate Charge(nC) Figure 9 Gate charge waveforms

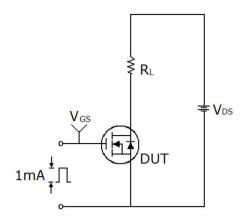


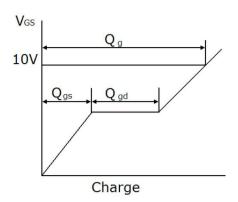
tp,Square Wave Pulse(S)

Figure 11 Transient Thermal Impedance

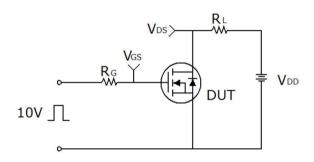


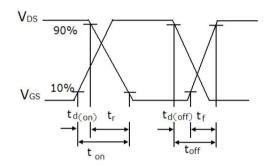
Test circuit



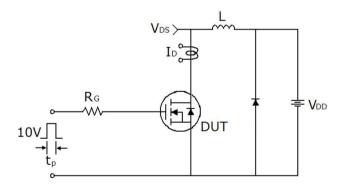


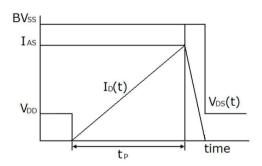
Gate charge test circuit & Waveform





Switch Time Test Circuit



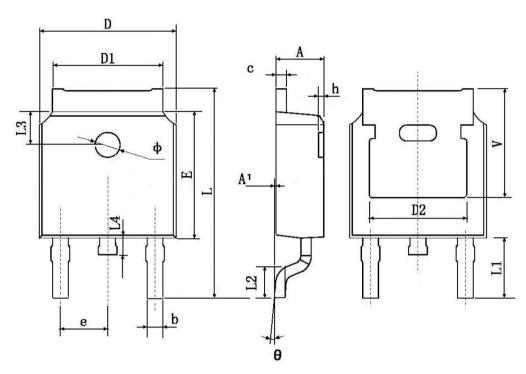


Unclamped Inductive Switching Test Circuit & Waveforms



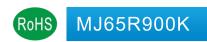


TO-252 Package Information



Comple al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	4.830 TYP.		TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP.	0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211	TYP.	





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