



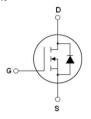
N-Channel Super Junction Power MOSFET II

General Description

The series of devices use advanced super junction technology and design to provide excellent R_{DS(ON)} with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- ◆ New technology for high voltage device
- ◆ Low on-resistance and low conduction losses
- ◆ Small package
- ◆ Ultra Low Gate Charge cause lower driving requirements
- ◆ 100% Avalanche Tested
- ◆ ROHS compliant





Schematic diagram

TO-252

Application

- ◆ Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- ◆ Uninterruptible Power Supply (UPS)

VDS@Tjmax	710	V
Rds(on)typ	2.2	Ω
I _D	2	А

Package Marking And Ordering Information

Device	Device Package	Marking	
MJ65R2K2K	TO-252	MJ65R2K2K	

Table 1. Absolute Maximum Ratings (Tc=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	VDS	650	V
Gate-Source Voltage (V _{DS} =0V)	Vgs	±30	V
Continuous Drain Current at Tc=25°C	ID (DC)	2	А
Continuous Drain Current at Tc=100°C	ID (DC)	1.3	А
Pulsed drain current (Note 1)	IDM (pluse)	6	А
Maximum Power Dissipation (Tc=25°C)	Po	23	W
Derate above 25°C	PD	0.184	W/°C
Single pulse avalanche energy (Note 2)	Eas	45	mJ
Avalanche current (Note 1)	lar	1	А
Repetitive Avalanche energy , tar limited by T _{jmax} (Note 1)	Ear	0.06	mJ

Parameter	Symbol	Value	Unit
Drain Source voltage slope, V _{DS} ≤480 V	dv/dt	50	V/ns
Reverse diode dv/dt, Vps ≤480 V,lsp <lp< td=""><td>dv/dt</td><td>15</td><td>V/ns</td></lp<>	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	TJ,Tsтg	-55+150	°C





Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	RthJC	5.4	°C/W
Thermal Resistance, Junction-to-Ambient (Maximum)	RthJA	75	°C/W

Table 3. Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
On/off states							
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250μA	650	-	_	V	
Zero Gate Voltage Drain Current (Tc=25°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	1	μΑ	
Zero Gate Voltage Drain Current (Tc=125°C)	loss	V _{DS} =650V,V _{GS} =0V	-	-	10	μA	
Gate-Body Leakage Current	Igss	V _{GS} =±30V,V _{DS} =0V	-	-	±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	2.5	3	3.5	V	
Drain-Source On-State Resistance	RDS(ON)	Vgs=10V,ID=1A	-	2200	2500	mΩ	
Dynamic Characteristics							
Forward Transconductance	g FS	V _{DS} =20V,I _D =1A	-	2	-	S	
Input Capacitance	Cies		-	190	-	PF	
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V F=1.0MHz	-	13	-	PF	
Reverse Transfer Capacitance	Crss		-	1.1	-	PF	
Total Gate Charge	Qg		-	3.2	10	nC	
Gate-Source Charge	Qgs	Vps=480V,lp=2A Vgs=10V	-	0.6	-	nC	
Gate-Drain Charge	Qgd	-	-	1.2	-	nC	
Intrinsic gate resistance	Rg	f=1 MHz open drain	-	9	-	Ω	
Switching times	'						
Turn-on Delay Time	t _{d(on)}		-	6	_	nS	
Turn-on Rise Time	tr	V _{DD} =380V,I _D =1A	-	3	-	nS	
Turn-Off Delay Time	t _{d(off)}	R _G =50Ω,V _{GS} =10V	-	65	-	nS	
Turn-Off Fall Time	tf	-	-	11	_	nS	
Source- Drain Diode Characteristics					I		
Source-drain current (Body Diode)	Isp		-	_	2	А	
Pulsed Source-drain current (Body Diode)	Isdm	- Tc=25°C	-	-	6	А	
Forward On Voltage	Vsp	T _j =25°C,I _{SD} =2A,V _{GS} =0V	-	1	1.3	V	
Reverse Recovery Time	trr		-	140	-	nS	
Reverse Recovery Charge	Qrr	Tj=25°C,I⊧=2A di/dt=100A/µs	-	0.65	_	uC	
Peak reverse recovery current	Irrm		_	9	_	А	

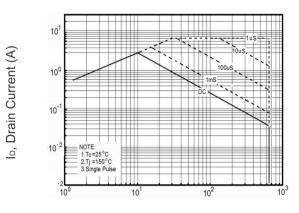


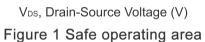


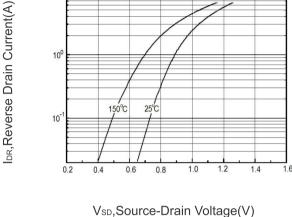
Notes

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- $2.T_j = 25^{\circ}C, V_{DD} = 50V, V_{G} = 10V, R_{G} = 25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)







10¹

lp, Drain Current (A)

RDS(ON), Drain-Source

On-Resistance(Ω)

Figure 2 Source-Drain Diode Forward Voltage

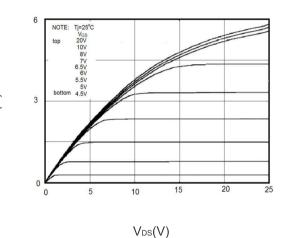
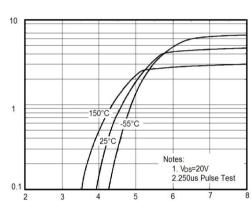
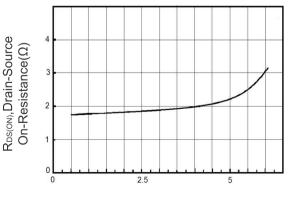


Figure 3 Output characteristics

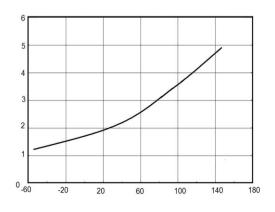


V_{GS}, Gate-Source Voltage (V)
Figure 4 Transfer characteristics



I_D, Drain Current (A)

Figure 5 Static drain-source
on resistance

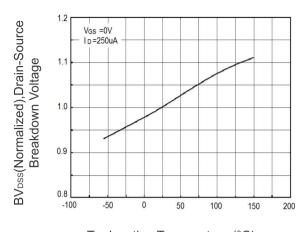


TJ, Junction Temperature (°C)
Figure 6 RDS(ON) vs Junction
Temperature

lo, Drain Current (A)

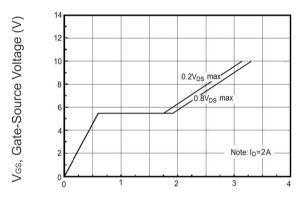
Capacitances(pF)





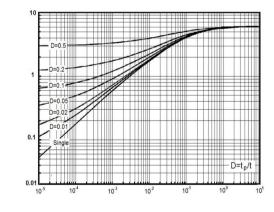
T_J, Junction Temperature (°C)

Figure 7 BVDSS vs Junction Temperature



Q_G,Total Gate Charge(nC)

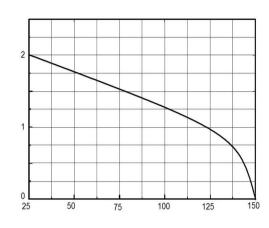
Figure 9 Gate charge waveforms



Zthuc, Thermal Respone

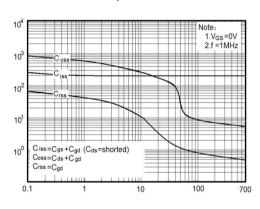
tp,Square Wave Pulse(S)

Figure 11 Transient Thermal Impedance



Tc, Case Temperature (°C)

Figure 8 Maximum ID vs Junction Temperature

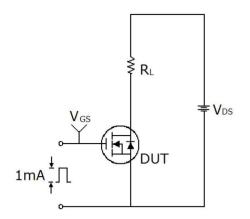


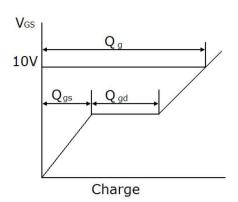
V_{DS}, Drain-Source Voltage (V)

Figure 10 Capacitance

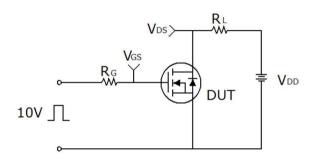


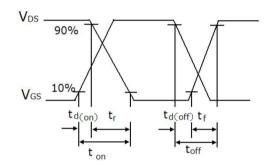
Test circuit



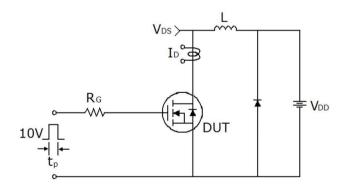


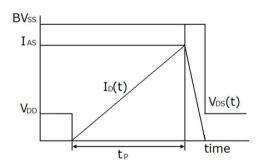
Gate charge test circuit & Waveform





Switch Time Test Circuit



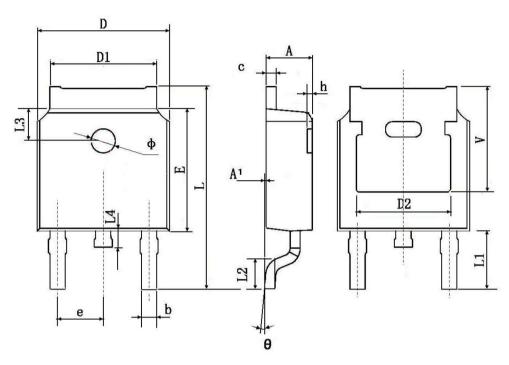


Unclamped Inductive Switching Test Circuit & Waveforms





TO-252 Package Information



October 1	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	0.190 TYP.		TYP.	
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP.		TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP.	0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	5.350 TYP. 0.211 TYP.		TYP.	





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