



# 650V, 60A, Trench FS II Fast IGBT

### General Description:

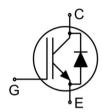
Using MJ's proprietary trench design and advanced FS (Field Stop) second generation technology, the 650V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

#### **Features**

- ◆ Trench FSII Technology offering
- ♦ Very low Vce (sat)
- ◆ High speed switching
- ◆ Positive temperature coefficient in V<sub>CE</sub> (sat)
- ◆ Very tight parameter distribution
- High ruggedness, temperature stable behavior

### **Application**

- Air Condition
- Inverters
- ♠ Motor drives







TO-247

### Package Marking and Ordering Information

Device	Device Package	Device Marking		
MJ60TD65BT	TO-247	MJ60TD65BT		

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Value	Units	
Collector-Emitter Voltage	Vces	650	V	
Gate- Emitter Voltage	VGES	±30	V	
Collector Current	Ic	120	А	
Collector Current @Tc = 100 °C	Ic	60	А	
Pulsed Collector Current, tp limited by T <sub>jmax</sub>	Cplus	180	А	
turn off safe operating area, VcE=650V, Tj=150°C	-	180	А	
Diode Continuous Forward Current @Tc = 100 °C	lf	60	А	
Diode Maximum Forward Current	lғм	180	А	
Power Dissipation @ Tc = 25°C	Po	316	W	
Power Dissipation @Tc = 100 °C	Po	158	W	
Operating Junction and Storage Temperature Range	TJ,Tstg	-55 to +175	°C	
Maximum Temperature for Soldering	T∟	260	°C	
Short circuit withstand time V <sub>GE</sub> =15.0V, V <sub>CC</sub> ≤400V, Allowed number of short circuits<1000Time between short circuits:≥1.0s,Tj≤150°C	tsc	5	us	





### Thermal Characteristic

Parameter	Symbol	Value	Units
Thermal Resistance, Junction to case for IGBT	Rejc	0.47	°C/W
Thermal Resistance, Junction to case for Diode	Rejc	1.72	°C/W
Thermal Resistance, Junction to Ambient	Reja	40	°C/W

## Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Cymbol			Value			115:4-
Parameter	Symbol Test Cond		naitions	Min	Тур	Max	Units
Static Characteristics							
Collector-Emitter Breakdown Voltage	V(BR)CES	V <sub>GE</sub> =0V,	Ice=1mA	650	-	-	V
Collector-Emitter Leakage Current	Ices	V <sub>GE</sub> =0V,\	/ce=650V	-	-	5	μA
Gate to Emitter Forward Leakage	Iges(F)	V <sub>GE</sub> =+30	V,VcE=0V	-	-	200	nA
Gate to Source Reverse Leakage	IGES(R)	V <sub>GE</sub> =-30	V,VcE=0V	-	-	200	nA
Collector-Emitter Saturation Voltage	Mary n	Ic=60A	Tj=25°C	-	1.7	1.9	V
Collector-Efficiel Saturation voltage	VCE(sat)	V <sub>GE</sub> =15V	Tj=150°C	-	1.9	-	V
Gate Threshold Voltage	V <sub>GE(th)</sub>	Ic=1mA	V <sub>CE</sub> =V <sub>GE</sub>	4.0	5.0	6.0	V
Dynamic Characteristics							
Input Capacitance	Cies	Vce=25V,Vge=0V, f=1MHz		-	7018	-	pF
Output Capacitance	Coss			-	199	-	pF
Reverse Transfer Capacitance	Crss			-	138	-	pF
Total Gate Charge	Qg	Vcc=480V, Ic=60A VcE=15V VcE=15V,Vcc≤400V, tsc≤5us,Tj≤150°C		-	262	-	nC
Gate to Emitter Charge	Qge			-	60	-	nC
Gate to Collector Charge	Qgc			-	113	-	nC
Short circuit collector current Max.1000 short circuits Time between short circuits: ≥1.0s	Ic(sc)			-	360	-	А
Switching Characteristics							
Turn-on Delay Time	t <sub>d</sub> (ON)			-	19	_	ns
Rise Time	tr			-	17	-	ns
Turn-Off Delay Time	t <sub>d(OFF)</sub>	VcE=400V,Ic=60A VGE=0/15V, Rg=5Ω Inductive Load		-	170	-	ns
Fall Time	tr			-	18	-	ns
Turn-On Switching Loss	Eon			-	2.2	-	mJ
Turn-Off Switching Loss	Eoff			-	0.9	-	mJ
Turn-Off Switching Loss	Ets			_	3.1	-	mJ

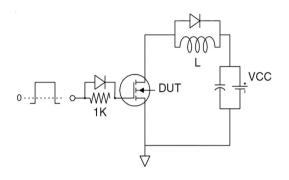




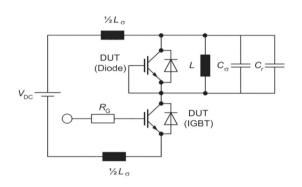
# Electrical Characteristics of the Diode (Tc= 25°C unless otherwise specified):

Doromotor	Symbol	Test Conditions	Rating			Units
Parameter	Symbol	rest Conditions	Min	Тур	Max	Units
Diode Forward Voltage	V <sub>FM</sub>	I==60A	_	1.7	2.0	V
Reverse Recovery Time	Trr		_	186	=	ns
Diode Peak Reverse Recovery Current	IRRM	I <sub>F</sub> =60A,di/dt=200A/uS	-	3.8	-	А
Reverse Transfer Capacitance	Qrr		-	0.3	-	uC
Pulse width ttp≤380μs,δ≤2%						

#### **Test Circuit**

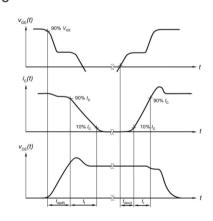


Gate Charge Test Circuit

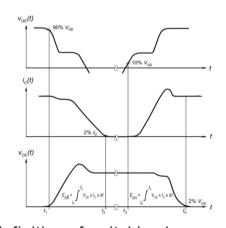


Switch Time Test Circuit

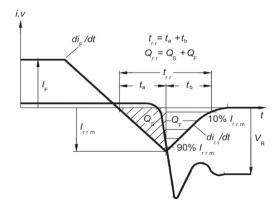
## Switching characteristics



definition of switching times

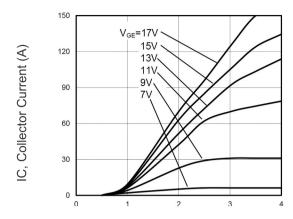


definition of switching losses

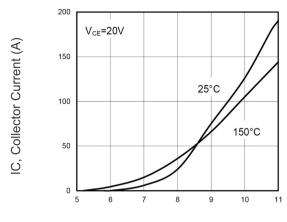


Definition of diode switching characteristics

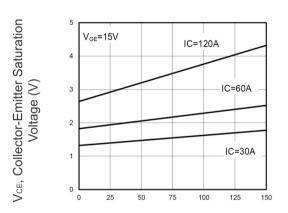
### Typical Electrical and Thermal Characteristics



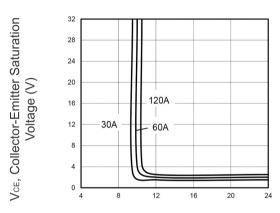
Vce, Collector-Emitter Voltage (V)
Figure 1 Output Characteristics



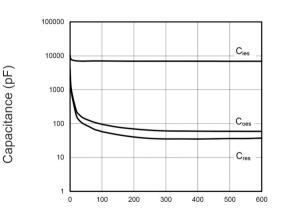
V<sub>GE</sub>, Gate-Emitter Voltage (V)
Figure 2 Transfer Characteristics



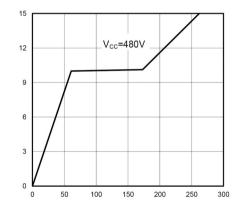
TJ, Junction Temperature (°C)
Figure 3 V<sub>CEsat</sub> vs. Case Temperature



VGE, Gate-Emitter Voltage (V)
Figure 4 Saturation Voltage vs. VGE



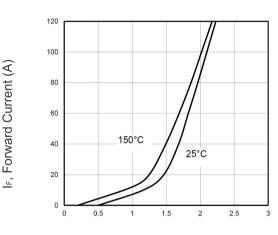
Vce, Collector-Emitter Voltage (V)
Figure 5 Capacitance Characteristics



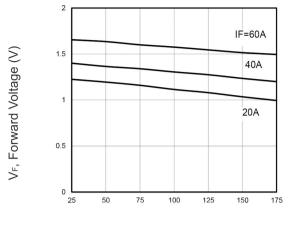
QG, Total Gate Charge (nC)
Figure 6 Gate charge waveform

VGE, Gate-Emitter Voltage (V)

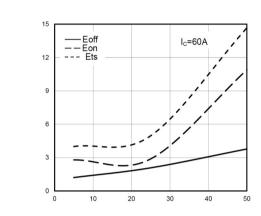
### Typical Electrical and Thermal Characteristics



V<sub>F</sub>, Forward Voltage (V)
Figure 7 Forward Characteristics

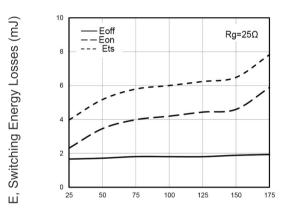


TJ, Junction Temperature (°C)
Figure 8 VF vs. Temperature



E, Switching Energy Losses (mJ)

 $R_{\text{G}}$ , Gate Resistor ( $\Omega$ )
Figure 9 Typical Switching Times as a Function of Gate Resistor



TJ, Junction Temperature (°C)
Figure 10 Typical Switching Times as a
Function of Junction Temperature

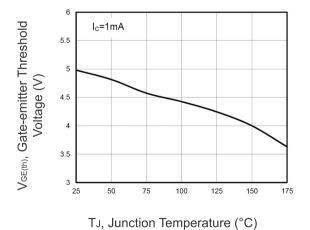


Figure 11 Gate-emitter Threshold Voltage as a Function of Junction Temperature

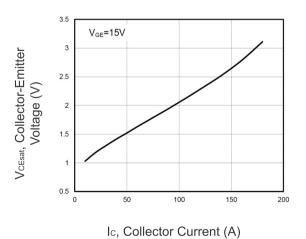
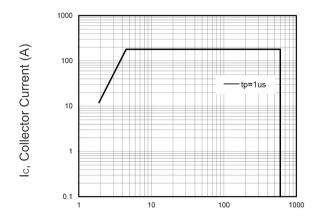


Figure 12 Typical Collector-emitter Saturation Voltage as a function of Collector Current



## Typical Electrical and Thermal Characteristics



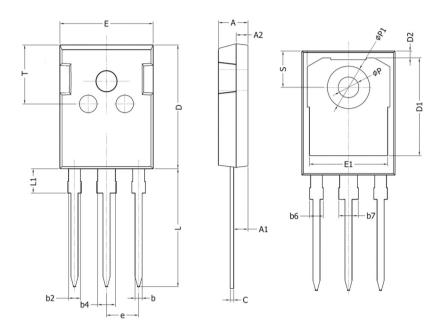
Vce, Collector-Emitter Voltage (V)

Figure 13 Forward Bias Safe Operating Area





# TO-247-3L Package Information



Comple of	Dimensions I	Dimensions In Millimeters		Inches
Symbol	Min.	Max.	Min.	Max.
Α	4.90	5.10	0.193	0.201
A1	2.31	2.51	0.091	0.099
A2	1.9	2.1	0.075	0.083
b	1.16	1.26	0.046	0.050
b2	1.96	2.06	0.077	0.081
b4	2.96	3.06	0.117	0.120
b6	-	2.25	-	0.089
b7	-	3.25	-	0.128
С	0.59	0.66	0.023	0.026
D	20.90	21.10	0.823	0.831
D1	16.25	16.85	0.640	0.663
D2	1.05	1.35	0.041	0.053
E	15.70	15.90	0.618	0.626
E1	13.10	13.50	0.516	0.531
е	5.436	BSC	C	
L	19.80	20.10	0.780	0.791
L1	-	4.30	-	0.169
Р	3.40	3.60	0.134	0.142
P1	7.00	7.40	0.276	0.291
S	6.05	6.25	0.238	0.246
Т	9.80	10.20	0.386	0.402





#### Attention:

Any and all MJ power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MJ power representative nearest you before using any MJ power products described or contained herein in such applications.

MJ power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MJ power products described or contained herein.

Specifications of any and all MJ power products described or contained herein stipulate the erformance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

MJ power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all MJ power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or therwise, without the prior written permission of MJ power Semiconductor CO.,LTD.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MJ power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MJ power product that you intend to use.

This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.