

MJ N-Channel Enhancement Mode Power MOSFET

Description

The MJ60ND45G uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

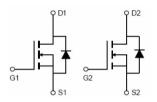
Application

Load switch

Power switching application

General Features

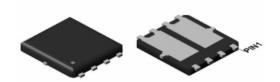
- VDs=60V,ID=45A
 RDS(ON)<15mΩ @ VGs=10V (Typ:11mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Low gate to drain charge to reduce switching losses



Schematic Diagram



Pin Assignment



DFN 5X6

100% UIS TESTED! 100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ60ND45G	MJ60ND45G	DFN 5X6 -8L	Ø330mm	12mm	5000

Absolute Maximum Ratings (Tc =25 °Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	45	А
Drain Current-Continuous(Tc =100°C)	ID(100℃)	32	А
Pulsed Drain Current	Ідм	140	А
Single pulse avalanche energy (Note 5)	Eas	260	mJ
Maximum Power Dissipation	PD	60	W
Operating Junction and Storage Temperature Range	Тј ,Тѕтс	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	2.08	°C/W	
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Electrical Characteristics (Tc =25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BVdss	Vgs=0V Id=250µA	60	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=60V,VGS=0V	-	-	1	μA
Gate-Body Leakage Current	lgss	VDS=±20V,VDS=0V	-	-	±100	nA
On Characteristics (Note 3)		1				1
Gate Threshold Voltage	VGS(th)	Vos=Vgs ,Io=250µA	2	3	4	V
Drain-Source On-State Resistance	Rds(ON)	V _{GS} =10V, I _D =20A	-	11	15	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =9A	25	-	-	S
Dynamic Characteristics ^(Note 4)	1					
Input Capacitance	Clss		-	2622.3	-	PF
Output Capacitance	Coss	V _{DS} =30V,V _{GS} =0V, F=1.0MHz	-	175.3	-	PF
Reverse Transfer Capacitance	Crss	-	-	126.5	-	PF
Switching Characteristics (Note 4)		1				1
Turn-on Delay Time	td(on)		-	8.5	-	nS
Turn-on Rise Time	tr	VDD=30V,RL=1.5Ω	-	6	-	nS
Turn-Off Delay Time	td(off)	Vgs=10V,Rgen=3Ω	-	30	-	nS
Turn-Off Fall Time	tr	-	-	5	-	nS
Total Gate Charge	Qg		-	52	_	nC
Gate-Source Charge	Qgs	− V⊳s=30V,I⊳=20A, V∈s=10V	-	13.9	-	nC
Gate-Drain Charge	Qgd	-	-	15.2	-	nC
Drain-Source Diode Characteristics		1				
Diode Forward Voltage (Note 3)	Vsd	V _{GS} =0V,Is=20A	_	-	1.2	V
Diode Forward Current (Note 2)	ls		-	-	45	А
Reverse Recovery Time	trr	Tj=25°C, IF=20A di/dt=100A/µs ^(Note 3)	_	30	-	nS
Reverse Recovery Charge	Qrr			44		nC

Notes:

1 Repetitive Rating: Pulse width limited by maximum junction temperature.

② Surface Mounted on FR4 Board, t≤10sec.

③ Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%.

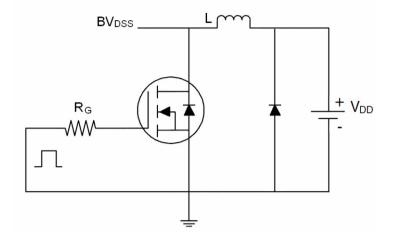
④ Guaranteed by design, not subject to production

(5) EAS condition:Tj=25°C ,V_DD=30V,V_G=10V,L=0.5mH,Rg=25\Omega

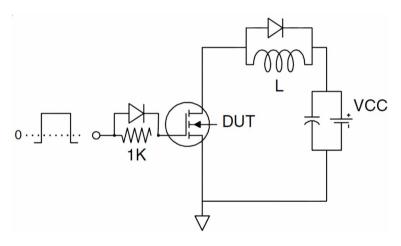




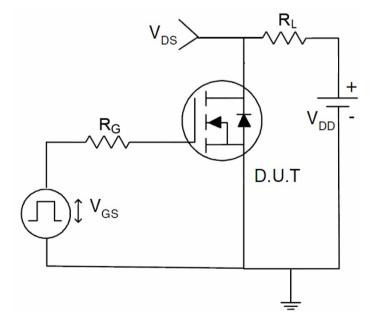
Test circuit



EAs test Circuit



Gate charge test Circuit



Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

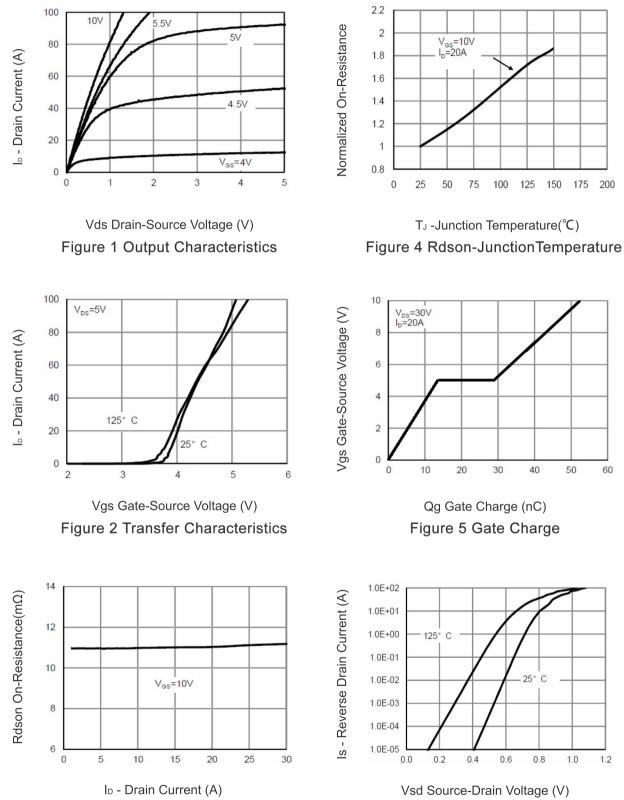
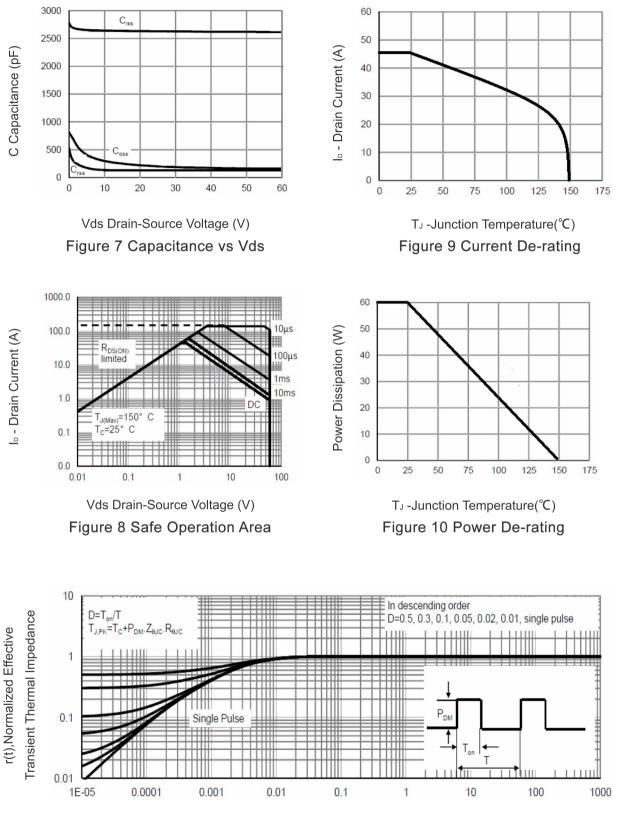


Figure 6 Source- Drain Diode Forward

Figure 3 Rdson- Drain Current







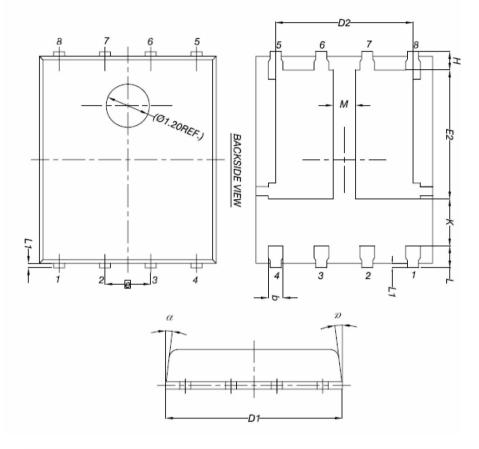
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

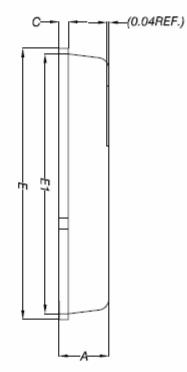




DFN5X6-8L Package Information



	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
b	0.33	0.41	0.51		
С	0.20	0.25	0.30		
D1	4.80	4.90	5.00		
D2	3.61	3.81	3.96		
Е	5.90	6.00	6.10		
E1	5.70	5.75	5.80		
E2	3.38	3.58	3.78		
е	1.27 BSC				
н	0.41	0.51	0.61		
К	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.06	0.13	0.20		
М	0.50	-	-		
α	0°	-	12°		







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