



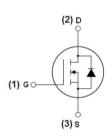
MJ N-Channel Enhancement Mode Power MOSFET

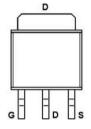
Description

The MJ6020AL uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- ♦ V_{DS} =60V,I_D =20A R_{DS(ON)} <35mΩ @ V_{GS}=10V R_{DS(ON)} <40mΩ @ V_{GS}=4.5V
- ◆ High density cell design for ultra low Rdson
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ Excellent package for good heat dissipation
- ◆ Special process technology for high ESD capability





Application

◆ Power switching application

Uninterruptible power supply

Hard switched and high frequency circuits



Schematic diagram

Marking and pin assignment

TO-251S top view

100% UIS TESTED! 100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ6020AL	MJ6020AL	TO-251S	-	-	-

Absolute Maximum Ratings (Tc =25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	20	А
Drain Current-Continuous(Tc =100°C)	I D(100℃)	14	А
Pulsed Drain Current	Ідм	60	А
Maximum Power Dissipation	Po	45	W
Derating factor		0.3	W/°C
Single pulse avalanche energy (Note 5)	Eas	72	mJ
Operating Junction and Storage Temperature Range	Тл ,Тѕтс	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	3.3	°C/W
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Electrical Characteristics (Tc =25°Cunless otherwise noted)

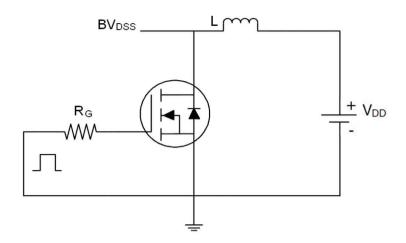
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V I _D =250µA	60	_	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	lgss	V _{DS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _G S(th)	V _{DS} =V _{GS} ,I _D =250µA	1.2	1.6	2.5	V
Desir Course On Chata Desirtance		V _{GS} =10V, I _D =20A	-	24	35	mΩ
Drain-Source On-State Resistance	Rds(on)	V _{GS} =4.5V, I _D =20A		30	40	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =5A	11	_	-	S
Dynamic Characteristics (Note 4)	1					
Input Capacitance	Clss		-	500	-	PF
Output Capacitance	Coss	V _{DS} =30V,V _{GS} =0V F=1.0MHz	-	60	-	PF
Reverse Transfer Capacitance	Crss		-	25	-	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t _{d(on)}		-	5	-	nS
Turn-on Rise Time	tr	V _{DD} =30V,I _D =2A,R _L =6.7Ω	-	2.6	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =10V,R _G =3Ω	-	16.1	-	nS
Turn-Off Fall Time	tr		-	2.3	_	nS
Total Gate Charge	Qg		-	25	-	nC
Gate-Source Charge	Qgs	V _{DS} =30V,I _D =4.5A V _{GS} =10V	-	4.5	-	nC
Gate-Drain Charge	Qgd		-	6.5	-	nC
Drain-Source Diode Characteristics	I					
Diode Forward Voltage ^(Note 3)	VsD	V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	20	А
Reverse Recovery Time	trr	TJ=25°C, IF=20A	-	29	-	nS
Reverse Recovery Charge	Qrr	di/dt=100A/µs (Note 3)	-	49	-	nC
Forward Turn-On Time	ton	Intrinsic turn-on time is ne	aliaible(tı	ırn-on is d	ominated h	v LS+I D

Notes:

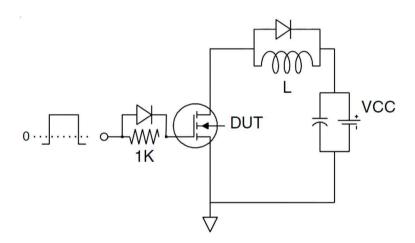
- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, $t \le 10$ sec.
- ③ Pulse Test: Pulse Width ≤ 300 μ s, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production
- $\begin{tabular}{ll} \hline \hline \texttt{S} & EAS & condition:} T_j = 25 \begin{tabular}{ll} $^\circ C$, $V_{DD} = 30V$, $V_G = 10V$, $L = 0.5mH$, $Rg = 25\Omega$ \\ \hline \end{tabular}$



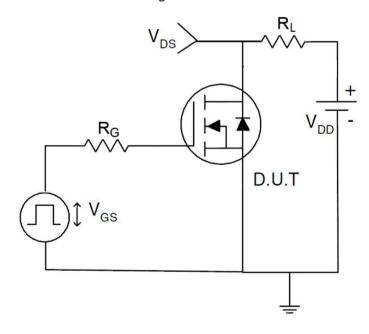
Test circuit



Eas test Circuit



Gate charge test Circuit



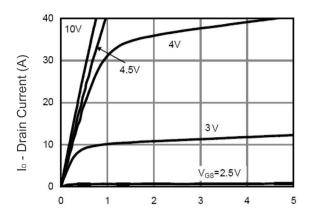
Switch Time Test Circuit





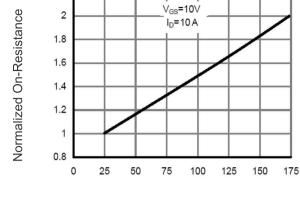
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Typical Electrical and Thermal Characteristics (Curves)



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



T_J -Junction Temperature(°C)
Figure 4 Rdson-Junction Temperature

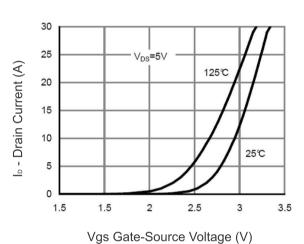


Figure 2 Transfer Characteristics

V_{gs}=4.5V

V_{GS}=10V

40

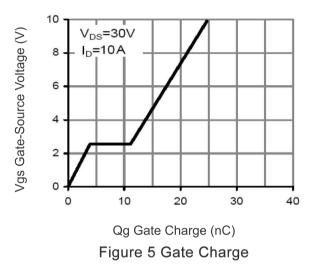
30

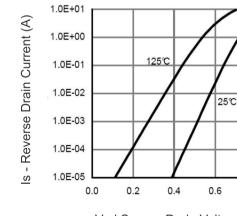
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10

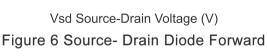
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Rdson On-Resistance(mΩ)









0.8

1.0



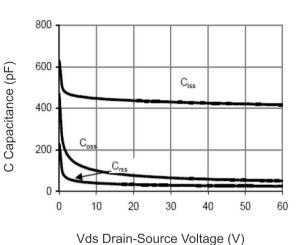
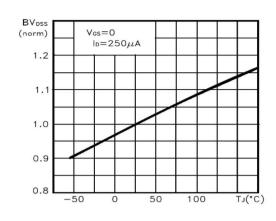


Figure 7 Capacitance vs Vds



T_J -Junction Temperature(°C)

Figure 9 BV_{DSS} vs Junction Temperature

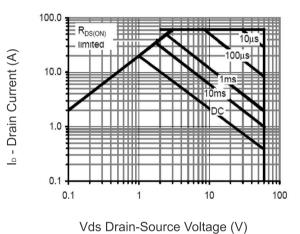
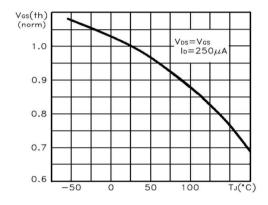
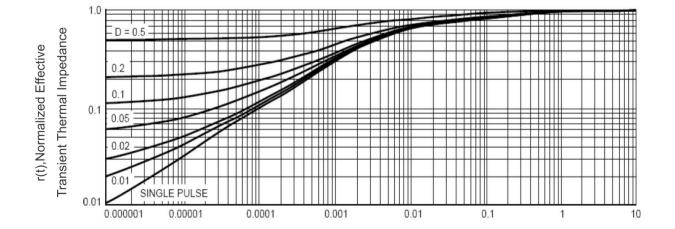


Figure 8 Safe Operation Area



T_J -Junction Temperature(°C)

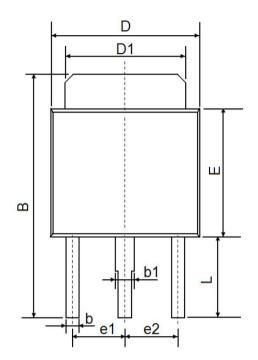
Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

TO-251S Package Information





Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
А	2.250	2.350	0.089	0.093
A1	1.150	1.250	0.045	0.049
В	10.200	10.800	0.402	0.425
b	0.550	0.650	0.022	0.026
b1	0.750	0.850	0.030	0.033
С	0.480	0.540	0.019	0.021
c1	0.480	0.540	0.019	0.021
D	6.400	6.600	0.252	0.260
D1	5.250	5.350	0.207	0.211
Е	5.400	5.600	0.213	0.220
e1	2.300 TYP		0.091	TYP
e2	2.300 TYP		0.091	TYP
L	3.300	3.700	0.130	0.146





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