

# MJ N-Channel Enhancement Mode Power MOSFET

## Description

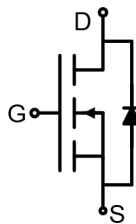
The MJ6003Y uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other switching application.

## General Features

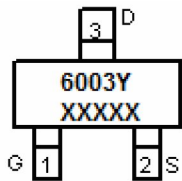
- ◆  $V_{DS} = 60V, I_D = 3A$   
 $R_{DS(ON)} < 105m\Omega @ V_{GS} = -10V$   
 $R_{DS(ON)} < 125m\Omega @ V_{GS} = 4.5V$
- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface mount package

## Application

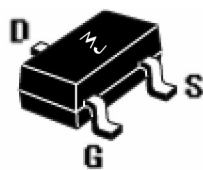
- ◆ Battery switch
- ◆ DC/DC converter



Schematic Diagram



Marking and Pin Assignment



SOT-23-3L top View

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
6003Y	MJ6003Y	SOT-23-3L	Ø180mm	8 mm	3000 units

## Absolute Maximum Ratings (T<sub>c</sub> =25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	±20	V
Drain Current-Continuous	$I_D$	3	A
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	10	A
Maximum Power Dissipation	$P_D$	1.7	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	73.5	°C/W
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Electrical Characteristics (T<sub>A</sub> =25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	65	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>GS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	0.8	1.3	2.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3A	-	78	105	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	95	125	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V,I <sub>D</sub> =3A	4	-	-	S
Dynamic Characteristics <sup>(Note 4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V, F=1.0MHz	-	510	-	PF
Output Capacitance	C <sub>oss</sub>		-	34	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	26	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, I <sub>D</sub> =3A V <sub>GS</sub> =10V,R <sub>GEN</sub> =1Ω	-	6	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	15	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	15	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =3A, V <sub>GS</sub> =10V	-	14.6	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =3A	-	-	1.2	V
Diode Forward Current <sup>(Note 2)</sup>	I <sub>S</sub>		-	-	3	A

Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④ Guaranteed by design, not subject to production

# Typical Electrical and Thermal Characteristics

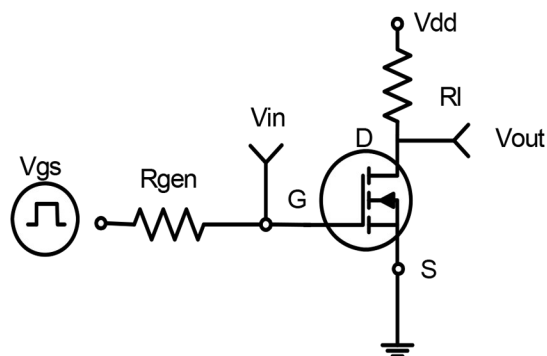


Figure 1 Switching Test Circuit

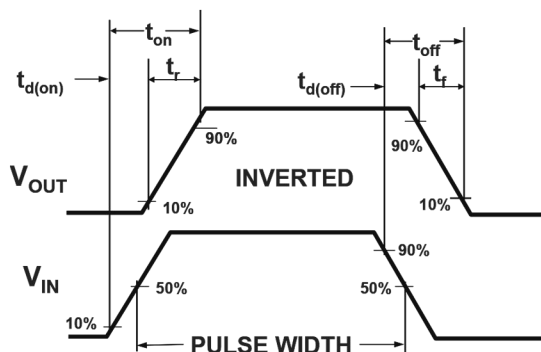


Figure 2 Switching Waveforms

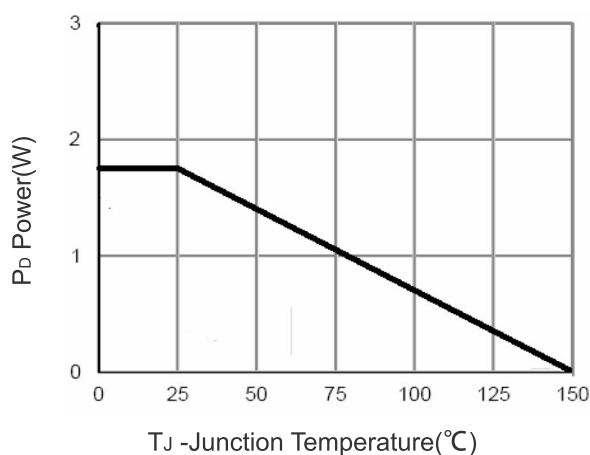


Figure 3 Power Dissipation

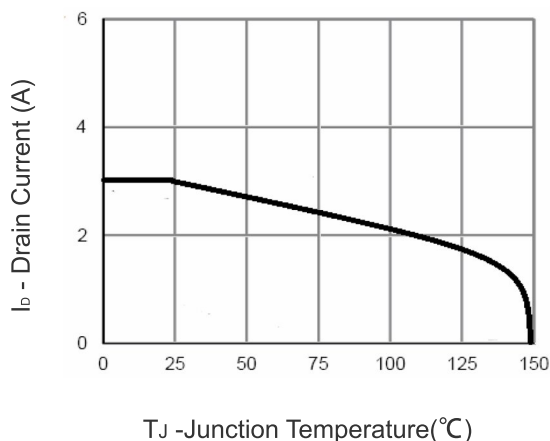


Figure 4 Drain Current

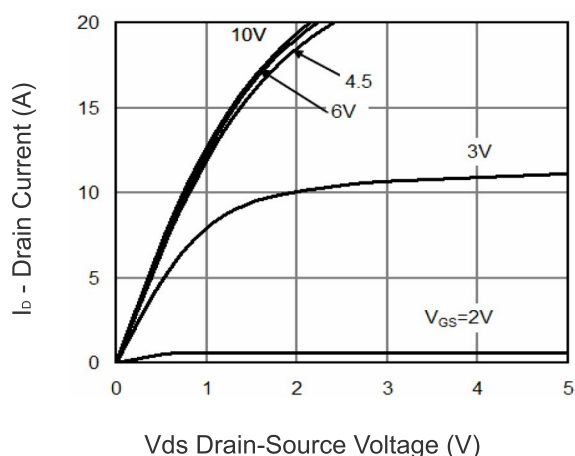


Figure 5 Output Characteristics

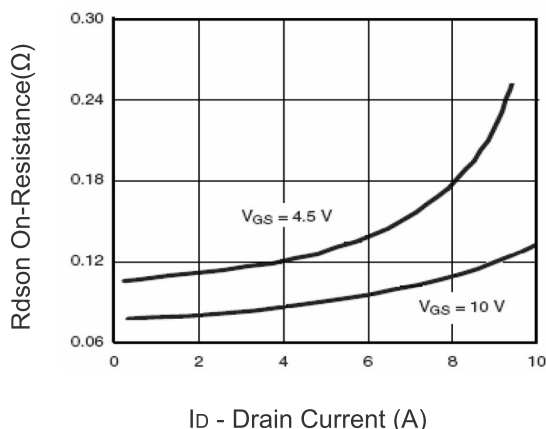


Figure 6 Drain-Source On-Resistance

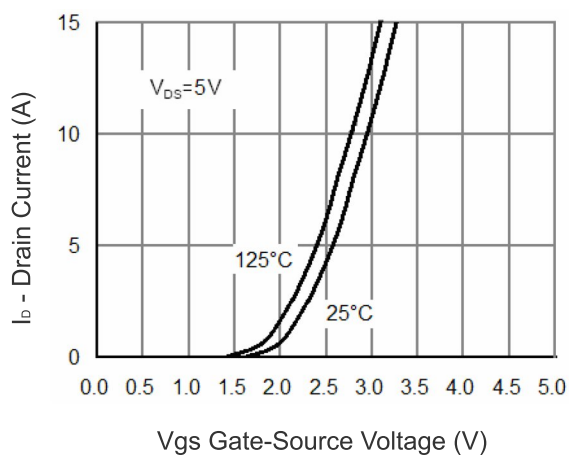


Figure 7 Transfer Characteristics

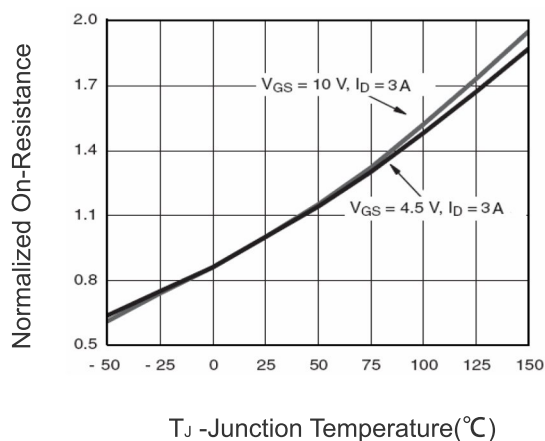


Figure 8 Drain-Source On-Resistance

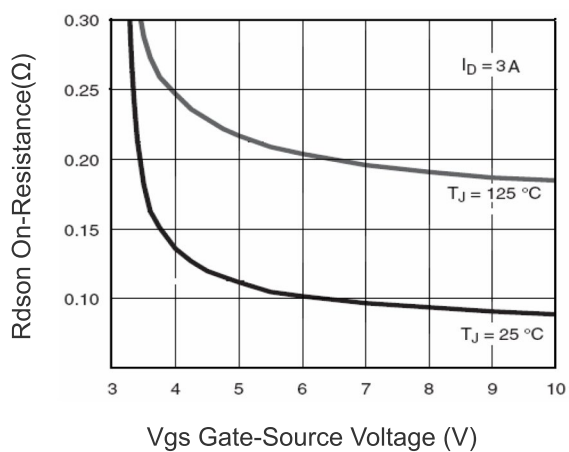
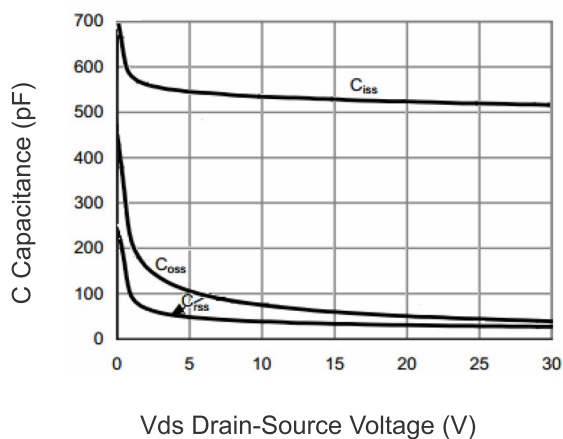
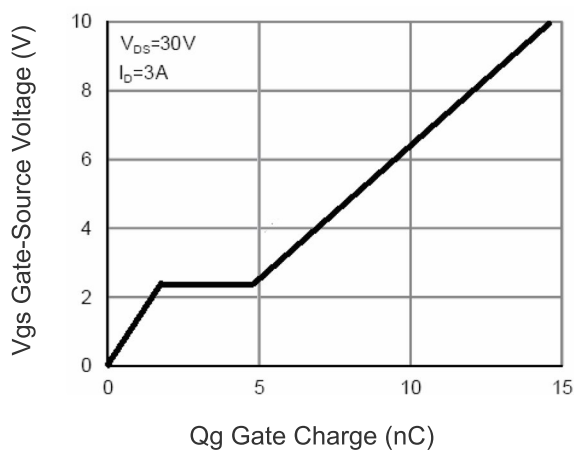

Figure 9  $R_{DS(on)}$  vs  $V_{GS}$ 

Figure 10 Capacitance vs  $V_{DS}$ 


Figure 11 Gate Charge

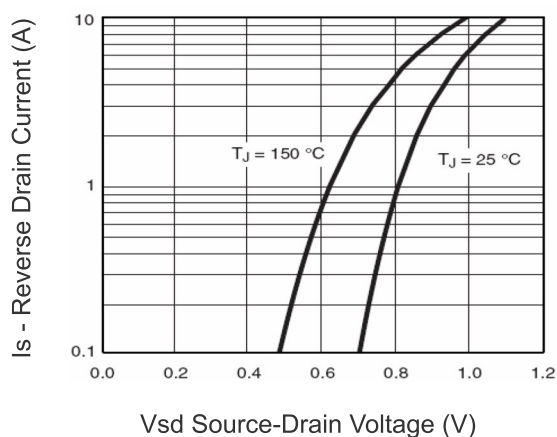


Figure 12 Source- Drain Diode Forward

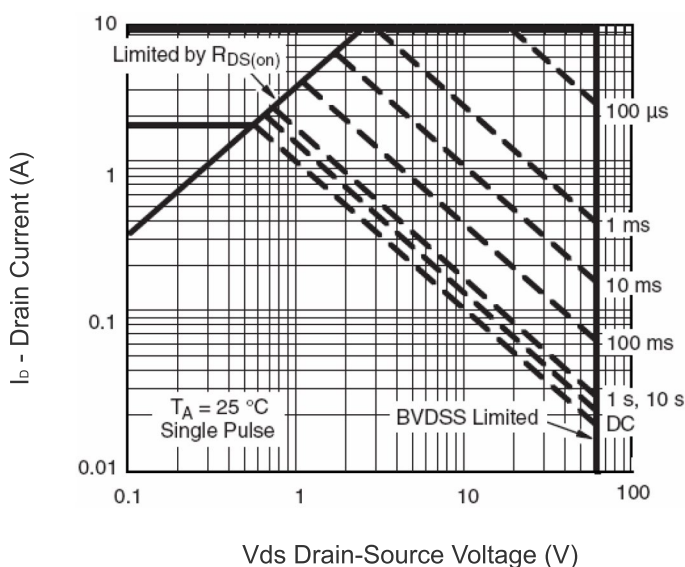


Figure 13 Safe Operation Area

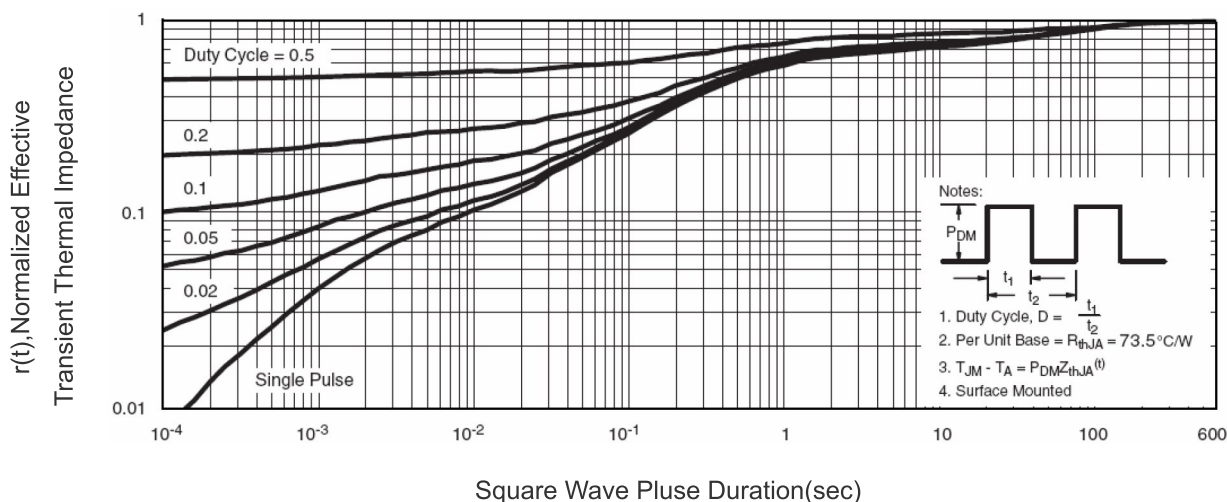
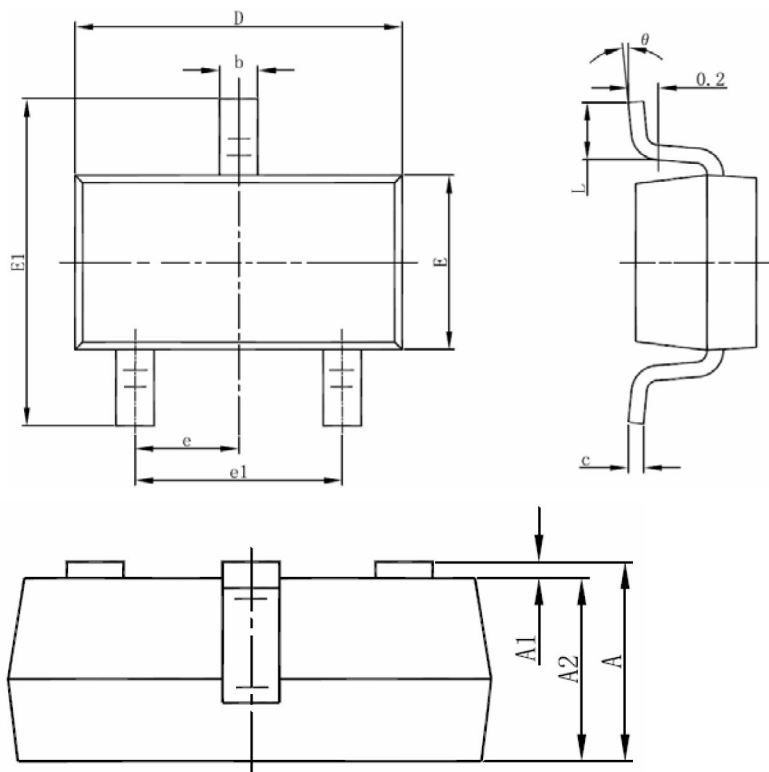


Figure 14 Normalized Maximum Transient Thermal Impedance

### SOT-23-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
$\theta$	0°	8°	0°	8°

#### Notes:

- ① All dimensions are in millimeters.
- ② Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
- ③ Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- ④ Dimension L is measured in gauge plane.
- ⑤ Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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