



MJ P-Channel Enhancement Mode Power MOSFET

Description

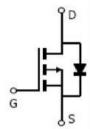
The MJ55P30K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

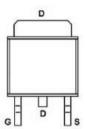
- $V_{DS} = -55V, I_{D} = -30A$ $R_{DS(ON)} < 40mΩ @ V_{GS} = -10V$
- ♦ High density cell design for ultra low Rdson
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ Excellent package for good heat dissipation

Application

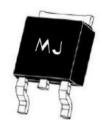
- ♦ Power switching application
- ◆ Hard switched and high frequency circuits
- Uninterruptible power supply







Marking and pin assignment



TO-252-2L top view

100% UIS TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ55P30K	MJ55P30K	TO-252-2L	2	<u> </u>	¥

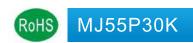
Absolute Maximum Ratings (Tc =25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-55	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	-30	А
Drain Current-Continuous(Tc =100℃)	I D(100℃)	-21	А
Pulsed Drain Current	IDM	110	А
Maximum Power Dissipation	Po	65	W
Derating factor		0.43	W/°C
Single pulse avalanche energy (Note 5)	Eas	420	mJ
Operating Junction and Storage Temperature Range	Тл ,Тѕтс	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rөjc	2.3	°C/W
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Electrical Characteristics (Tc =25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	'					
Drain-Source Breakdown Voltage	BVpss	V _{GS} =0V I _D =-250µA	-55	-	-	V
Zero Gate Voltage Drain Current	loss	V _{DS} =-55V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	lgss	V _{DS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	'					
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =-250μA	-2	-2.6	-4	V
Drain-Source On-State Resistance	Rds(on)	V _{GS} =-10V, I _D =-15A	-	30	40	mΩ
Forward Transconductance	g FS	V _{DS} =-25V,I _D =-16A	8	-	-	S
Dynamic Characteristics (Note 4)	-		1			
Input Capacitance	Clss		-	3500	-	PF
Output Capacitance	Coss	V _{DS} =-30V,V _{GS} =0V F=1.0MHz	-	240	-	PF
Reverse Transfer Capacitance	Crss	•	-	153	-	PF
Switching Characteristics (Note 4)	'					
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	tr	V _{DD} =-30V,I _D =-15A	-	15	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =-10V,R _{GEN} =3Ω	-	38	-	nS
Turn-Off Fall Time	tf	•	-	15	-	nS
Total Gate Charge	Qg		-	56	-	nC
Gate-Source Charge	Qgs	V _{DS} =-30V,I _D =-15A V _{GS} =-10V	-	11	-	nC
Gate-Drain Charge	Q _{gd}		-	24	-	nC
Drain-Source Diode Characteristics	I	I	1	1	<u> </u>	1
Diode Forward Voltage (Note 3)	VsD	V _G s=0V,I _S =-15A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	-30	Α
Reverse Recovery Time	trr	T」= 25°C, I⊧=-15A	-	-	71	nS
Reverse Recovery Charge	Qrr	di/dt =100A/µs (Note3)	_	-	170	nC

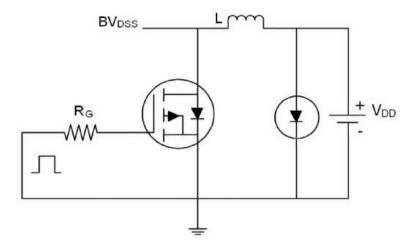
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board, t ≤ 10 sec.
- $\ensuremath{\mathfrak{J}}$ Pulse Test: Pulse Width $\leq 300 \mu s,$ Duty Cycle $\leq 2\%.$
- ④ Guaranteed by design, not subject to production
- $\textbf{(5)} \ \ E_{AS} \ \ condition: \ \ T_j=25^{\circ}C, V_{DD}=-25V, V_{G}=-20V, L=0.5mH, Rg=25\Omega, I_{AS}=29A$

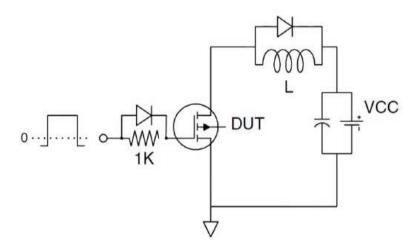




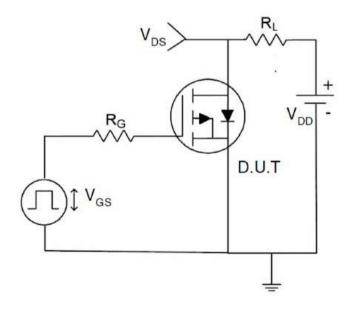
Test circuit



Eas test Circuit



Gate charge test Circuit

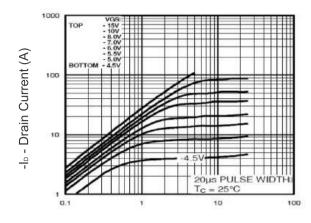


Switch Time Test Circuit



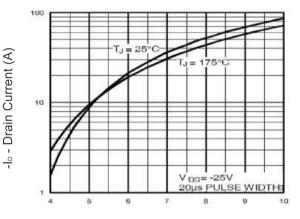


Typical Electrical and Thermal Characteristics (Curves)



-Vds Drain-Source Voltage (V)





-Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

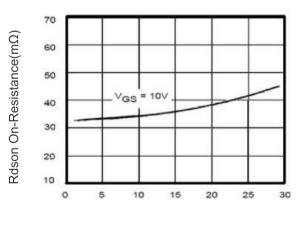
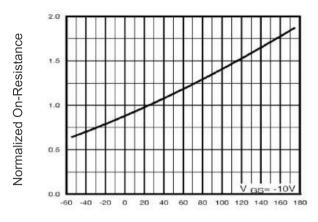
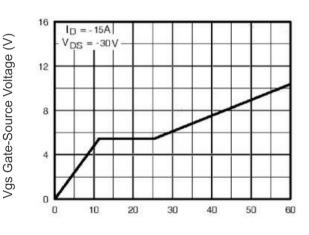


Figure 3 Rdson- Drain Current



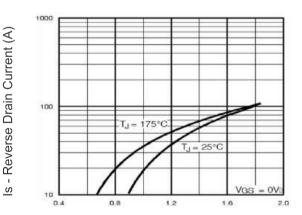
T_J -Junction Temperature(°C)

Figure 4 Rdson-Junction Temperature



Qg Gate Charge (nC)

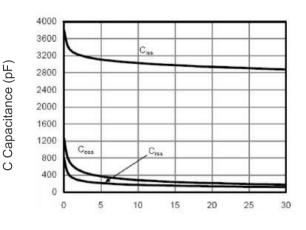
Figure 5 Gate Charge



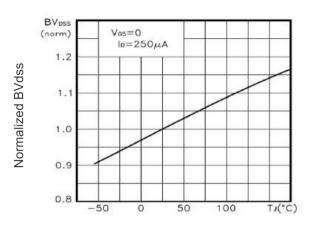
Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward

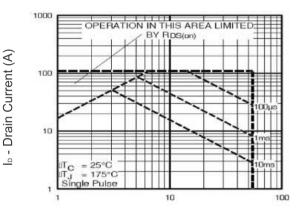




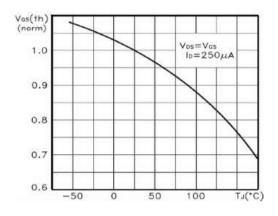
Vds Drain-Source Voltage (V) Figure 7 Capacitance vs Vds



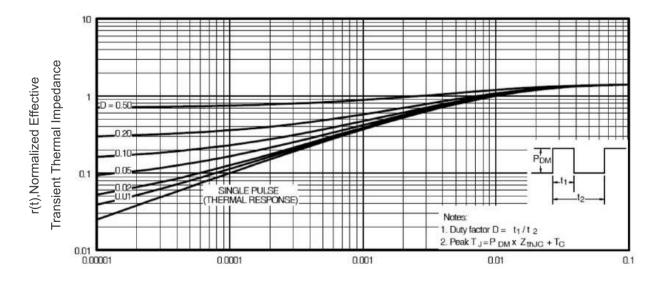
Temperature(°C) Figure 9 BVpss vs Junction Temperature



Vds Drain-Source Voltage (V) Figure 8 Safe Operation Area



T_J -Junction Temperature(°C) Figure 10 V_{GS(th)} vs Junction Temperature



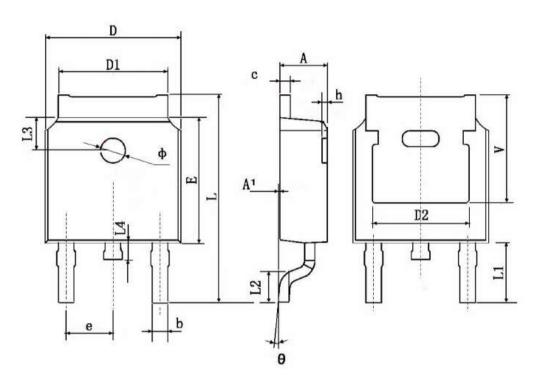
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance





TO-252 Package Information



Complete	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.8	30 TYP.	0.190 TYP.	
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600	TYP.	0.063	TYP.
L4	0.600	1.000	0.024	0.039
Ф	1.100	1.300	0.043	0.051
θ	0°	8°	0.	8°
h	0.000	0.300	0.000	0.012
V	5.350	TYP.	0.211	TYP.





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