



# MJ P-Channel Enhancement Mode Power MOSFET

### Description

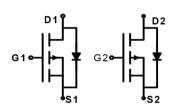
The MJ4953A uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a load switch or in PWM applications.

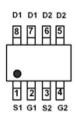
#### **General Features**

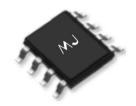
- ♦ V<sub>DS</sub> =-30V,I<sub>D</sub> =-5.3A R<sub>DS(ON)</sub> <85mΩ @ V<sub>GS</sub>=-4.5V R<sub>DS(ON)</sub> <49mΩ @ V<sub>GS</sub>=-10V
- ◆ High power and current handing capability
- Lead free product is acquired
- ◆ Surface Mount Package

### **Application**

- ◆ PWM applications
- ♦ Load switch
- ◆ Power management







Schematic diagram

Marking and pin Assignment

SOP-8 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4953A	MJ4953A	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	-5.3	А
Drain Current-Pulsed (Note 1)	IDM	-20	А
Maximum Power Dissipation	Po	2.6	W
Operating Junction and Storage Temperature Range	Тл ,Тѕтс	-55 To 150	°C

#### Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	Reja	49	°C/W
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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Uni
Off Characteristics	'					
Drain-Source Breakdown Voltage	BVpss	V <sub>GS</sub> =0V,I <sub>D</sub> =-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	-	-	-1	μΑ
Gate-Body Leakage Current	lgss	Igss Vps=±20V,Vps=0V		-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1	-1.6	-3	V
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-5.3A	-	43	49	mΩ
Drain-Source On-State Resistance	Rds(on)	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.2A	-	68	100	mΩ
Forward Transconductance	grs	V <sub>DS</sub> =-15V,I <sub>D</sub> =-4.5A	4	7	_	s
Dynamic Characteristics (Note 4)				'		
Input Capacitance	Clss		-	540	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V F=1.0MHz	-	150	-	PF
Reverse Transfer Capacitance	Crss	•	-	75	-	PF
Switching Characteristics (Note 4)	'					
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	tr	VDD=-15V,ID=-1A	-	14	-	nS
Turn-Off Delay Time	td(off)	V <sub>GS</sub> =-10V,R <sub>GEN</sub> =6Ω	-	18	-	nS
Turn-Off Fall Time	tr	-	-	10	_	nS
Total Gate Charge	Qg		-	12	_	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =-15V,I <sub>D</sub> =-5.3A V <sub>GS</sub> =-10V	-	2.4	_	nC
Gate-Drain Charge	Q <sub>gd</sub>	V G510 V	-	3.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	VsD	V <sub>GS</sub> =0V,I <sub>S</sub> =-5.3A	_	_	-1.2	V

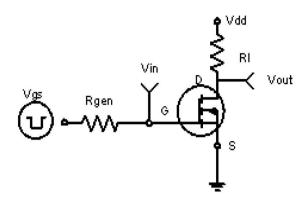
### Notes:

- $\textcircled{1} \ \mathsf{Repetitive} \ \mathsf{Rating:} \ \mathsf{Pulse} \ \mathsf{width} \ \mathsf{limited} \ \mathsf{by} \ \mathsf{maximum} \ \mathsf{junction} \ \mathsf{temperature}.$
- ② Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production



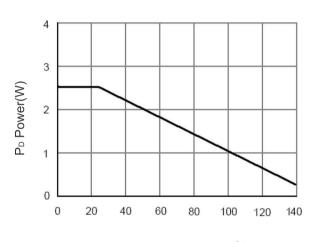


### Typical Electrical and Thermal Characteristics

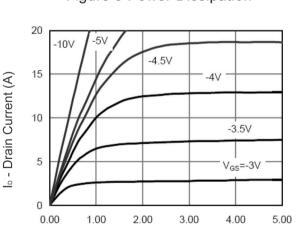


 $t_{d(on)}$   $t_{r}$   $t_{d(off)}$   $t_{d(off)$ 

Figure 1 Switching Test Circuit

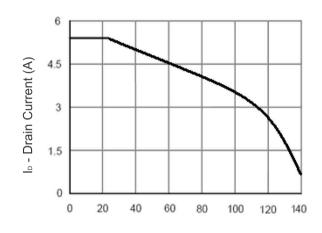


T<sub>J</sub>-Junction Temperature(°C)
Figure 3 Power Dissipation

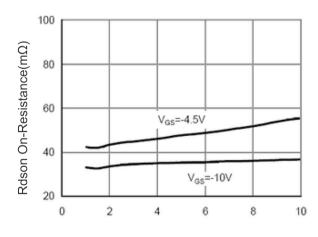


Vds Drain-Source Voltage (V)
Figure 5 Output Characteristics

Figure 2 Switching Waveforms

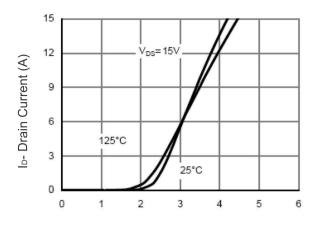


T<sub>J</sub>-Junction Temperature(°C) Figure 4 Drain Current

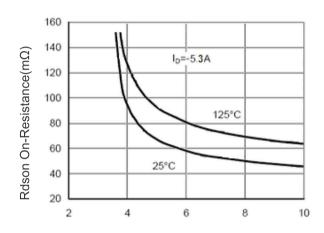


I<sub>D</sub>- Drain Current (A)
Figure 6 Drain-Source On-Resistance

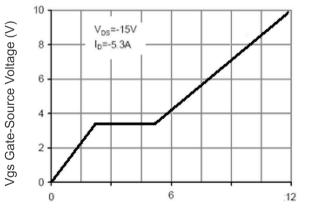




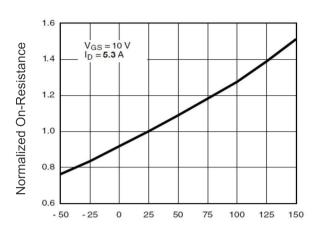
Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



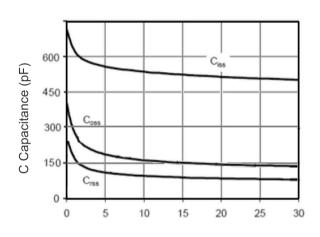
Vgs Gate-Source Voltage (V) Figure 9 Rdson vs Vgs



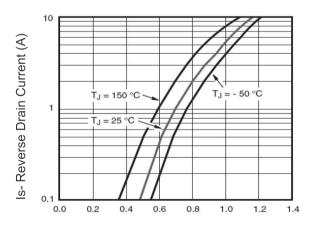
Qg Gate Charge (nC)
Figure 11 Gate Charge



T<sub>J</sub>-Junction Temperature(°C)
Figure 8 Drain-Source On-Resistance

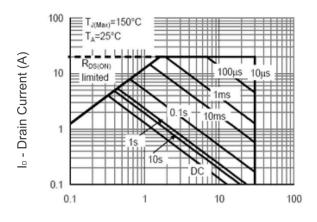


Vds Drain-Source Voltage (V)
Figure10 Capacitance vs Vds



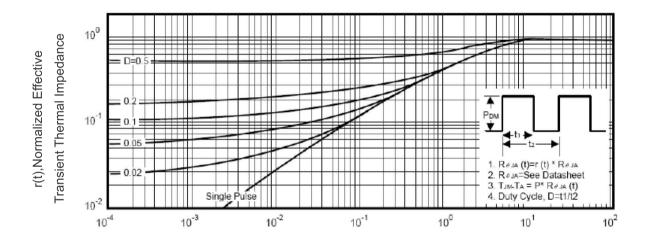
Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area



Square Wave Pluse Duration(sec)

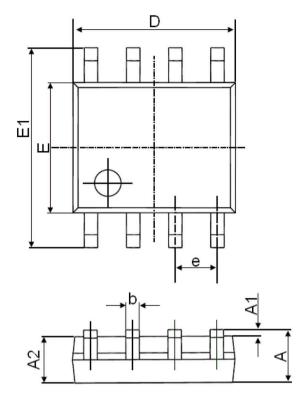
Figure 14 Normalized Maximum Transient Thermal Impedance

θ





# SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	





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