

# N and P-Channel Enhancement Mode Power MOSFET

## Description

The MJ4688 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

## General Features

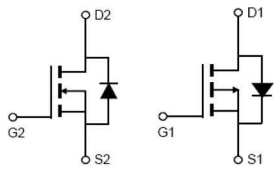
### N-Channel

- ◆  $V_{DS}=60V, I_D=6.3A$   
 $R_{DS(ON)}<30m\Omega$  @  $V_{GS}=10V$

### P-Channel

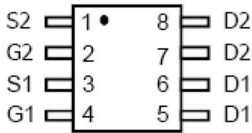
- ◆  $V_{DS}=-60V, I_D=-5A$   
 $R_{DS(ON)}<80m\Omega$  @  $V_{GS}=-10V$

- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface mount package

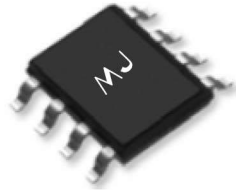


N-channel      P-channel

Schematic diagram



Marking and pin assignment



SOP-8 top view

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4688	MJ4688	SOP-8	Ø330mm	12mm	2500 units

## Absolute Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		$V_{DS}$	60	-60	V
Gate-Source Voltage		$V_{GS}$	±20	±20	V
Continuous Drain Current	$T_A=25^{\circ}C$	$I_D$	6.3	-5	A
	$T_A=70^{\circ}C$	$I_D$	4.5	-3.5	A
Pulsed Drain Current <sup>(Note 1)</sup>		$I_{DM}$	40	-25	A
Maximum Power Dissipation	$T_A=25^{\circ}C$	$P_D$	2.0	2.0	W
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 To 150	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	N-Ch	62.5	°C/W
Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	P-Ch	62.5	°C/W

N-CH Electrical Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.2	1.6	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =6A	-	26	30	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V,I <sub>D</sub> =6A	15	-	-	S
Dynamic Characteristics <sup>(Note 4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V F=1.0MHz	-	500	-	PF
Output Capacitance	C <sub>oss</sub>		-	60	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	25	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V,R <sub>L</sub> =4.7Ω V <sub>GS</sub> =10V,R <sub>GEN</sub> =3Ω	-	5	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	2.6	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	16.1	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	2.3	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =6A V <sub>GS</sub> =10V	-	25	-	nC
Gate-Source Charge	Q <sub>GS</sub>		-	4.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	6.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =6A	-	0.8	1.2	V

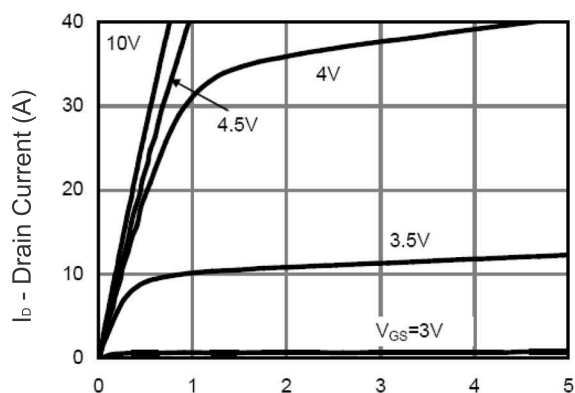
P-CH Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.5	-2.6	-3.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	-	64	80	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-5A	16	-	-	S
Dynamic Characteristics <sup>(Note 4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V F=1.0MHz	-	1450	-	PF
Output Capacitance	C <sub>oss</sub>		-	145	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	110	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-30V,R <sub>L</sub> =30Ω V <sub>GS</sub> =-10V,R <sub>GEN</sub> =6Ω	-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	65	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V,I <sub>D</sub> =-5A V <sub>GS</sub> =-10V	-	26	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	4.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-5A	-	-	-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	I <sub>S</sub>		-	-	-5	A

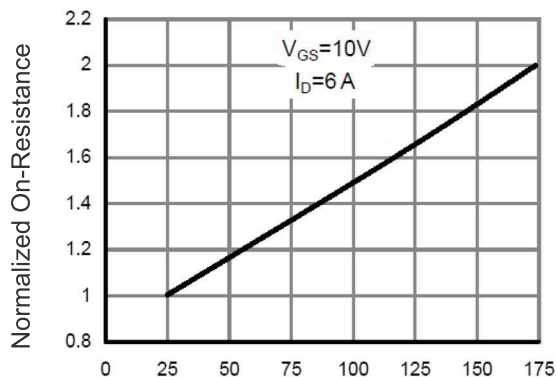
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④ Guaranteed by design, not subject to production

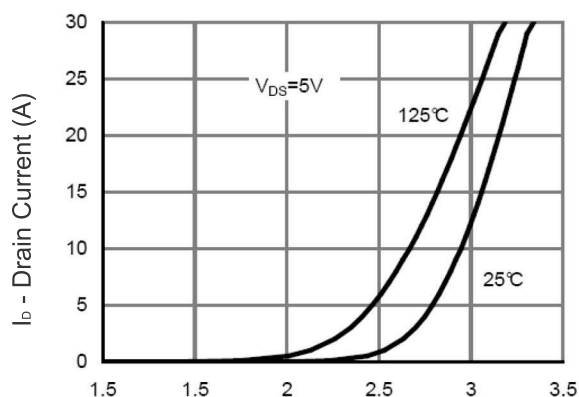
## N-CH Typical Electrical and Thermal Characteristics (Curves)



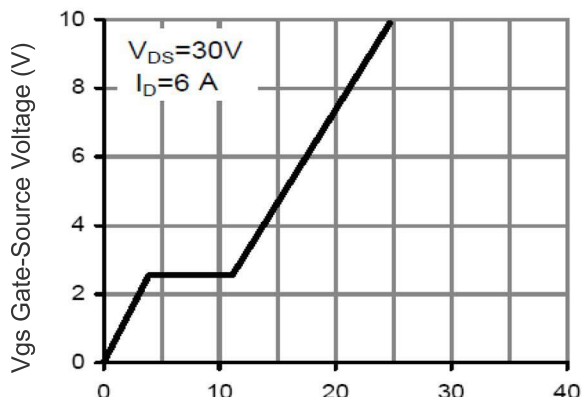
V<sub>DS</sub> Drain-Source Voltage (V)  
Figure 1 Output Characteristics



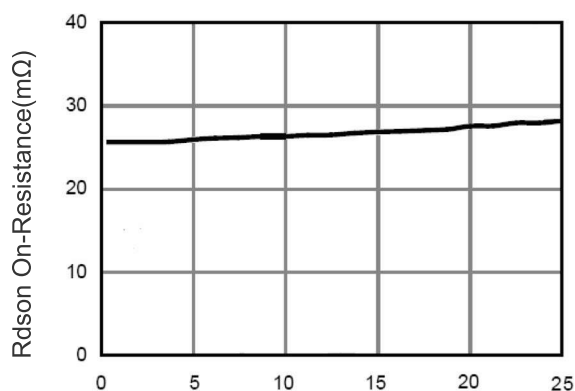
T<sub>J</sub> -Junction Temperature(°C)  
Figure 4 Rdson-Junction Temperature



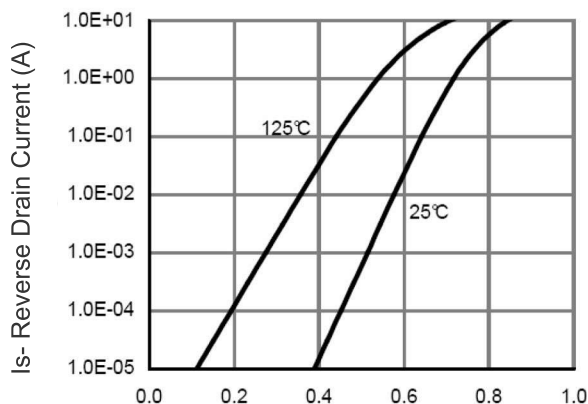
V<sub>GS</sub> Gate-Source Voltage (V)  
Figure 2 Transfer Characteristics



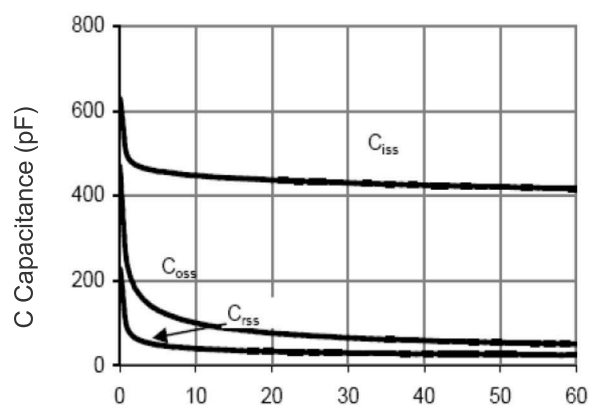
Q<sub>g</sub> Gate Charge (nC)  
Figure 5 Gate Charge



I<sub>D</sub>- Drain Current (A)  
Figure 3 Rdson- Drain Current

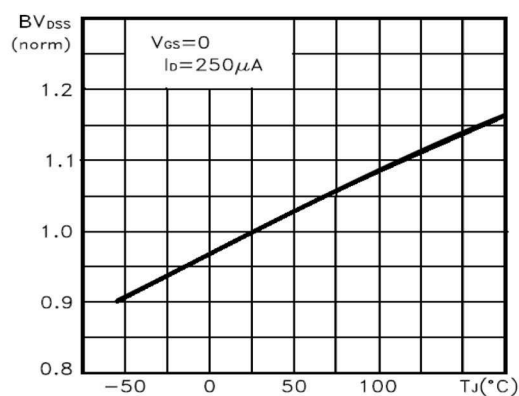


V<sub>SD</sub> Source-Drain Voltage (V)  
Figure 6 Source- Drain Diode Forward



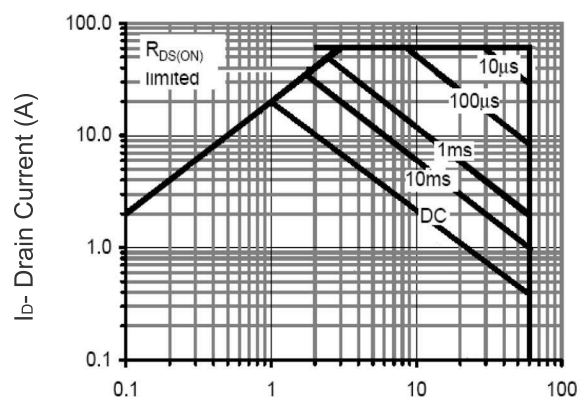
Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



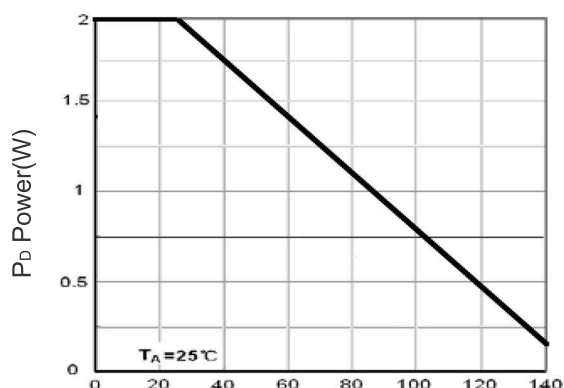
TJ -Junction Temperature(°C)

Figure 9 BV<sub>DSS</sub> vs Junction Temperature



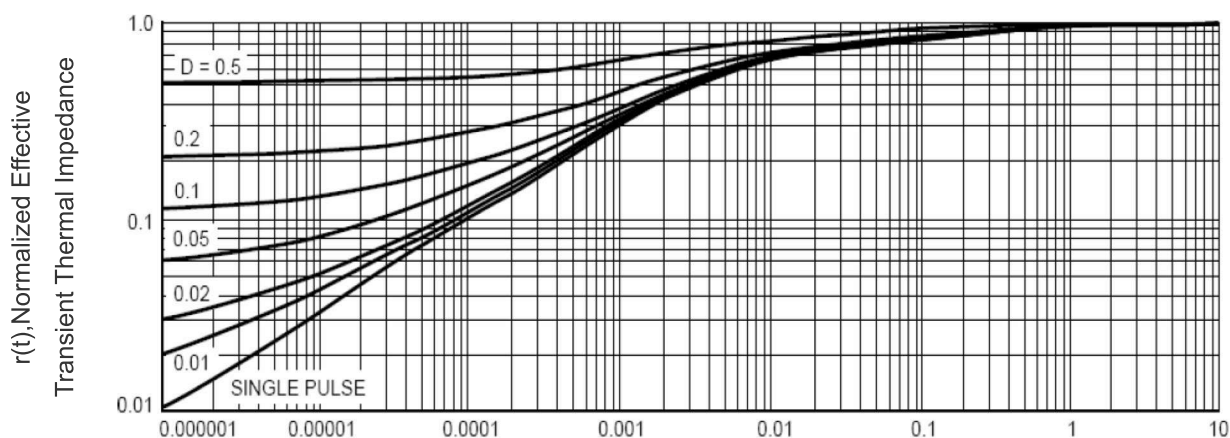
Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



TJ -Junction Temperature(°C)

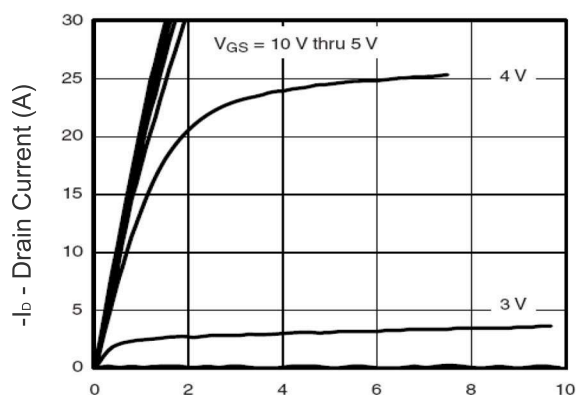
Figure 10 Power Dissipation



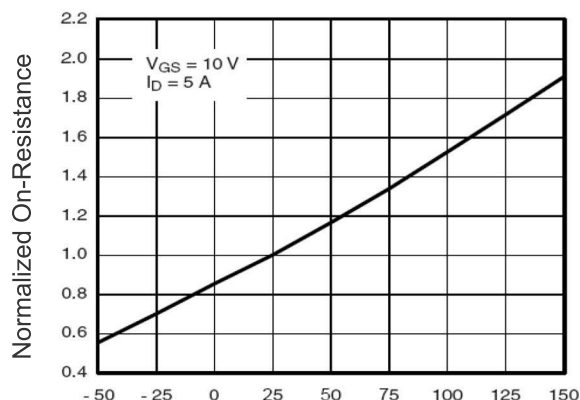
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

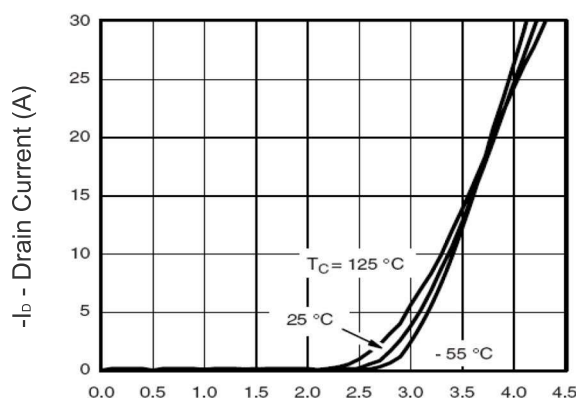
## P-CH Typical Electrical and Thermal Characteristics (Curves)



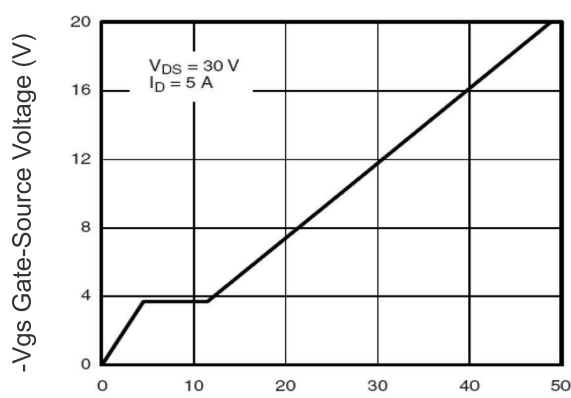
-VDS Drain-Source Voltage (V)  
Figure 1 Output Characteristics



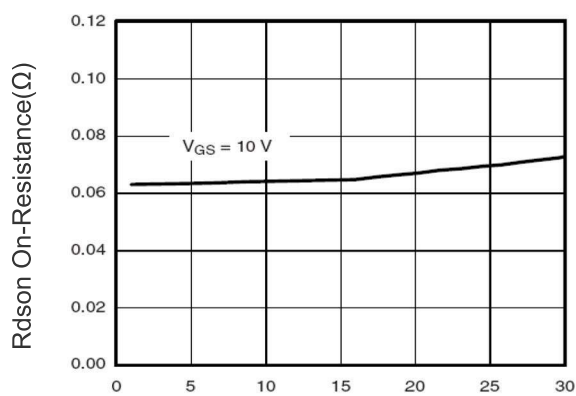
TJ -Junction Temperature(°C)  
Figure 4 Rdson-Junction Temperature



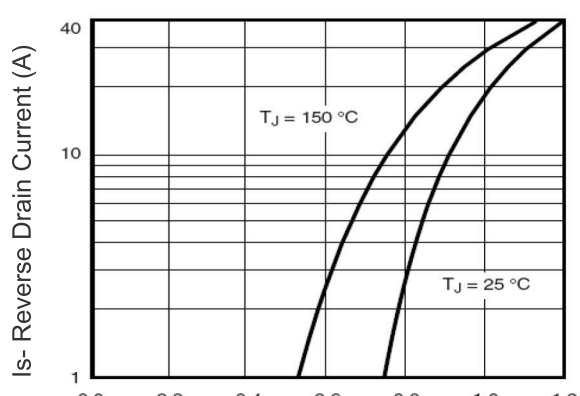
-VGS Gate-Source Voltage (V)  
Figure 2 Transfer Characteristics



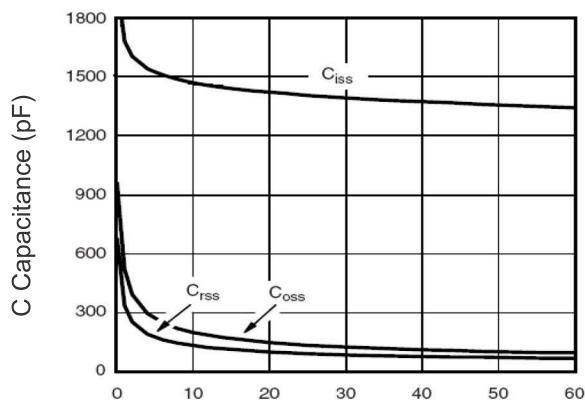
Qg Gate Charge  
Figure 5 Gate Charge



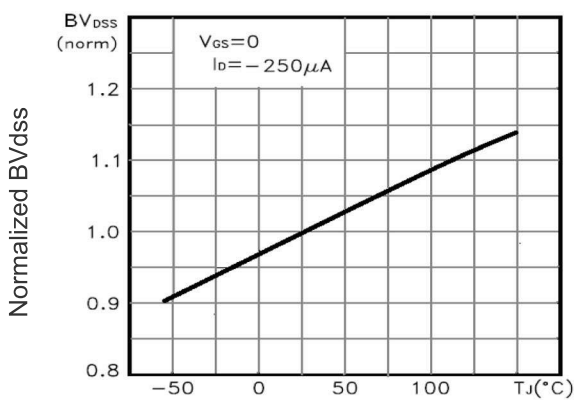
-ID- Drain Current (A)  
Figure 3 Rdson- Drain Current



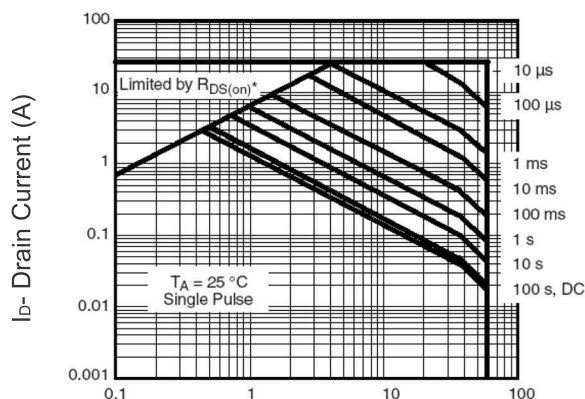
VSD Source-Drain Voltage (V)  
Figure 6 Source- Drain Diode Forward



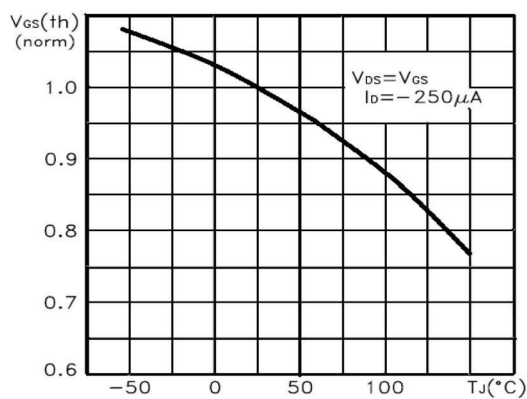
Vds Drain-Source Voltage (V)  
Figure 7 Capacitance vs Vds



TJ -Junction Temperature(°C)  
Figure 9 BV<sub>DSS</sub> vs Junction Temperature



Vds Drain-Source Voltage (V)  
Figure 8 Safe Operation Area



TJ -Junction Temperature(°C)  
Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

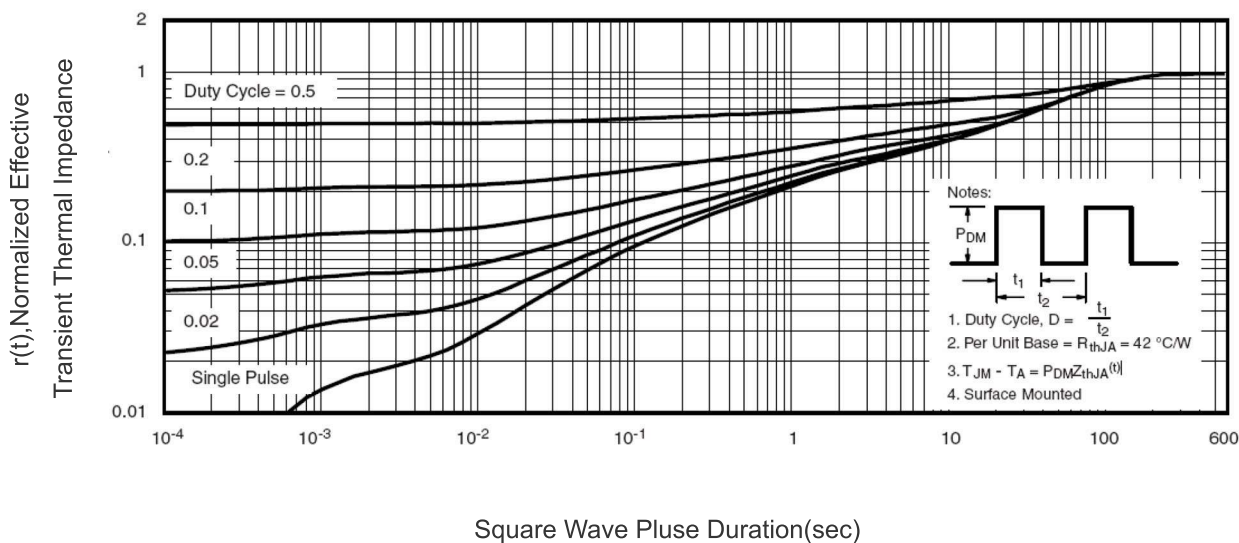
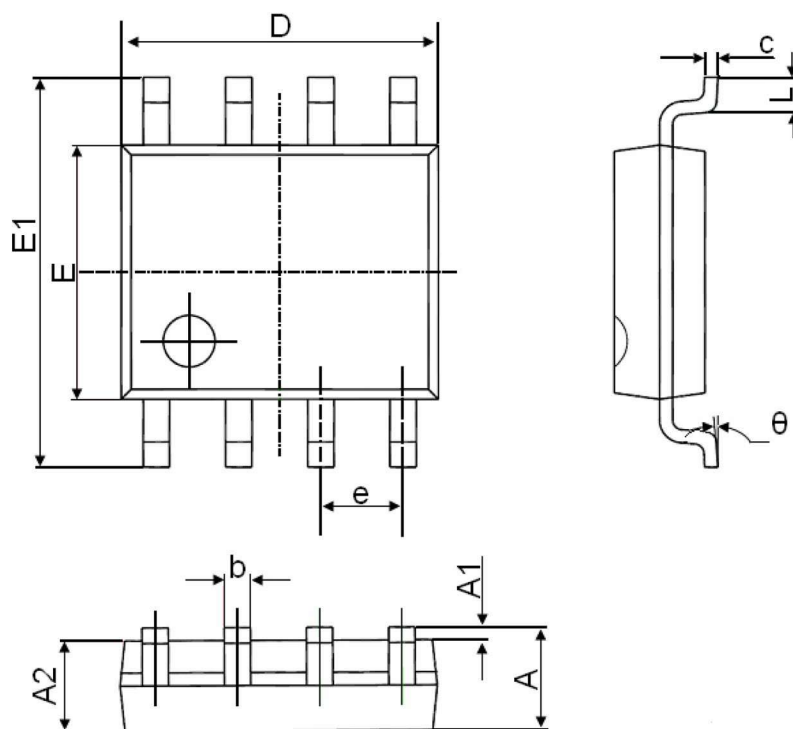


Figure 11 Normalized Maximum Transient Thermal Impedance

# SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



## Attention:

Any and all MJ power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MJ power representative nearest you before using any MJ power products described or contained herein in such applications.

MJ power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MJ power products described or contained herein.

Specifications of any and all MJ power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

MJ power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all MJ power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MJ power Semiconductor CO.,LTD.

Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MJ power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MJ power product that you intend to use.

This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.