

# MJ P-Channel Enhancement Mode Power MOSFET

## Description

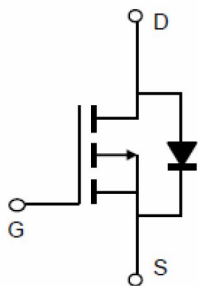
The MJ30P16Q uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. This device is suitable for use as a load switch or in PWM applications.

## General Features

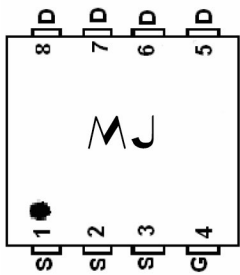
- ◆  $V_{DS}=-30V, I_D=-16A$   
 $R_{DS(ON)}<30m\Omega @ V_{GS}=-4.5V$   
 $R_{DS(ON)}<18m\Omega @ V_{GS}=-10V$
- ◆ High Power and current handling capability
- ◆ Lead free product is acquired
- ◆ Surface mount package

## Application

- ◆ PWM applications
- ◆ Load switch
- ◆ Power management



Schematic diagram



Marking and pin assignment

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ30P16Q	MJ30P16Q	DFN3.3X3.3	Ø330mm	12mm	2500 units

## Absolute Maximum Ratings (T<sub>c</sub> =25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	±20	V
Drain Current-Continuous	$I_D$	-16	A
Drain Current-Continuous(T <sub>c</sub> =100°C)	$I_{D(100^{\circ}C)}$	-11.3	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-64	A
Maximum Power Dissipation	$P_D$	30	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JA}$	4.2	°C/W
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Electrical Characteristics (T<sub>A</sub> =25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.2	-1.6	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A	-	13	18	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	-	22	30	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V,I <sub>D</sub> =-10A	-	20	-	S
Dynamic Characteristics <sup>(Note 4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, F=1.0MHz	-	1363	-	PF
Output Capacitance	C <sub>oss</sub>		-	250	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	210	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-30V, R <sub>L</sub> =3Ω, V <sub>GS</sub> =-10V,R <sub>G</sub> =2.5Ω	-	9	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	10	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	25	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A, V <sub>GS</sub> =-10V	-	31.2	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.2	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	9.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-10A	-		-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	I <sub>S</sub>		-	-	-16	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> =25°C, I <sub>F</sub> =-10A di/dt=100A/μs <sup>(Note 2)</sup>	-	24	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>		-	16	-	nC

Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④ Guaranteed by design, not subject to production
- ⑤ EAS condition: Tj=25℃,V<sub>DD</sub>=-10V,V<sub>G</sub>=-10V,L=0.5mH,Rg=25Ω

# Typical Electrical and Thermal Characteristics

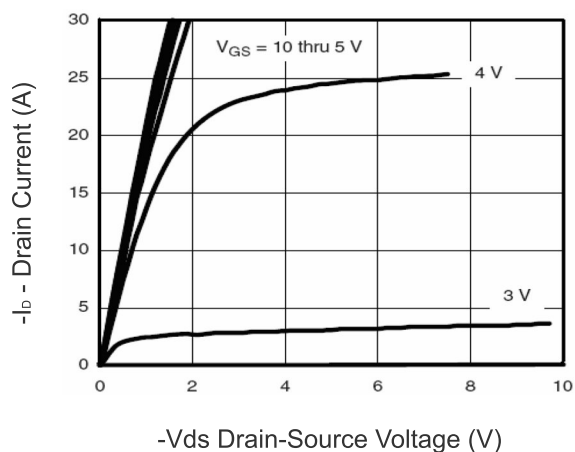


Figure 1 Output Characteristics

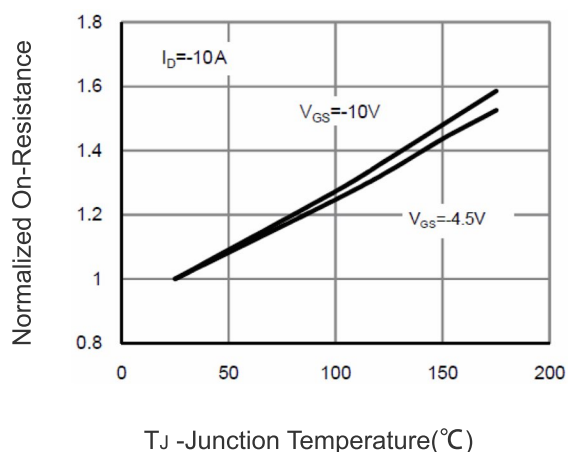


Figure 4 Rdson-Junction Temperature

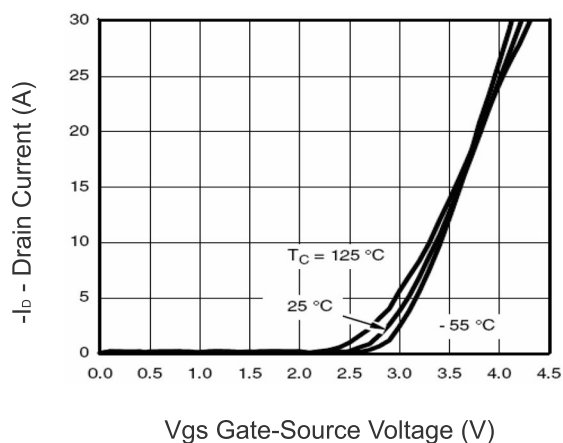


Figure 2 Transfer Characteristics

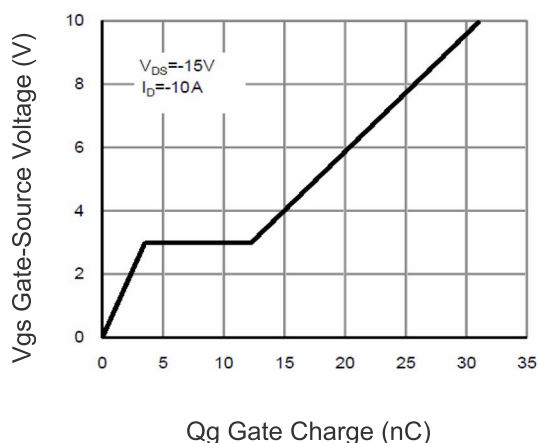


Figure 5 Gate Charge

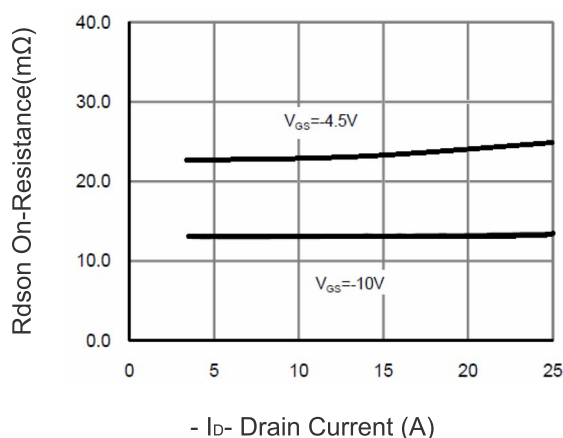


Figure 3 Rdson- Drain Current

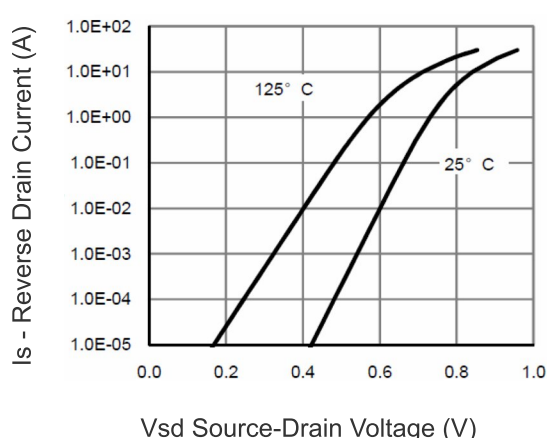
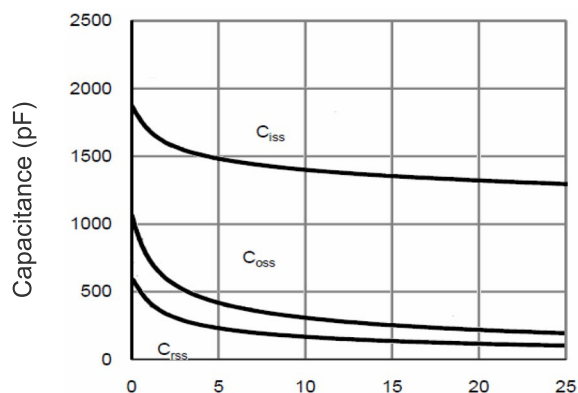
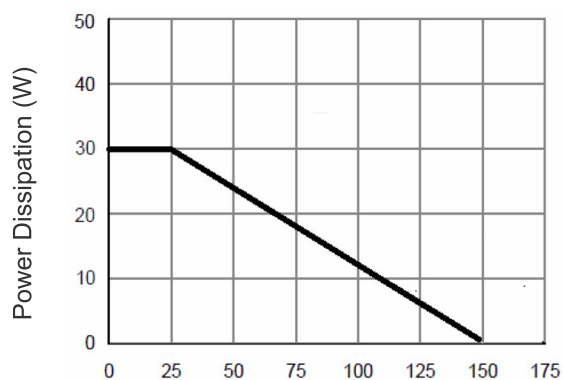


Figure 6 Source- Drain Diode Forward



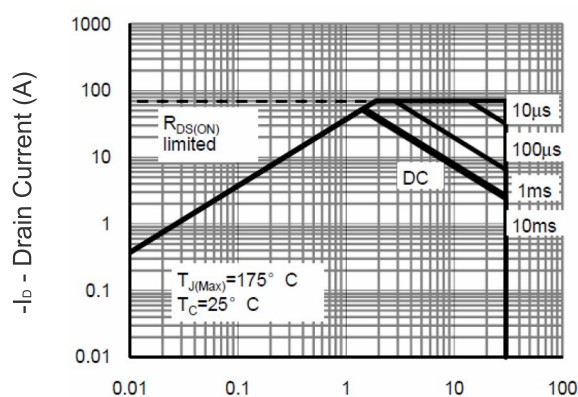
-Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



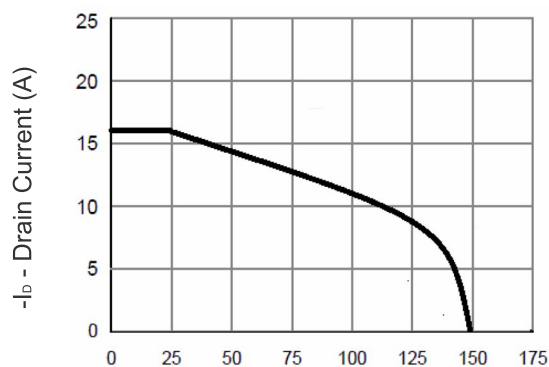
T<sub>J</sub> - Junction Temperature(°C)

Figure 9 Power De-rating



-Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



T<sub>J</sub> - Junction Temperature(°C)

Figure 10 -Current De-rating

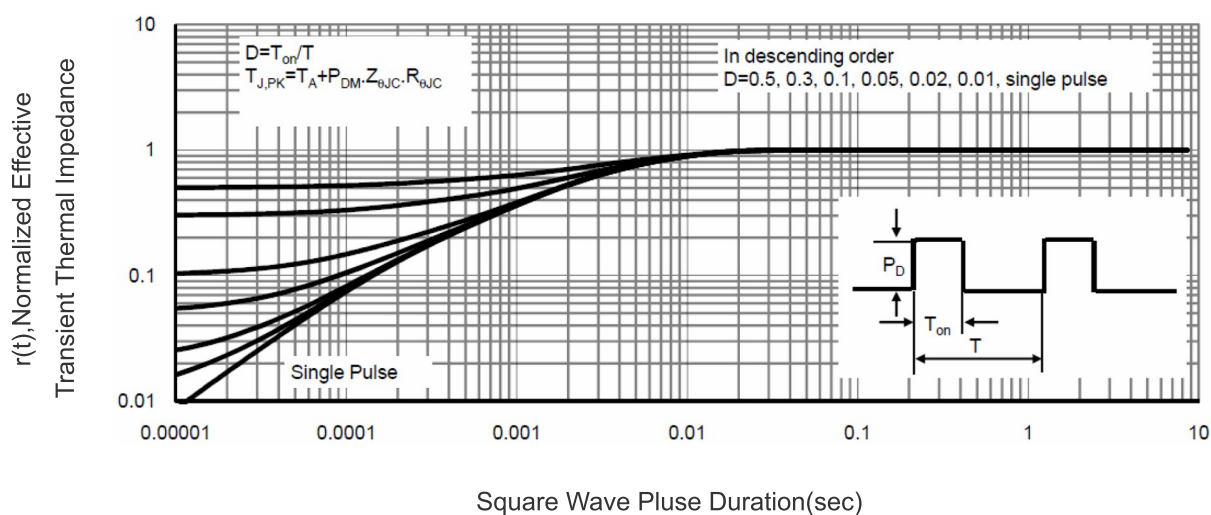
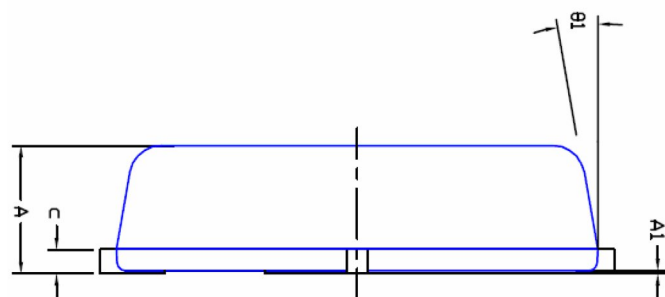
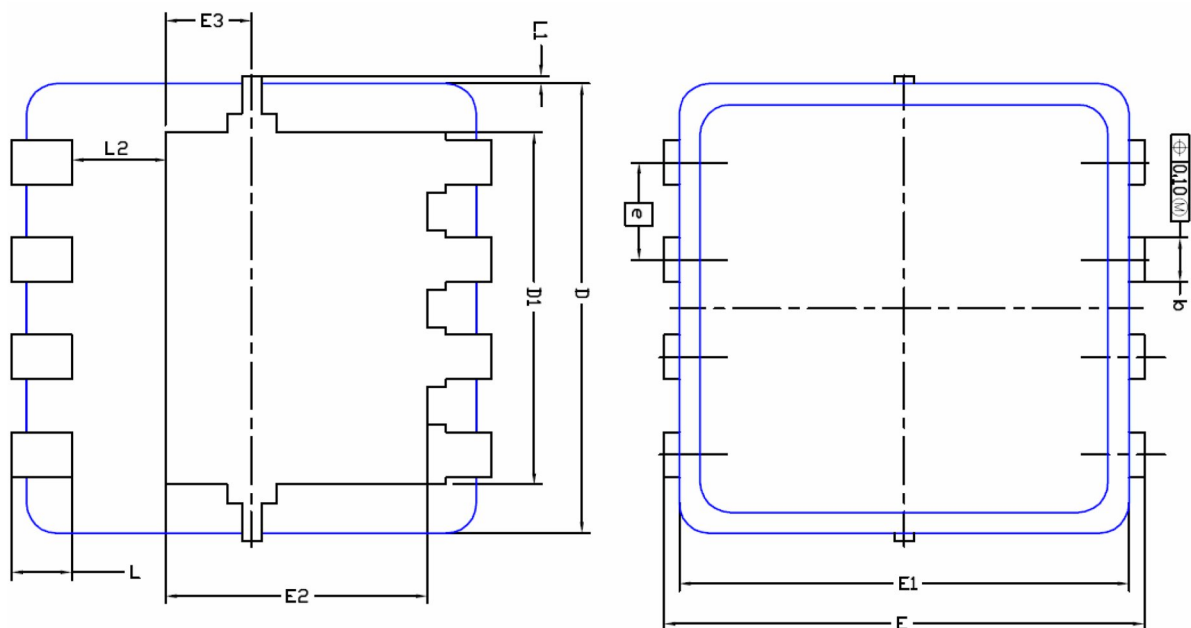


Figure 11 Normalized Maximum Transient Thermal Impedance

# DFN3X3 EP Package Information



DIM.	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.80	0.900	0.0276	0.0315	0.0354
A1	0.00	---	0.05	0.000	---	0.002
b	0.24	0.30	0.35	0.009	0.012	0.014
c	0.10	0.152	0.25	0.004	0.006	0.010
D	3.00 BSC			0.118 BSC		
D1	2.35 BSC			0.093 BSC		
E	3.20 BSC			0.126 BSC		
E1	3.00 BSC			0.118 BSC		
E2	1.75 BSC			0.069 BSC		
E3	0.575 BSC			0.023 BSC		
e	0.65 BSC			0.026 BSC		
L2	0.685 BSC			0.0274 BSC		
L	0.30	0.40	0.50	0.0118	0.0157	0.0197
L1	0	---	0.100	0	---	0.004
θ1	0°	10°	12°	0°	10°	12°

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