

# MJ N-Channel Enhancement Mode Power MOSFET

## Description

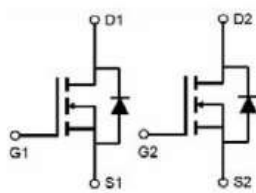
The MJ30ND09S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## General Features

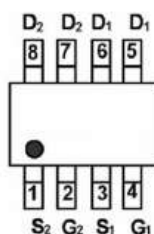
- ◆  $V_{DS} = 30V, I_D = 9A$   
 $R_{DS(ON)} < 12m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(ON)} < 17m\Omega$  @  $V_{GS} = 4.5V$
- ◆ High density cell design for ultra low  $R_{dson}$
- ◆ Fully characterized avalanche voltage and current

## Application

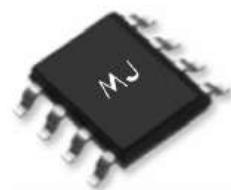
- ◆ Power switching application
- ◆ Hard switched and high frequency circuits
- ◆ Uninterruptible power supply



Schematic diagram



Marking and pin Assignment



SOP-8 top view

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30ND09S	MJ30ND09S	SOP-8	Ø330mm	12mm	4000 units

## Absolute Maximum Ratings ( $T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	9	A
Drain Current-Continuous( $T_C = 100^\circ C$ )	$I_{D(100^\circ C)}$	6.4	A
Pulsed Drain Current	$I_{DM}$	40	A
Maximum Power Dissipation	$P_D$	2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

## Thermal Characteristic

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	62.5	85	$^\circ C/W$

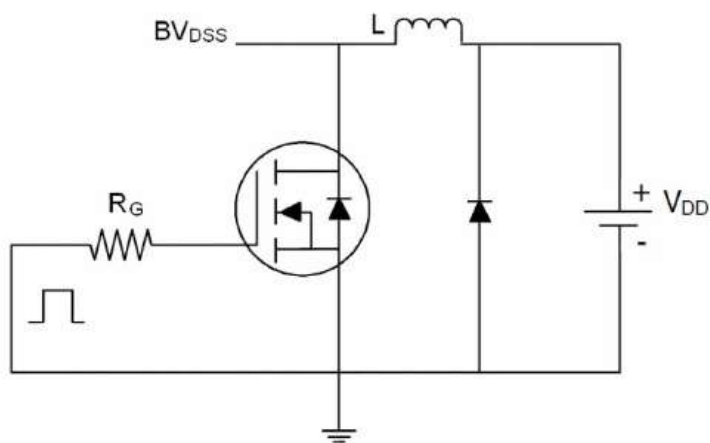
Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>GS</sub> =0V	-	-	±100	nA
On Characteristics <small>(Note 3)</small>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.0	1.6	2.2	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =6A	-	10.3	12.0	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		13.9	17.0	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V,I <sub>D</sub> =6A	26	-	-	S
Dynamic Characteristics <small>(Note 4)</small>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V F=1.0MHz	-	1210	-	PF
Output Capacitance	C <sub>oss</sub>		-	160	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	105	-	PF
Switching Characteristics <small>(Note 4)</small>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V, R <sub>L</sub> =0.75Ω V <sub>GS</sub> =10V,R <sub>G</sub> =3Ω	-	5	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	12	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	19	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	6	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =6A V <sub>GS</sub> =10V	-	17.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	4.1	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <small>(Note 3)</small>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =6A	-	-	1.2	V
Diode Forward Current <small>(Note 2)</small>	I <sub>S</sub>		-	-	9	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> =25°C, I <sub>F</sub> =6A di/dt=100A/μs <small>(Note 3)</small>	-	19	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>		-	10	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible(turn-on is dominated by LS+LD)				

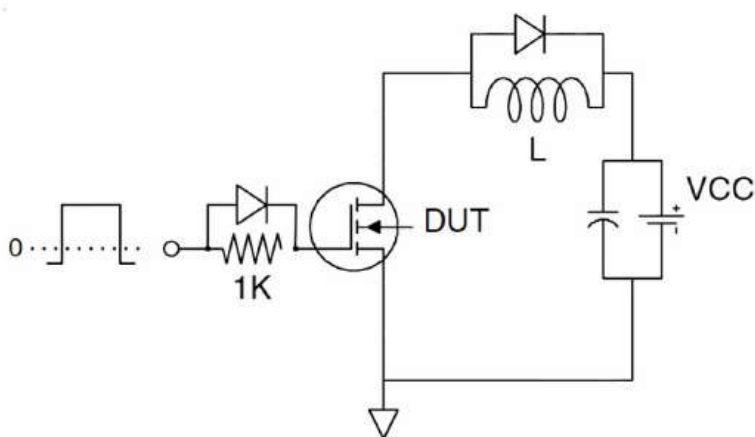
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④ Guaranteed by design, not subject to production
- ⑤ EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=15V,V<sub>G</sub>=10V,L=0.5mH,R<sub>G</sub>=25Ω

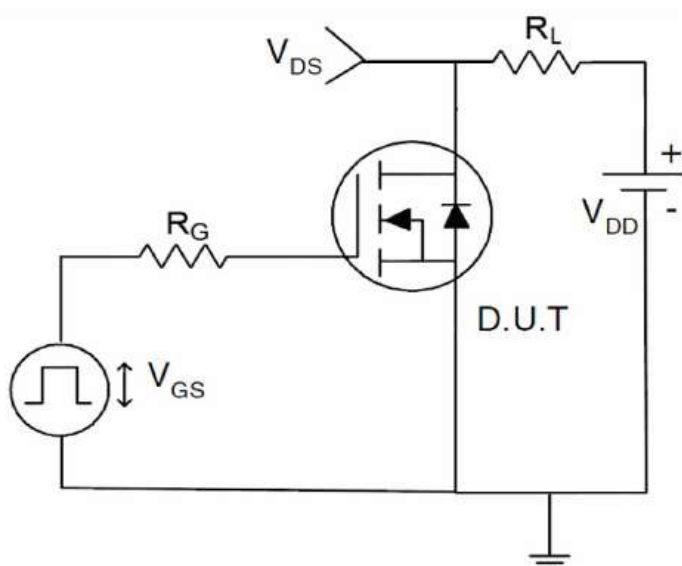
## Test circuit



EAS test Circuit

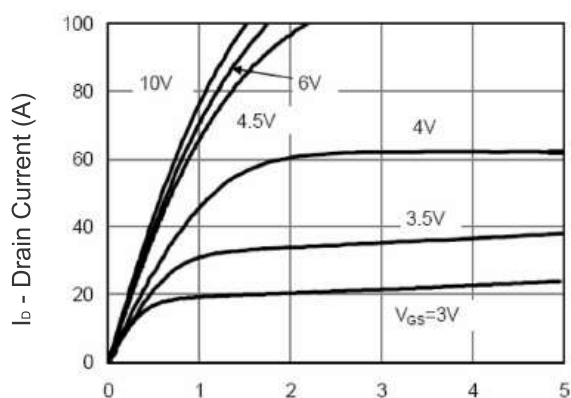


Gate charge test Circuit



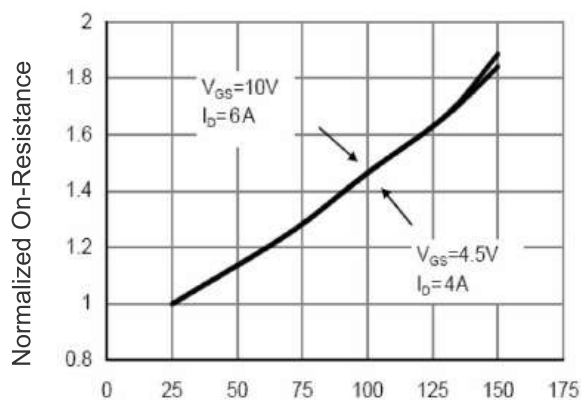
Switch Time Test Circuit

## Typical Electrical and Thermal Characteristics (Curves)



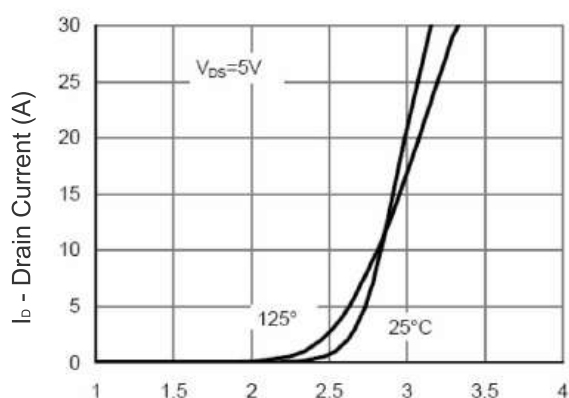
$V_{DS}$  Drain-Source Voltage (V)

Figure 1 Output Characteristics



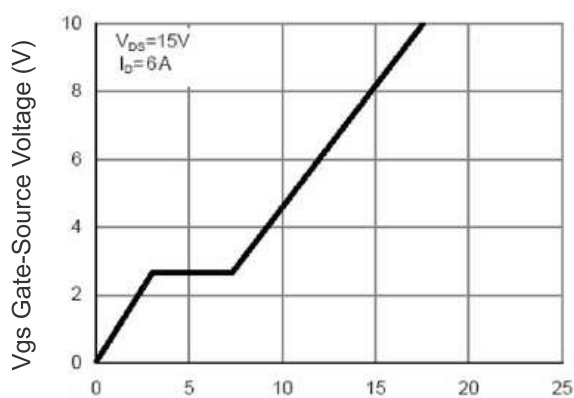
$T_J$  -Junction Temperature(°C)

Figure 4  $R_{DS(on)}$ -Junction Temperature



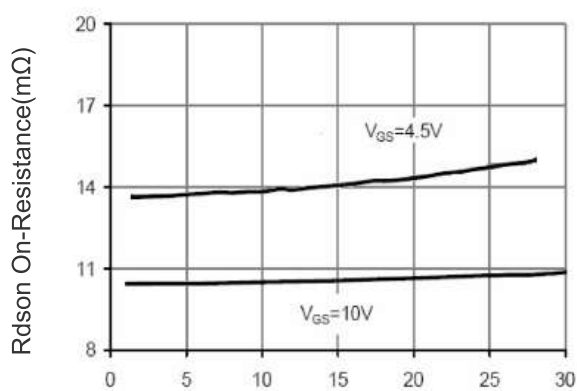
$V_{GS}$  Gate-Source Voltage (V)

Figure 2 Transfer Characteristics



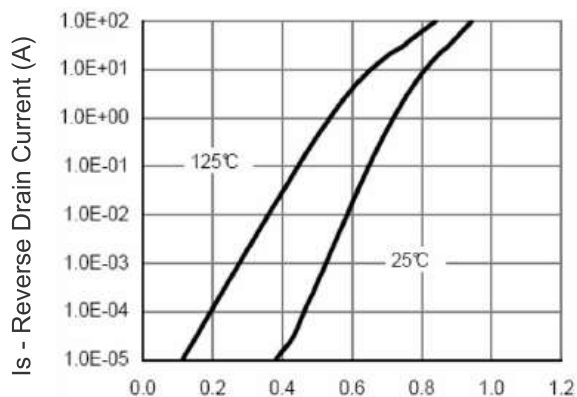
$Q_g$  Gate Charge (nC)

Figure 5 Gate Charge



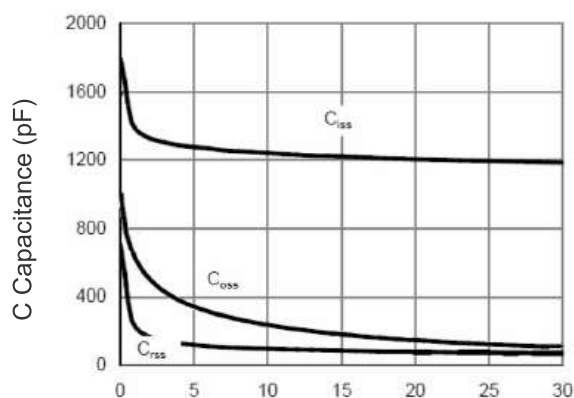
$I_D$  - Drain Current (A)

Figure 3  $R_{DS(on)}$ - Drain Current

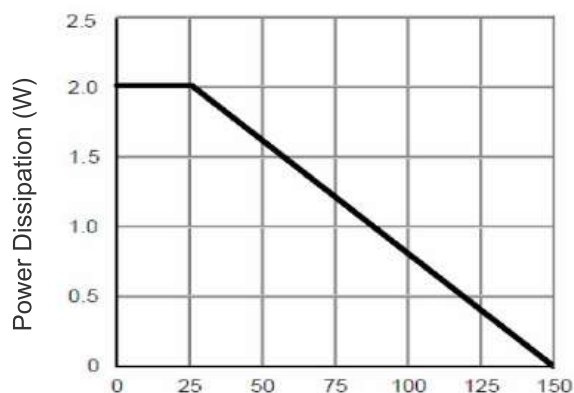


$V_{SD}$  Source-Drain Voltage (V)

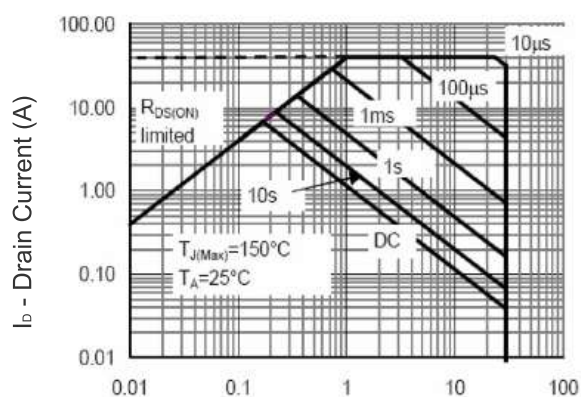
Figure 6 Source- Drain Diode Forward



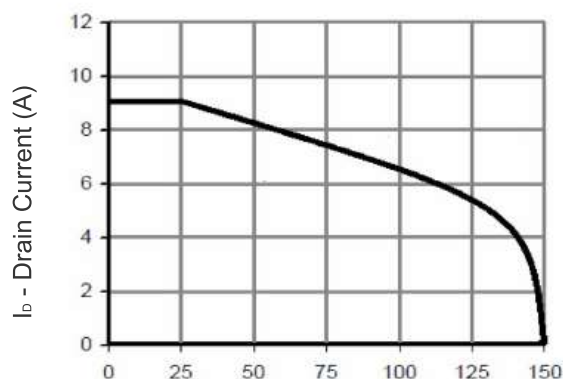
Vds Drain-Source Voltage (V)  
Figure 7 Capacitance vs Vds



TJ -Junction Temperature(°C)  
Figure 9 Power De-rating



Vds Drain-Source Voltage (V)  
Figure 8 Safe Operation Area



TJ -Junction Temperature(°C)  
Figure 10 Id Current De-rating

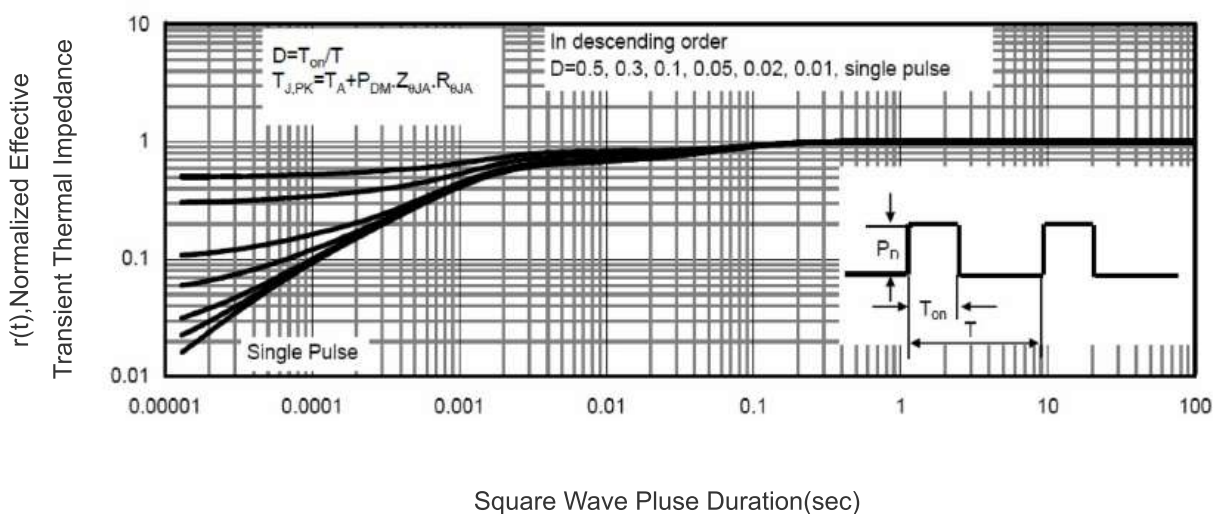
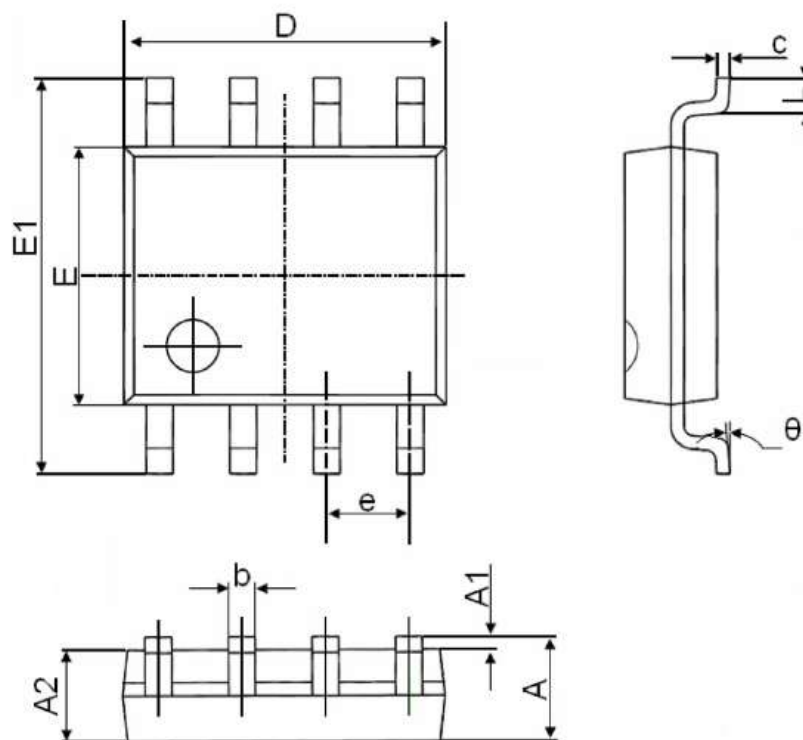


Figure 11 Normalized Maximum Transient Thermal Impedance

# SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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