MJ N-Channel Enhancement Mode Power MOSFET

Description

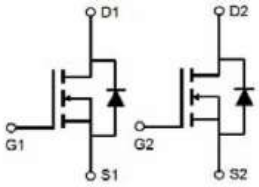
The MJ30ND07AS uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

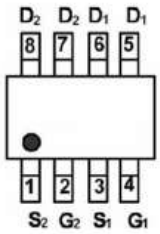
- V_{DS} =30V,I_D =7A
  $R_{DS(ON)}$ <23.5mΩ @ V_{GS}=10V
  $R_{DS(ON)}$ <28mΩ @ V_{GS}=4.5V
  $R_{DS(ON)}$ <34mΩ @ V_{GS}=2.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

Application

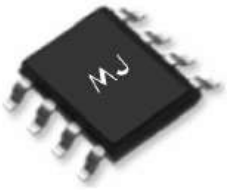
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



Marking and pin Assignment



SOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30ND07AS	MJ30ND07AS	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±12	V
Drain Current-Continuous	I_D	7	A
Drain Current-Continuous(T_C =100℃)	$I_{D(100℃)}$	4.95	A
Pulsed Drain Current	I_{DM}	30	A
Maximum Power Dissipation	P_D	2	W
Operating Junction and Storage Temperature Range	T_J ,T_{STG}	-55 To 150	℃

Thermal Characteristic

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance,Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62.5	85	℃/W

Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{DS}=±12V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS} ,I_D=250μA	0.6	0.9	1.2	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=7A	-	20.5	23.5	mΩ
		V_{GS}=4.5V, I_D=6A	-	22	28	mΩ
		V_{GS}=2.5V, I_D=5A	-	26.7	34	mΩ
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=7A	-	15	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{lss}	V_{DS}=15V,V_{GS}=0V F=1.0MHz	-	657.1	-	PF
Output Capacitance	C_{oss}		-	65	-	PF
Reverse Transfer Capacitance	C_{rss}		-	50	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=15V, R_L=2Ω V_{GS}=10V,R_G=3Ω	-	5	-	nS
Turn-on Rise Time	t_r		-	3	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	20	-	nS
Turn-Off Fall Time	t_f		-	4	-	nS
Total Gate Charge	Q_g	V_{DS}=15V,I_D=7A V_{GS}=4.5V	-	9.4	-	nC
Gate-Source Charge	Q_{gs}		-	1.1	-	nC
Gate-Drain Charge	Q_{gd}		-	2.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_S=7A	-	-	1.2	V
Diode Forward Current (Note 2)	I_S		-	-	7	A

Notes:

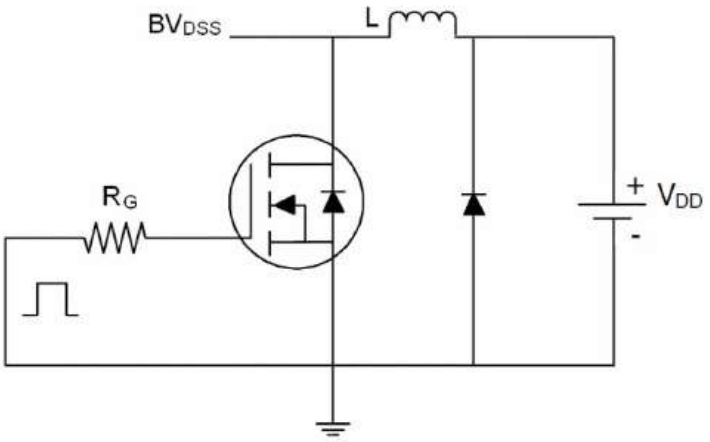
① Repetitive Rating: Pulse width limited by maximum junction temperature.

② The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.Surface Mounted on FR4 Board, t ≤ 10 sec. The current rating is based on the t ≤10s thermal resistance rating.

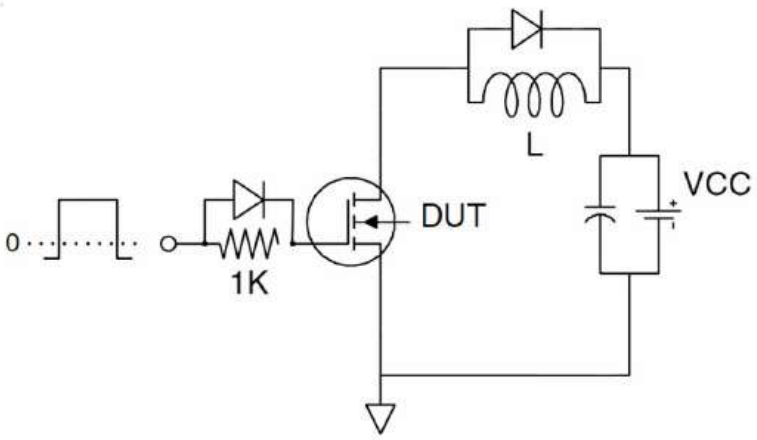
③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

④ Guaranteed by design, not subject to production

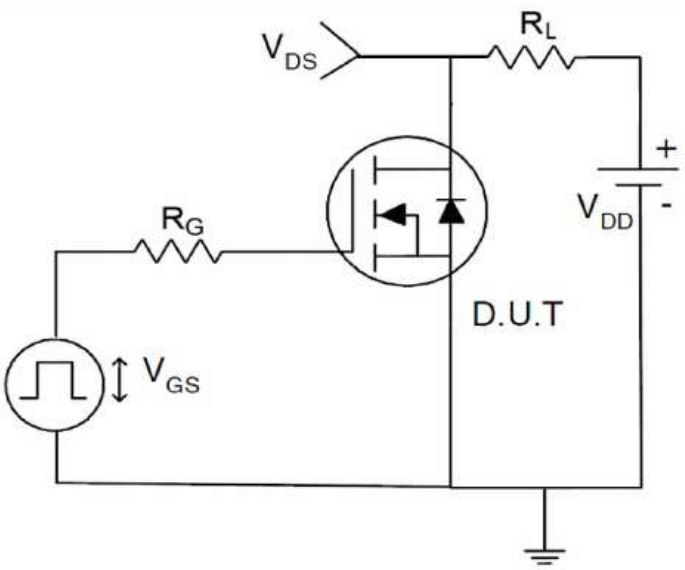
Test circuit



EAS test Circuit



Gate charge test Circuit



Switch Time Test Circuit

N-channel Typical Electrical and Thermal Characteristics (Curves)

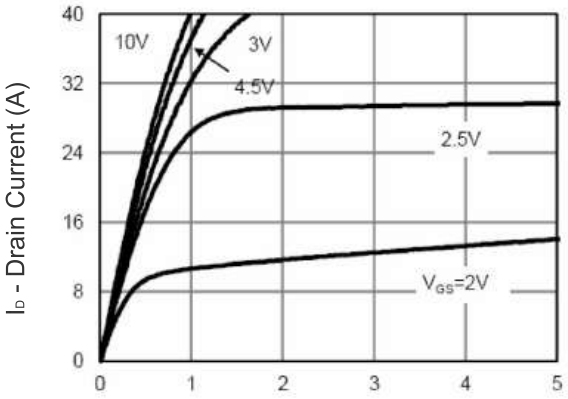


Figure 1 Output Characteristics

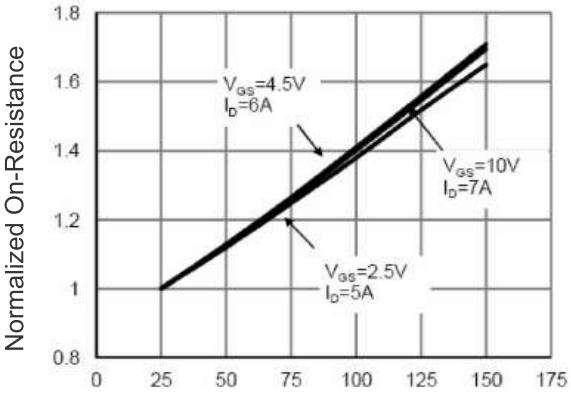


Figure 4 Rdson-Junction Temperature

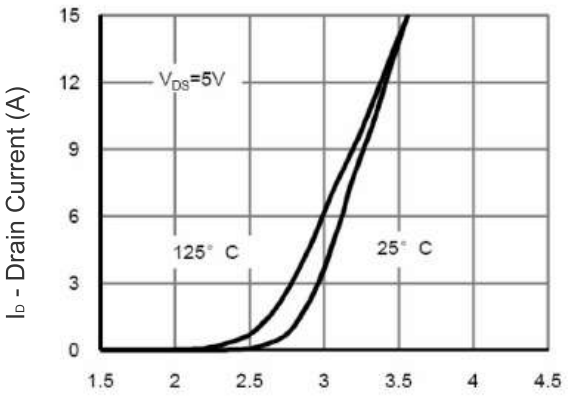


Figure 2 Transfer Characteristics

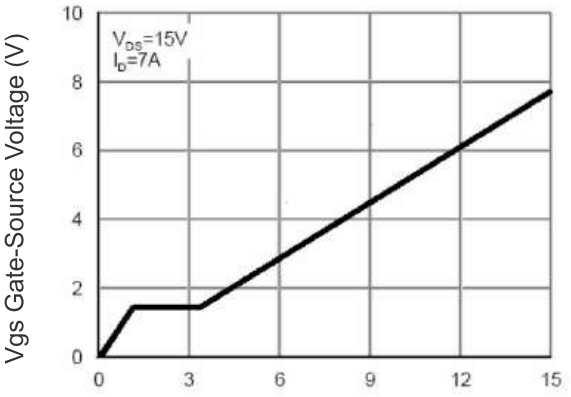


Figure 5 Gate Charge

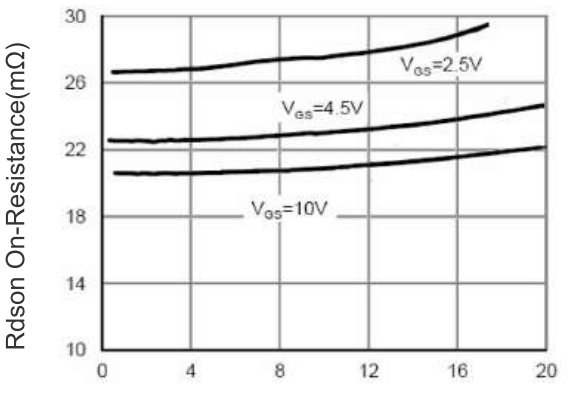


Figure 3 Rdson- Drain Current

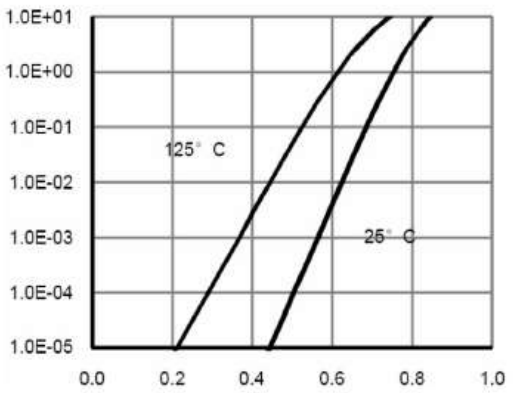
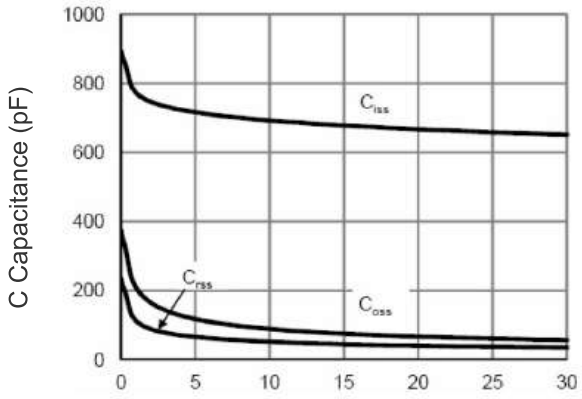
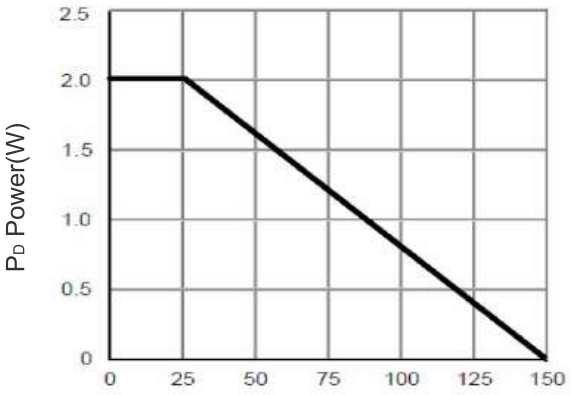


Figure 6 Source- Drain Diode Forward



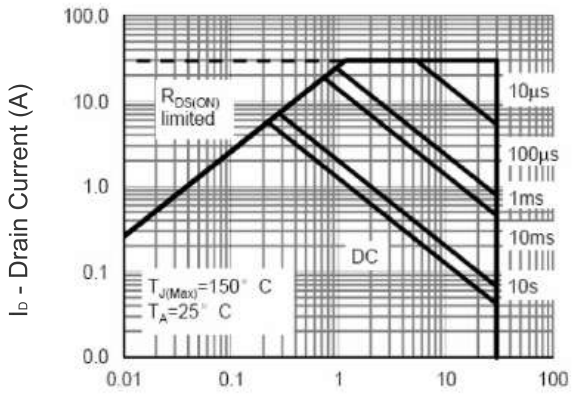
Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



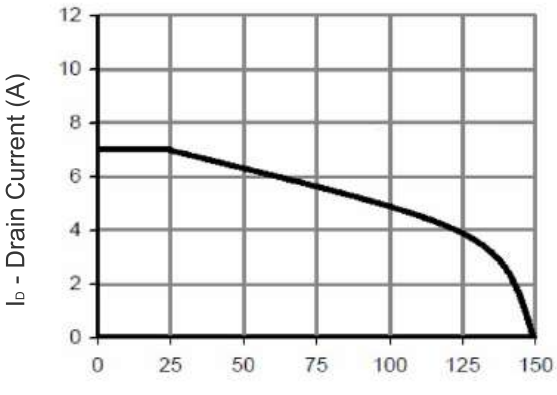
TJ -Junction Temperature(℃)

Figure 9 Power De-rating



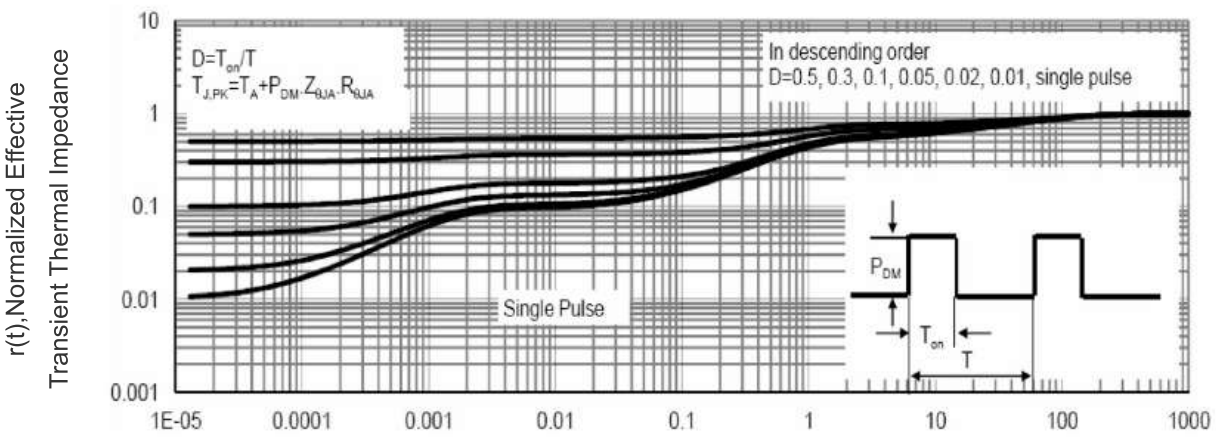
Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area



TJ -Junction Temperature(℃)

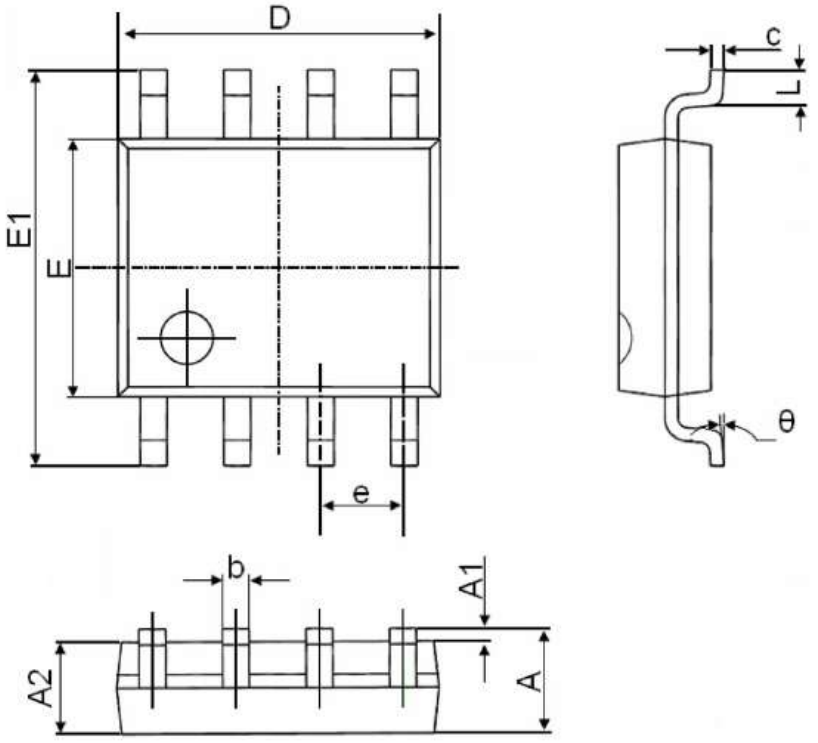
Figure 10 Current De-rating



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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