

# MJ N-Channel Enhancement Mode Power MOSFET

## Description

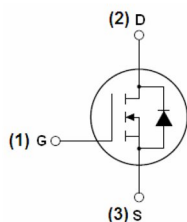
The MJ30H10G uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## General Features

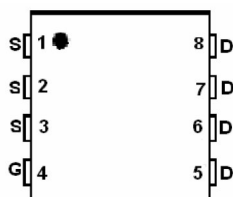
- ◆  $V_{DS}=30V, I_D=100A$   
 $R_{DS(ON)}<2.5m\Omega$  @  $V_{GS}=10V$   
 $R_{DS(ON)}<3.5m\Omega$  @  $V_{GS}=4.5V$
- ◆ High density cell design for ultra low  $R_{dson}$
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ Excellent package for good heat dissipation
- ◆ Special process technology for high ESD capability

## Application

- ◆ Power switching application
- ◆ Hard switched and high frequency circuits
- ◆ Uninterruptible power supply



Schematic diagram



Marking and pin assignment



DFN5X6-8L top view

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ30H10G	MJ30H10G	DFN 5X6 -8L	-	-	-

## Absolute Maximum Ratings ( $T_c=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	100	A
Drain Current-Continuous( $T_c=100^{\circ}C$ )	$I_{D(100^{\circ}C)}$	70.7	A
Pulsed Drain Current	$I_{DM}$	300	A
Maximum Power Dissipation	$P_D$	65	W
Derating factor		0.43	W/ $^{\circ}C$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^{\circ}C$

## Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	2.3	$^{\circ}C/W$
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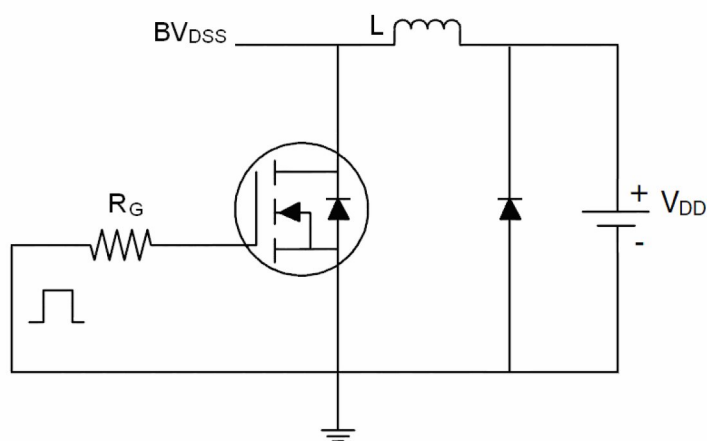
Electrical Characteristics (Tc =25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	35	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =±20V,V <sub>GS</sub> =0V	-	-	±100	nA
On Characteristics <sup>(Note 3)</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	1.2	1.7	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	1.9	2.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	2.9	3.5	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =10V,I <sub>D</sub> =20A	32	-	-	S
Dynamic Characteristics <sup>(Note 4)</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	5000	-	PF
Output Capacitance	C <sub>oss</sub>		-	1135	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	563	-	PF
Switching Characteristics <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V,R <sub>L</sub> =15Ω V <sub>GS</sub> =10V,R <sub>G</sub> =2.5Ω	-	26	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	24	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	91	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	39	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	38	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	9	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	13	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <sup>(Note 3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	-	1.2	V
Diode Forward Current <sup>(Note 2)</sup>	I <sub>S</sub>		-	-	100	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> =25°C, I <sub>F</sub> =20A di/dt=100A/μs <sup>(Note 3)</sup>	-	42	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>		-	39	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible(turn-on is dominated by LS+LD)				

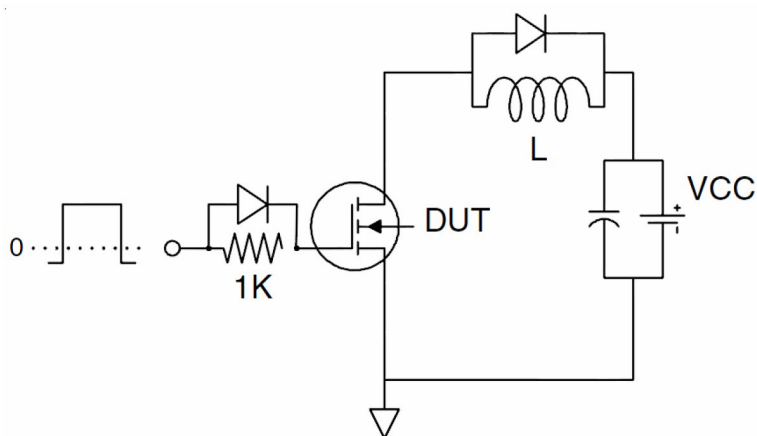
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t≤10sec.
- ③ Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%.
- ④ Guaranteed by design, not subject to production

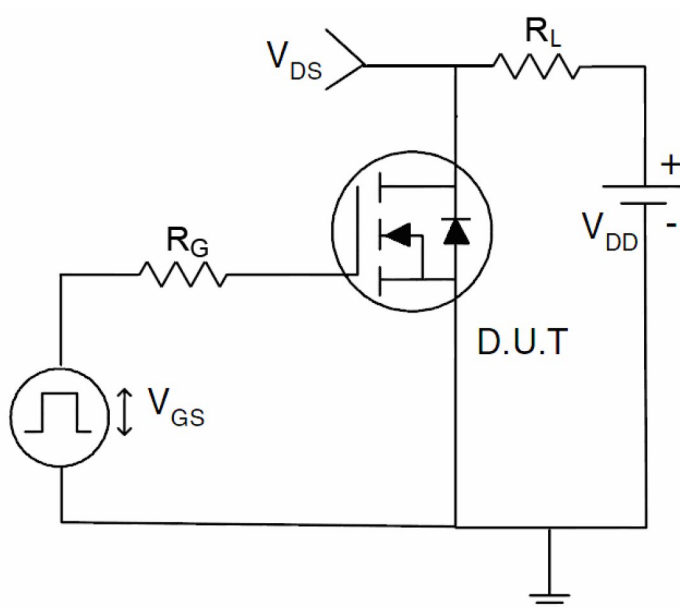
# Test circuit



EAS test Circuit

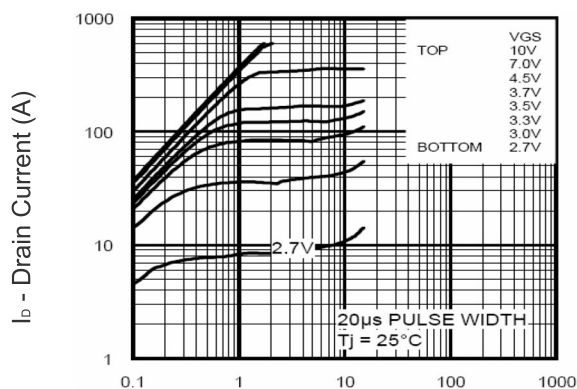


Gate charge test Circuit



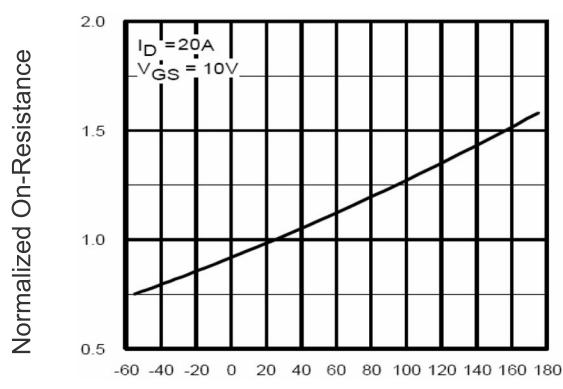
Switch Time Test Circuit

# Typical Electrical and Thermal Characteristics (Curves)



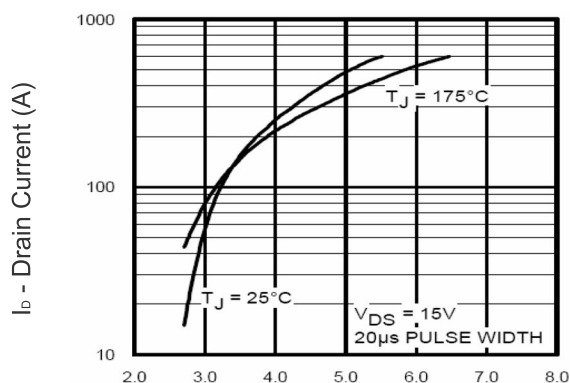
Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



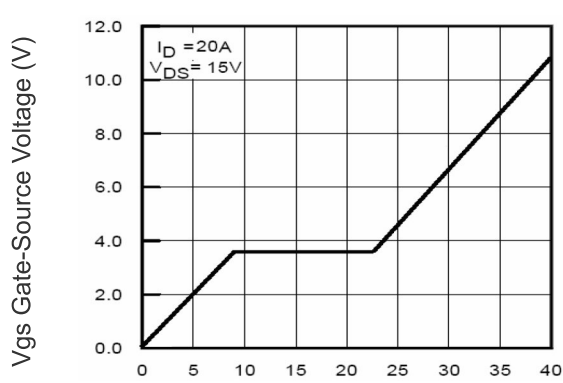
TJ -Junction Temperature(°C)

Figure 4 Rdson-Junction Temperature



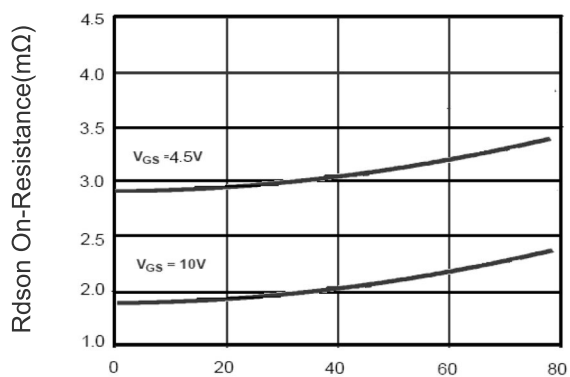
Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics



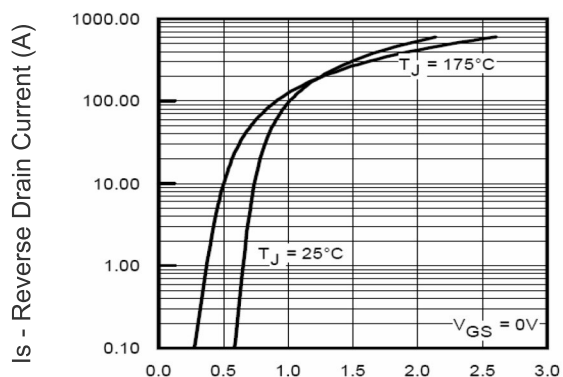
Qg Gate Charge (nC)

Figure 5 Gate Charge



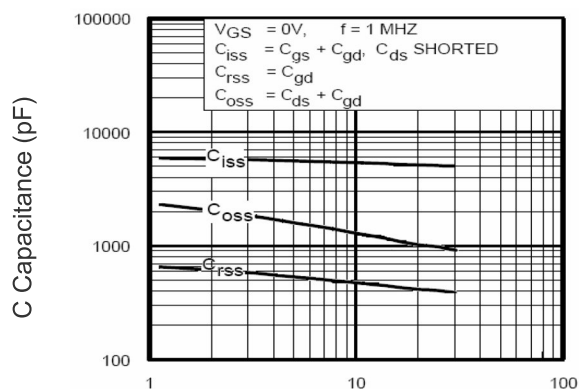
ID - Drain Current (A)

Figure 3 Rdson- Drain Current



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds

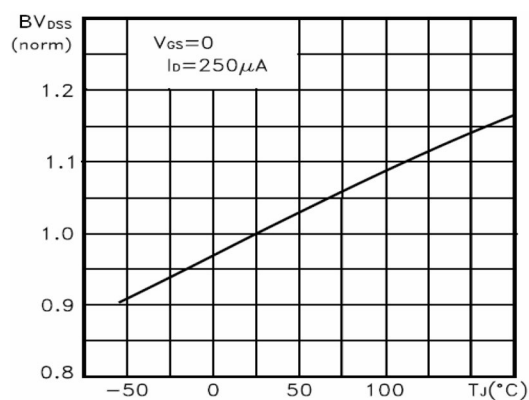
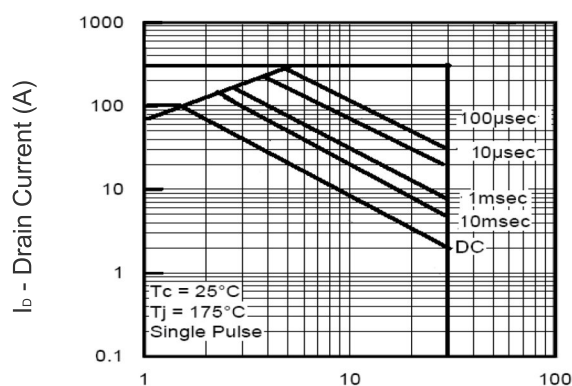

T<sub>J</sub> -Junction Temperature(°C)

Figure 9 BV<sub>DSS</sub> vs Junction Temperature


Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

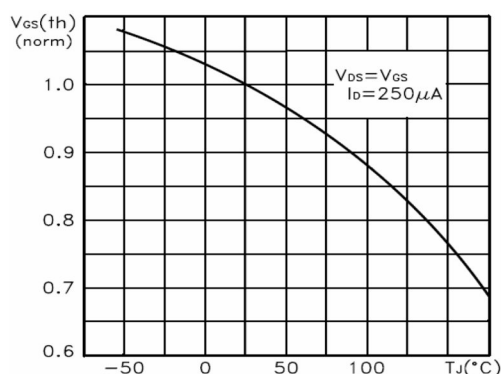
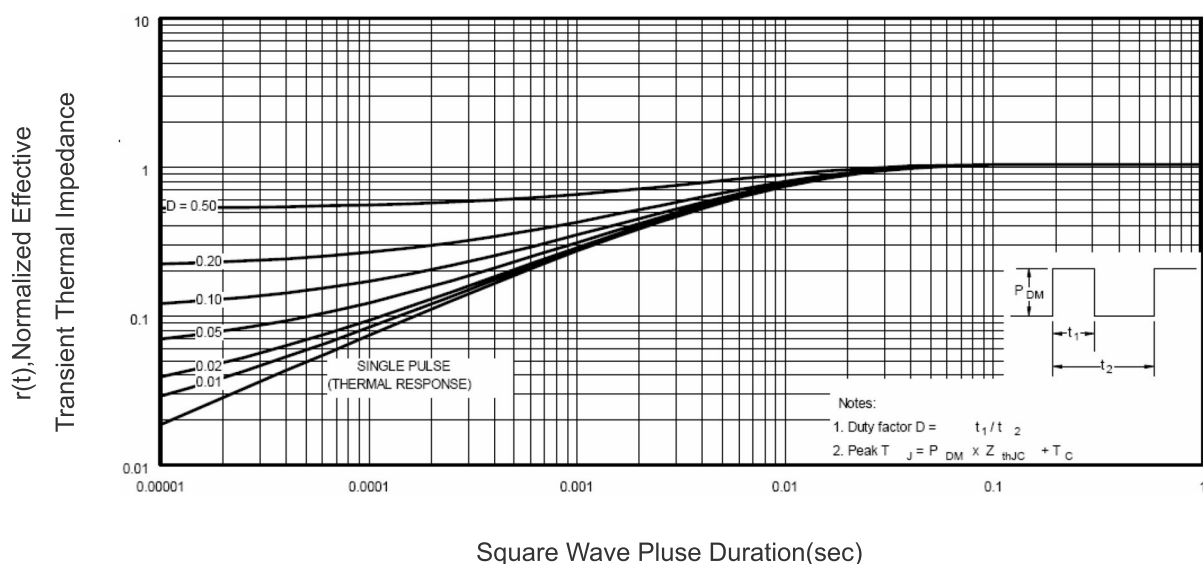
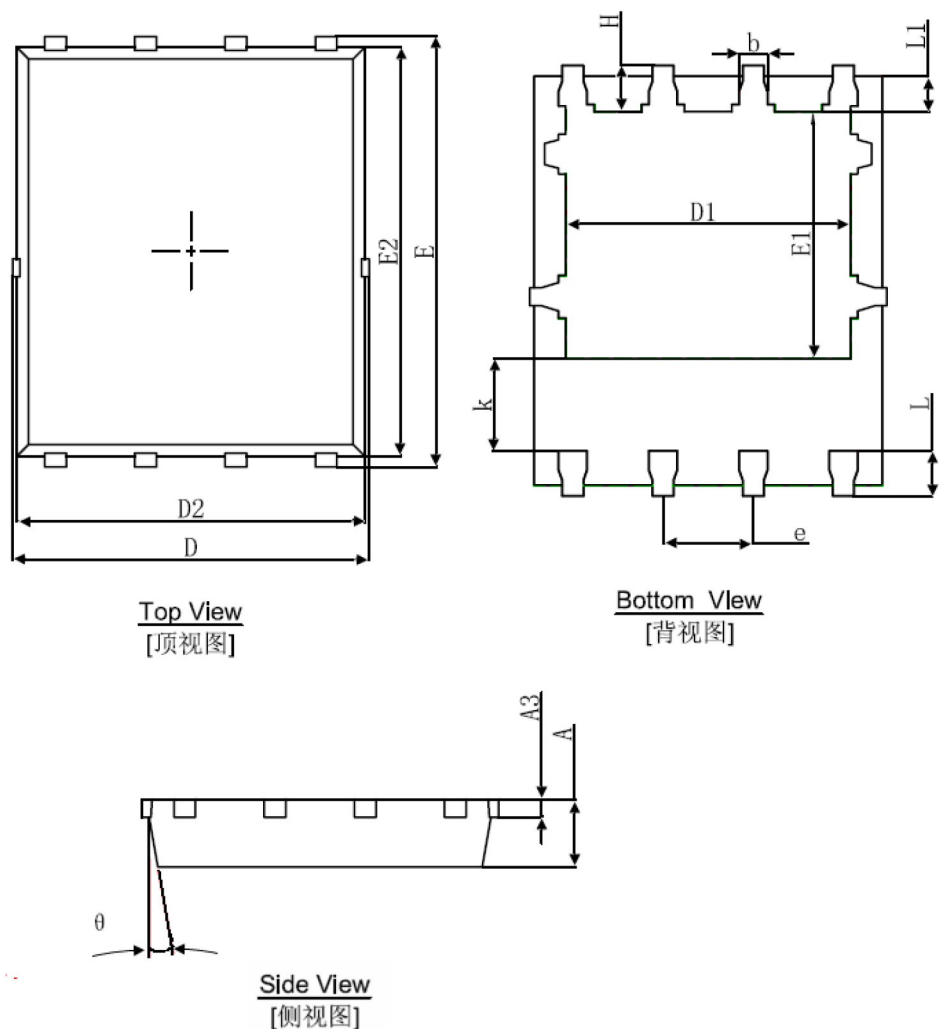

T<sub>J</sub> -Junction Temperature(°C)

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	8°	12°	8°	12°

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