



## MJ N-Channel Enhancement Mode Power MOSFET

### Description

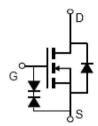
The MJ3011E uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.It is ESD protested.

#### **General Features**

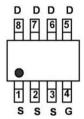
- ♦ V<sub>DS</sub> =30V,I<sub>D</sub> =11A R<sub>DS(ON)</sub> <10mΩ @ V<sub>GS</sub>=10V R<sub>DS(ON)</sub> <14mΩ @ V<sub>GS</sub>=4.5V ESD Rating: 2000V HBM
- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface Mount Package

### **Application**

- ◆ PWM application
- ◆ Load switch







Marking and pin assignment



SOP-8 top view

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3011E	MJ3011E	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	Vgs	±10	V
Drain Current-Continuous	lo	11	А
Drain Current-Pulsed (Note 1)	IDM	50	А
Maximum Power Dissipation	Po	2.5	W
Operating Junction and Storage Temperature Range	Тл,Тsтg	-55 To 150	°C

#### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2) ReJA 50	°C/W
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# Electrical Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BVpss	V <sub>GS</sub> =0V,I <sub>D</sub> =250µA	30	-	-	V
Zero Gate Voltage Drain Current	loss	Vps=30V,Vgs=0V	-	-	1	μΑ
Gate-Body Leakage Current	lgss	V <sub>DS</sub> =±10V,V <sub>DS</sub> =0V	-	-	±10	μA
On Characteristics (Note 3)	'					
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.0	1.5	2.0	V
Durin Oursea On Olate Bestelland		Vgs=10V, ID=8A	-	7	10	mΩ
Drain-Source On-State Resistance	Rds(on)	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	-	10	14	mΩ
Forward Transconductance	grs	V <sub>DS</sub> =10V,I <sub>D</sub> =11A	25	-	-	S
Dynamic Characteristics (Note 4)						'
Input Capacitance	Ciss		-	1155	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V F=1.0MHz	-	260	-	PF
Reverse Transfer Capacitance	Crss		-	95	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	10	-	nS
Turn-on Rise Time	tr	Vdd=15V,RL=2.2Ω	-	16	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}=5V,R_{GEN}=3\Omega$	-	40	-	nS
Turn-Off Fall Time	tr		-	10.8	_	nS
Total Gate Charge	Qg		-	17.5	-	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =15V,I <sub>D</sub> =8A V <sub>GS</sub> =4.5V	-	4.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.5	-	nC
Drain-Source Diode Characteristics	I	<u> </u>	1	1	<u> </u>	
Diode Forward Voltage (Note 3)	VsD	V <sub>GS</sub> =0V,I <sub>S</sub> =1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	11	А

#### Notes

- 1) Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board,  $t \le 10$  sec.
- ③ Pulse Test: Pulse Width ≤ 300 $\mu$ s, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production



## Typical Electrical and Thermal Characteristics

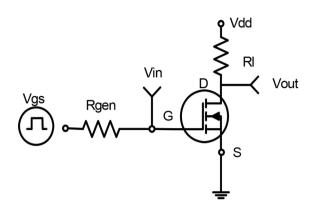


Figure 1 Switching Test Circuit

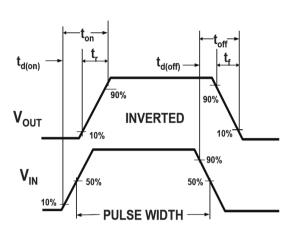


Figure 2 Switching Waveforms

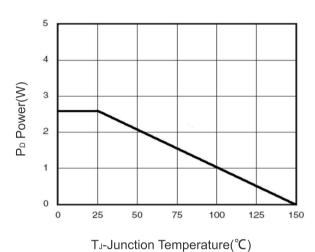
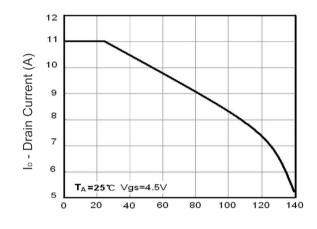


Figure 3 Power Dissipation



T<sub>J</sub>-Junction Temperature(°C)
Figure 4 Drain Current

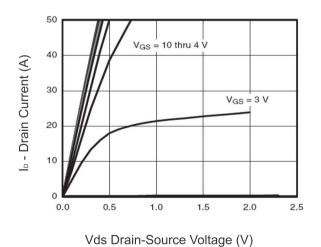


Figure 5 Output Characteristics

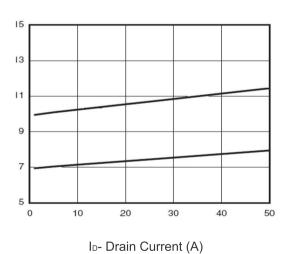
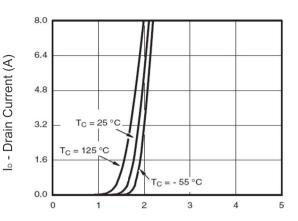
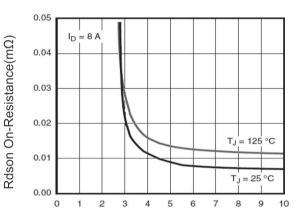


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V) Figure 9 Rdson vs Vgs

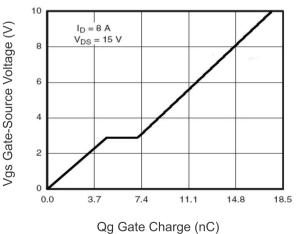
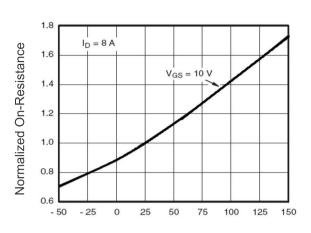
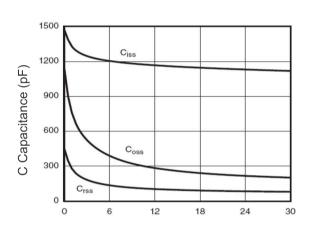


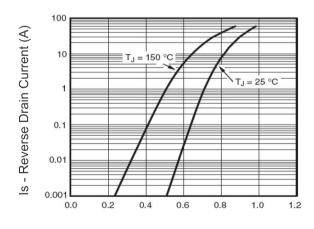
Figure 11 Gate Charge



T<sub>J</sub>-Junction Temperature(°C)
Figure 8 Drain-Source On-Resistance

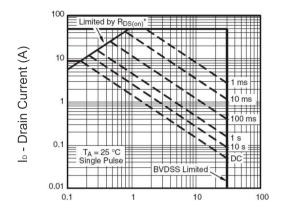


Vds Drain-Source Voltage (V)
Figure 10 Capacitance vs Vds



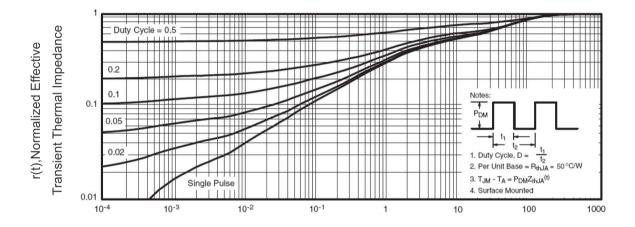
Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area



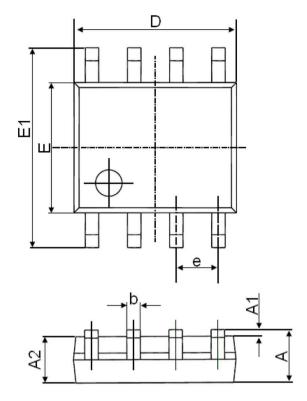
Square Wave Pluse Duration(sec)

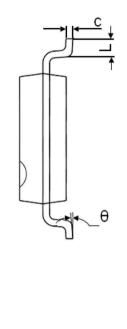
Figure 14 Normalized Maximum Transient Thermal Impedance





# SOP-8 Package Information





Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	





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