



MJ P-Channel Enhancement Mode Power MOSFET

Description

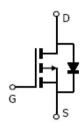
The MJ3007S uses advanced trench technology and design to provide excellent $R_{\text{DS}(\text{ON})}$ with low gate charge. It can be used in load switch and battery protection applications.

General Features

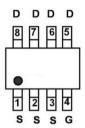
- $ightharpoonup V_{DS} = -30 \text{V}, I_D = -6.5 \text{A}$ $R_{DS(ON)} < 42 \text{m}\Omega$ @ $V_{GS} = -10 \text{V}$ $R_{DS(ON)} < 72 \text{m}\Omega$ @ $V_{GS} = -4.5 \text{V}$
- ♦ High density cell design for ultra low Rdson
- ◆ Fully characterized avalanche voltage and current

Application

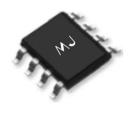
- ◆ Load switch
- battery protection







Marking and pin Assignment



SOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ3007S	MJ3007S	SOP-8	Ø330mm	12mm	4000 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	lo	-6.5	А
Drain Current-Continuous(Tc =100°C)	ID(100°C)	-4.5	А
Pulsed Drain Current	Ідм	-30	А
Maximum Power Dissipation	Po	3.1	W
Operating Junction and Storage Temperature Range	Тл ,Тѕтс	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	RөJA	40	°C/W
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Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Uni
Off Characteristics	'					
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V,I _D =-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =-30V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	lgss	V _{DS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =-250µA	-1.3	-1.65	-2.5	V
		V _{GS} =-10V,I _D =-6.5A	-	30	42	mΩ
Drain-Source On-State Resistance	Rds(on)	V _{GS} =-4.5V,I _D =-5A	-	53	72	mΩ
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-6.5A	14	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	Clss		-	660	-	PF
Output Capacitance	Coss	V _{DS} =-15V,V _{GS} =0V F=1.0MHz	-	100	-	PF
Reverse Transfer Capacitance	Crss		-	65	-	PF
Switching Characteristics (Note 4)	-					
Turn-on Delay Time	t _{d(on)}		-	7.5	-	nS
Turn-on Rise Time	tr	VDD=-15V,ID=-4A	-	5.5	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =-10V,R _{GEN} =3Ω	-	19	_	nS
Turn-Off Fall Time	tr		-	7	-	nS
Total Gate Charge	Qg		-	9.2	-	nC
Gate-Source Charge	Qgs	V _{DS} =-15V,I _D =-6.5A V _{GS} =-10V	-	1.6	-	nC
Gate-Drain Charge	Q _{gd}		-	2.2	_	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	Vsp	V _{GS} =0V,I _S =-6.5A	_	_	-1.2	V
Diode Forward Current (Note 2)	ls		_	_	-6.5	А

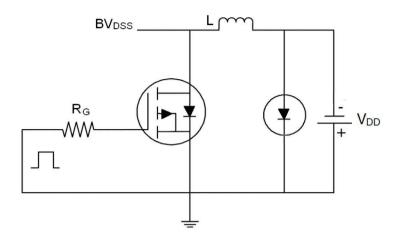
Notes

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3 Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4 Guaranteed by design, not subject to production

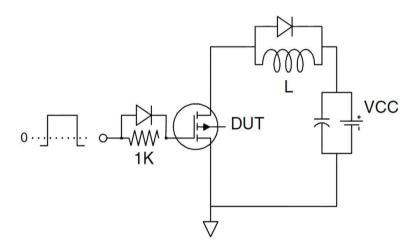




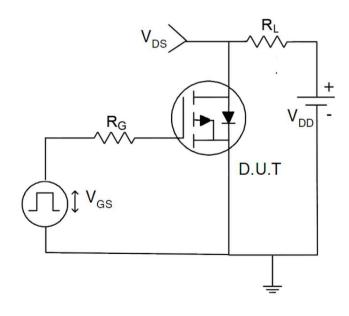
Test circuit



Eas test Circuit



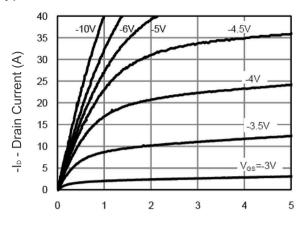
Gate charge test Circuit

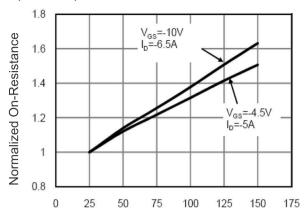


Switch Time Test Circuit



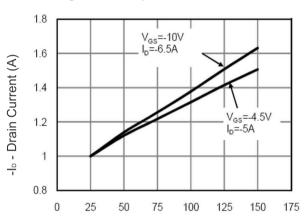
Typical Electrical and Thermal Characteristics (Curves)



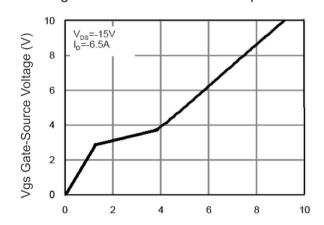


-Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

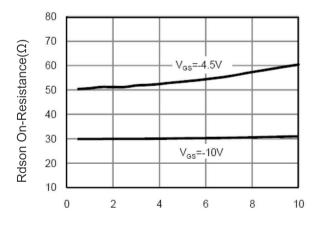


T_J -Junction Temperature(°C) Figure 4 Rdson-Junction Temperature

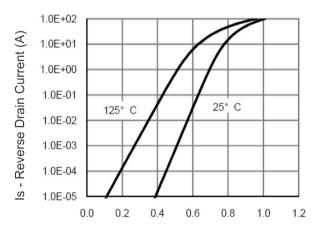


-Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics



Qg Gate Charge (nC) Figure 5 Gate Charge

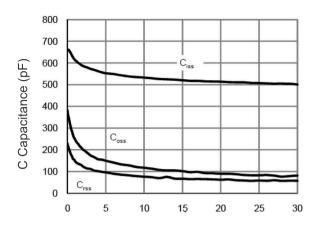


ID - Drain Current (A)

Figure 3 Rdson- Drain Current

Vsd Source-Drain Voltage (V) Figure 6 Source- Drain Diode Forward

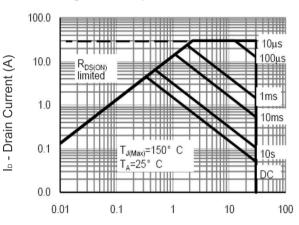




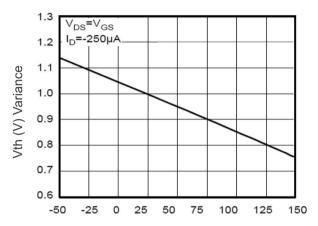
BVDSS V_{GS}=0 I_D=-250μA (norm) 1.2 Normalized BVdss 1.1 1.0 0.9 0.8 -50 50 100 TJ(°C)

Vds Drain-Source Voltage (V)

Figure 7 Capacitance vs Vds



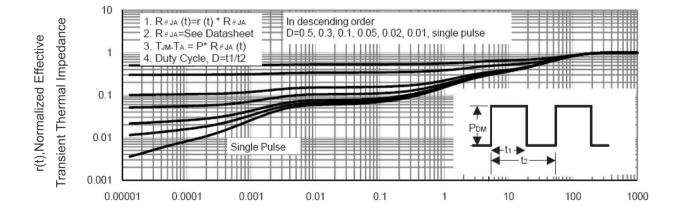
T_J -Junction Temperature(°C) Figure 9 BVDSS vs Junction Temperature



Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

T_J -Junction Temperature(°C) Figure 10 V_{GS(th)} vs Junction Temperature



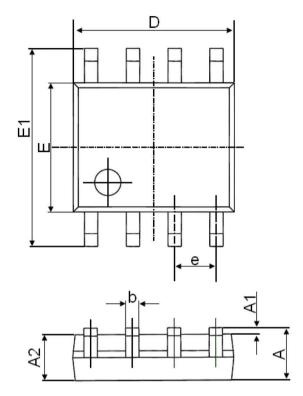
Square Wave Pluse Duration(sec)

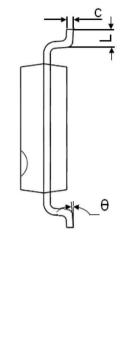
Figure 11 Normalized Maximum Transient Thermal Impedance





SOP-8 Package Information





Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	





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