



# MJ P-Channel Enhancement Mode Power MOSFET

### Description

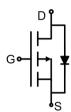
The NCE2301F uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications

#### **General Features**

- ♦ V<sub>DS</sub> =-20V,I<sub>D</sub> =-2A R<sub>DS(ON)</sub> <150mΩ @ V<sub>GS</sub> =-2.5V R<sub>DS(ON)</sub> <120mΩ @ V<sub>GS</sub> =-4.5V
- ◆ High power and current handing capability
- ◆ Lead free product is acquired
- ◆ Surface Mount Package

#### **Application**

- ◆ PWM applications
- Load switch







Schematic diagram

Marking and pin Assignment

SOT-23 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2301F	MJ2301F	SOT23-3L	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (Tc =25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-20	V
Gate-Source Voltage	VDS	±12	V
Drain Current-Continuous	lo	-2.0	А
Pulsed Drain Current (Note 1)	IDM	-10	А
Maximum Power Dissipation	Po	0.7	W
Operating Junction and Storage Temperature Range	Тл,Тsтg	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note 2)	RөJA	178	°C/W	
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## Electrical Characteristics (TA =25°Cunless otherwise noted)

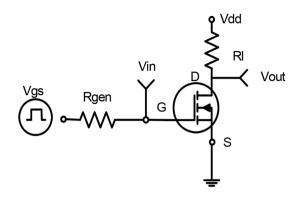
Parameter	Symbol	Condition	Min	Тур	Max	Uni
Off Characteristics	'					
Drain-Source Breakdown Voltage	BVDSS	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	-20	-	-	V
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	lgss	V <sub>DS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.4	-0.7	-1	V
Drain-Source On-State Resistance	Rds(on)	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	88	120	mΩ
Drain-Source On-State Resistance	TADS(ON)	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1.8A	-	120	150	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-2A	4	-	-	S
Dynamic Characteristics (Note 4)	'					
Input Capacitance	Clss	V <sub>DS</sub> =-10V,V <sub>GS</sub> =0V, F=1.0MHz	-	285	-	PF
Output Capacitance	Coss		-	58	-	PF
Reverse Transfer Capacitance	Crss		-	32	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	9.8	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =-10V, ,RL =5Ω	-	4.9	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-4.5V,R <sub>GEN</sub> =3 $\Omega$	-	20.5	-	nS
Turn-Off Fall Time	tr		-	7	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =-10V,I <sub>D</sub> =-2A, V <sub>GS</sub> =-4.5V	-	2.9	-	nC
Gate-Source Charge	Qgs		-	0.45	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	0.75	-	nC
Drain-Source Diode Characteristics	1			1		1
Diode Forward Voltage (Note 3)	VsD	V <sub>GS</sub> =0V,I <sub>S</sub> =-2A	-	-	-1.2	V
Diode Forward Current (Note 2)	ls		_	-	-2.0	А

#### Notes:

- ${\small \textcircled{1}} \ \ \mathsf{Repetitive} \ \ \mathsf{Rating:} \ \ \mathsf{Pulse} \ \ \mathsf{width} \ \ \mathsf{limited} \ \ \mathsf{by} \ \ \mathsf{maximum} \ \ \mathsf{junction} \ \ \mathsf{temperature}.$
- ② Surface Mounted on FR4 Board,  $t \le 10$  sec.
- ③ Pulse Test: Pulse Width ≤ 300 $\mu$ s, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production



## Typical Electrical and Thermal Characteristics



 $t_{d(on)}$   $t_{d(off)}$   $t_{d$ 

Figure 1 Switching Test Circuit

Figure 2 Switching Waveforms

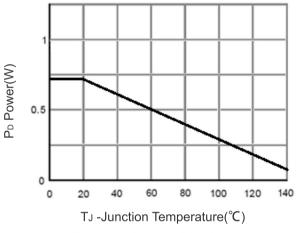
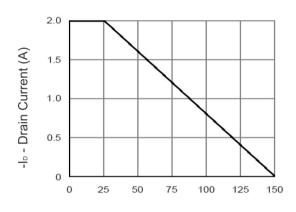
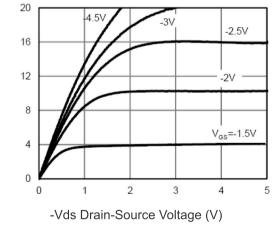


Figure 3 Power Dissipation



T<sub>J</sub> -Junction Temperature(°C) Figure 4 Drain Current



Io - Drain Current (A)

Figure 5 Output Characteristics

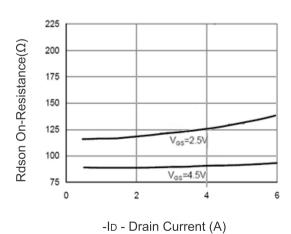


Figure 6 Drain-Source On-Resistance



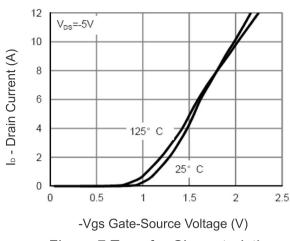
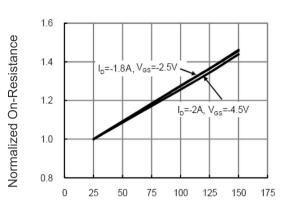


Figure 7 Transfer Characteristics



T

J -Junction Temperature(°C)

Figure 8 Drain-Source On-Resistance

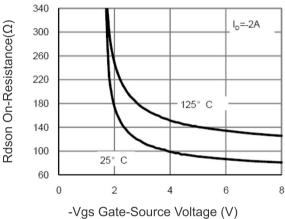
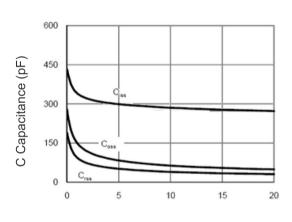


Figure 9 Rdson vs Vgs



-Vds Drain-Source Voltage (V)
Figure 10 Capacitance vs Vds

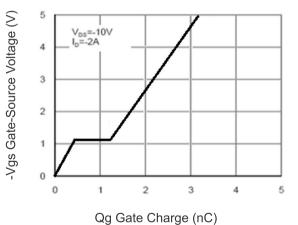


Figure 11 Gate Charge

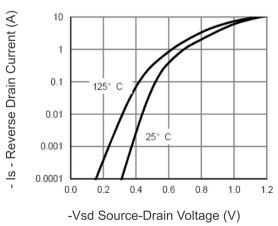
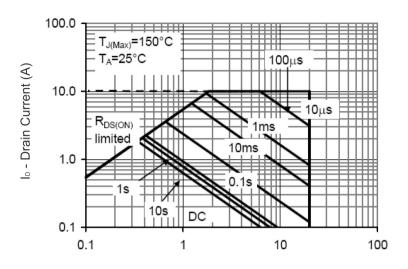


Figure 12 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 13 Safe Operation Area

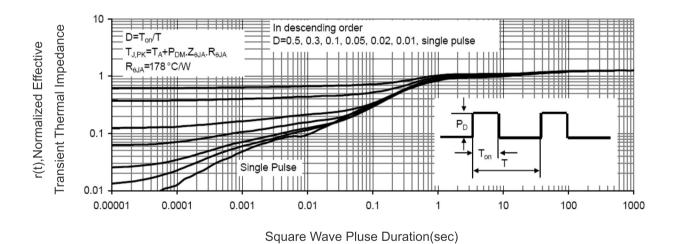
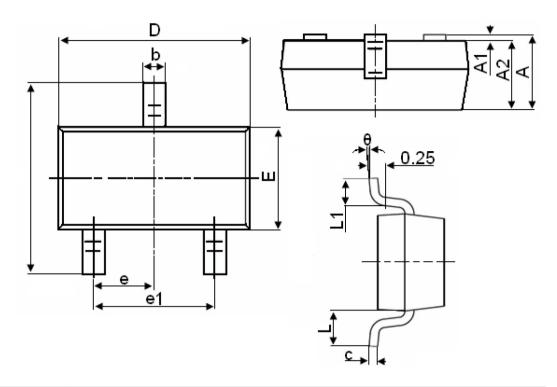


Figure 14 Normalized Maximum Transient Thermal Impedance





## SOT-23 Package Information



Symbol	Dimensions in Millimeters			
Symbol	MIN.	MAX.		
А	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е	0.950TYP			
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

### Notes:

- ① All dimensions are in millimeters.
- 2 Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3 Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4 Dimension L is measured in gauge plane.
- ⑤ Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.





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