



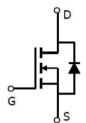
# MJ N-Channel Enhancement Mode Power MOSFET

## Description

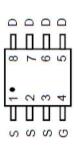
The MJ2025S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **General Features**

- V<sub>DS</sub> =20V,I<sub>D</sub> =25A
  R<sub>DS(ON)</sub> <4mΩ @ V<sub>GS</sub>=4.5V
  R<sub>DS(ON)</sub> <6mΩ @ V<sub>GS</sub>=2.5V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current



Schematic diagram



Application

DC/DC Converter

Battery protection



en la

SOP-8 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ2025S	MJ2025S	SOP-8	Ø330mm	12mm	2500 units

## Absolute Maximum Ratings (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	lо	25	А
Drain Current-Continuous(T₄ =100°C)	ID(100℃)	17.7	А
Pulsed Drain Current	lдм	140	А
Maximum Power Dissipation	PD	2.5	W
Operating Junction and Storage Temperature Range	Тл,Тѕтс	-55 To 150	°C

### Thermal Characteristic





## Electrical Characteristics (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	I	1	1			
Drain-Source Breakdown Voltage	BVDSS	V <sub>GS</sub> =0V,I <sub>D</sub> =250µA	20	-	-	V
Zero Gate Voltage Drain Current	loss	VDS=20V,VGS=0V	-	-	1	μA
Gate-Body Leakage Current	lgss	VDS=±20V,VDS=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	Vos=Vgs ,Id=250µA	0.5	0.75	1.2	V
		Vgs=4.5V, Id=20A	-	3.5	4	mΩ
Drain-Source On-State Resistance	Rds(on)	Vgs=2.5V, Id=18A	-	4.2	6	mΩ
Forward Transconductance	<b>g</b> FS	Vds=5V,Id=20A	60	-	-	S
Dynamic Characteristics (Note 4)		1	1			1
Input Capacitance	Cliss		-	5300	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V F=1.0MHz	-	785	-	PF
Reverse Transfer Capacitance	Crss		-	629	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	td(on)		-	10	-	nS
Turn-on Rise Time	tr	Vdd=10V,RL=0.5Ω	-	12	-	nS
Turn-Off Delay Time	td(off)	$V_{GS}$ =4.5V,R <sub>GEN</sub> =3 $\Omega$	-	50	-	nS
Turn-Off Fall Time	tr		-	20	-	nS
Total Gate Charge	Qg		-	64.9	-	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =10V,I <sub>D</sub> =20A V <sub>GS</sub> =4.5V	-	6.5	-	nC
Gate-Drain Charge	Qgd		-	13.8		nC
Drain-Source Diode Characteristics	I	1	1	1	I	1
Diode Forward Voltage (Note 3)	Vsd	V <sub>GS</sub> =0V,Is=25A	-	-	1.2	V
Diode Forward Current (Note 2)	ls		-	-	25	A

#### Notes:

① Repetitive Rating: Pulse width limited by maximum junction temperature.

② Surface Mounted on FR4 Board,  $t \le 10$  sec.

3 Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

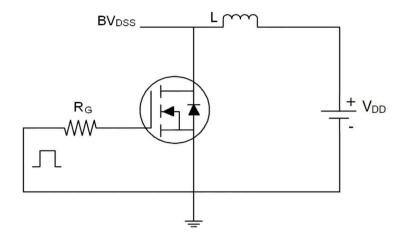
④ Guaranteed by design, not subject to production



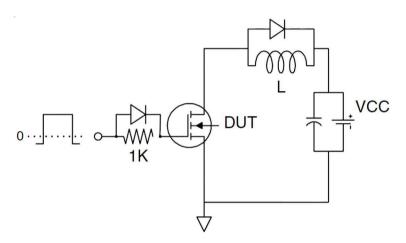




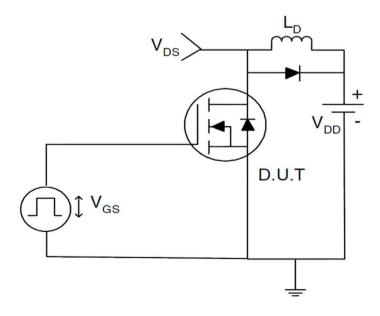
### Test circuit







# Gate charge test Circuit



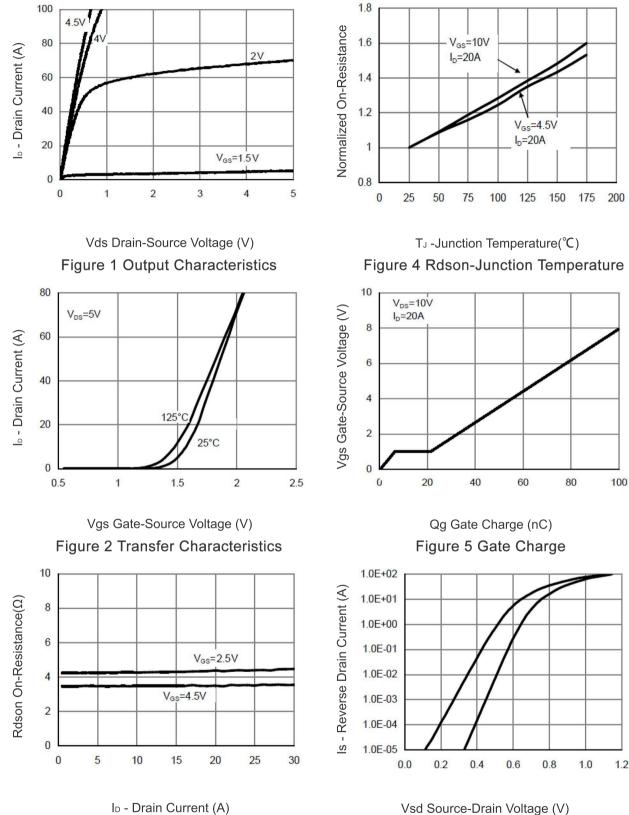
Switch Time Test Circuit







# Typical Electrical and Thermal Characteristics (Curves)



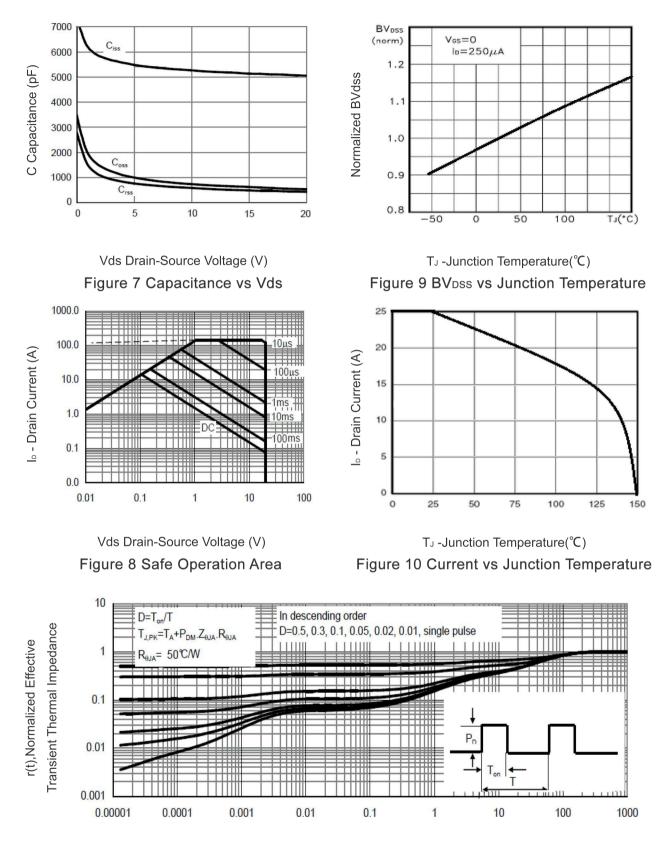
Vsd Source-Drain Voltage (V) Figure 6 Source- Drain Diode Forward

Figure 3 Rdson- Drain Current







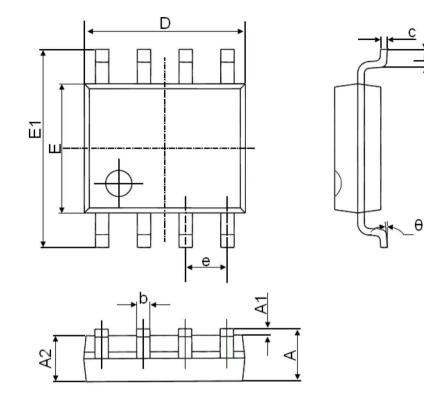


Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance





# SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	





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