



MJ P-Channel Enhancement Mode Power MOSFET

Description

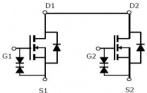
The MJ2008E uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications .It is ESD protested.

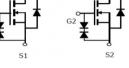
General Features

- ♦ V_{DS} =20V.I_D =6A $R_{DS(ON)}$ <30m Ω @ Vgs =2.5V $R_{DS(ON)}$ <24m Ω @ V_{GS} =4.5V ESD Rating: 2000V HBM
- High power and current handing capability
- Lead free product is acquired
- Surface Mount Package

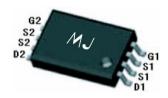
Application

- PWM application
- Load switch









Schematic diagram

Marking and pin Assignment

TSSOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity	
2008E	MJ2008E	TSSOP-8	Ø330mm	12mm	3000 units	

Absolute Maximum Ratings (Tc =25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	20	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	lo	6	А
Pulsed Drain Current (Note 1)	IDM	30	А
Maximum Power Dissipation	Po	1.5	W
Operating Junction and Storage Temperature Range	Тл,Тsтg	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	RөJA	83.3	°C/W
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Electrical Characteristics (TA =25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Uni
Off Characteristics	'	1				
Drain-Source Breakdown Voltage	BVpss	V _{GS} =0V I _D =250µA	20	-	-	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =20V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	Igss	V _{DS} =±10V,V _{DS} =0V	-	-	±10	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250μA	0.45	0.7	1.0	V
Drain-Source On-State Resistance	Rds(on)	V _{GS} =4.5V, I _D =6A	-	17	24	mΩ
	NDS(ON)	V _{GS} =2.5V, I _D =5A	-	22	30	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =6A	-	20	-	s
Dynamic Characteristics (Note 4)	1				ı	
Input Capacitance	Clss	V _{DS} =10V,V _{GS} =0V, F=1.0MHz	-	650	-	PF
Output Capacitance	Coss		-	140	-	PF
Reverse Transfer Capacitance	Crss		-	60	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	$V_{DD} = 10V, R_L = 1.5\Omega$ $V_{GS} = 5V, R_{GEN} = 3\Omega$	-	0.5	-	nS
Turn-on Rise Time	tr		-	1	-	nS
Turn-Off Delay Time	t _{d(off)}		-	12	-	nS
Turn-Off Fall Time	tf		-	4	-	nS
Total Gate Charge	Qg	V _{DS} =10V,I _D =6A, V _{GS} =4.5V	-	8	-	nC
Gate-Source Charge	Qgs		-	2.5	-	nC
Gate-Drain Charge	Qgd	-	-	3	-	nC
Drain-Source Diode Characteristics		I	1	1	I	ı
Diode Forward Voltage (Note 3)	VsD	Ves =0V,Is =1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		_	-	6	А

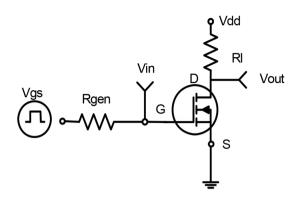
Notes:

- $\textcircled{1} \ \ \mathsf{Repetitive} \ \ \mathsf{Rating:} \ \ \mathsf{Pulse} \ \ \mathsf{width} \ \ \mathsf{limited} \ \ \mathsf{by} \ \ \mathsf{maximum} \ \ \mathsf{junction} \ \ \mathsf{temperature}.$
- ② Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3 Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- ④ Guaranteed by design, not subject to production





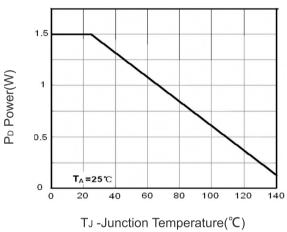
Typical Electrical and Thermal Characteristics



 $t_{d(on)}$ $t_{d(off)}$ t_{d

Figure 1 Switching Test Circuit

Figure 2 Switching Waveforms





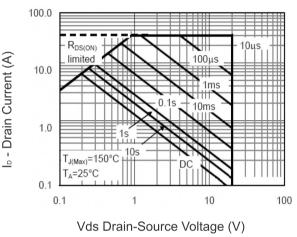


Figure 4 Safe Operation Area

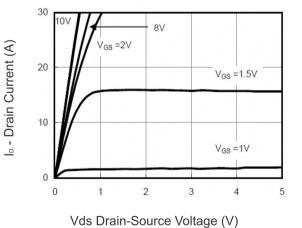


Figure 5 Output Characteristics

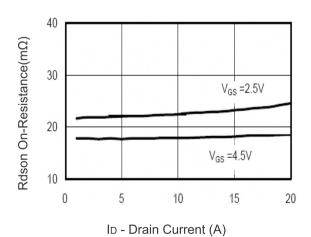


Figure 6 Drain-Source On-Resistance

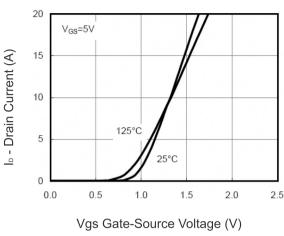


Figure 7 Transfer Characteristics

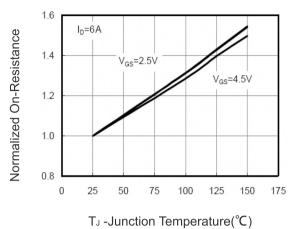


Figure 8 Drain-Source On-Resistance

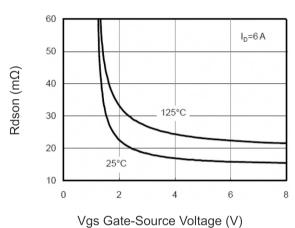


Figure 9 Rdson vs Vgs

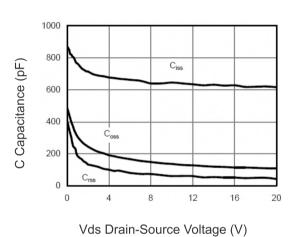
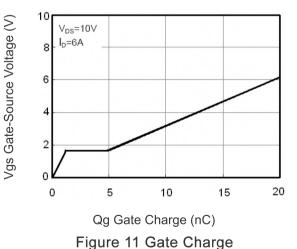


Figure 10 Capacitance vs Vds



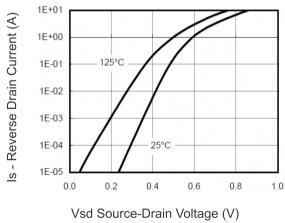


Figure 12 Source- Drain Diode Forward



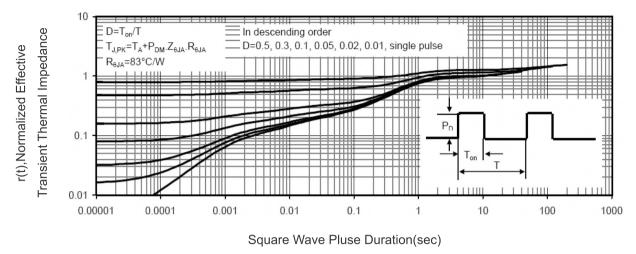
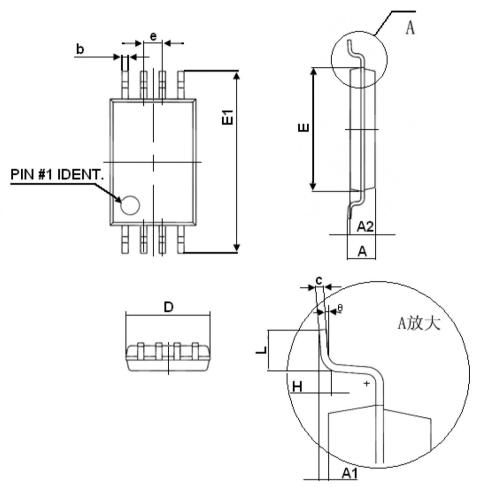


Figure 13 Normalized Maximum Transient Thermal Impedance



Tssop-8 Package Information



Symbol	Dimensions In Millimeters		
Symbol	Min	Max	
D	2.900	3.100	
E	4.300	4.500	
b	0.190	0.300	
С	0.090	0.200	
E1	6.250	6.550	
Α		1.100	
A2	0.800	1.000	
A 1	0.020	0.150	
е	0.65	(BSC)	
L	0.500	0.700	
Н	0.25(TYP)		
Θ	1°	7°	





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