



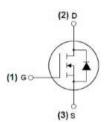
# MJ N-Channel Enhancement Mode Power MOSFET

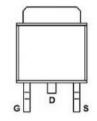
## Description

The MJ1540KA uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### General Features

- ♦  $V_{DS}$  =150V, $I_{D}$  =40A  $R_{DS(ON)}$  <45 $m\Omega$  @  $V_{GS}$ =10V (Typ:35 $m\Omega$ )
- ♦ High density cell design for ultra low Rdson
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high EAS
- ◆ Excellent package for good heat dissipation
- ◆ Special process technology for high ESD capability



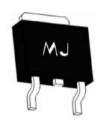


Application

◆ Power switching application

Uninterruptible power supply

Hard switched and high frequency circuits



Schematic diagram

Marking and pin assignment

TO-252-2L top view

### 100% UIS TESTED! 100% ΔVds TESTED!

# Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ1540KA	MJ1540KA	TO-252-2L	ä	=	-

### Absolute Maximum Ratings (Tc =25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	150	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	lo	40	А
Drain Current-Continuous(Tc =100℃)	I <sub>D</sub> (100℃)	29	А
Pulsed Drain Current	Ідм	160	А
Maximum Power Dissipation	PD	140	W
Derating factor		0.93	W/°C
Single pulse avalanche energy (Note 5)	Eas	350	mJ
Operating Junction and Storage Temperature Range	TJ ,TsTG	-55 To 175	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	1.07	°C/W
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# Electrical Characteristics (Tc =25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics				'		
Drain-Source Breakdown Voltage	BVDSS	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	150	-	-	V
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> =150V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	less	V <sub>DS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	0.7	1.05	1.4	V
Drain-Source On-State Resistance	Rds(ON)	V <sub>GS</sub> =10V, I <sub>D</sub> =18A	-	35	45	mΩ
Forward Transconductance	gFS	V <sub>DS</sub> =5V,I <sub>D</sub> =18A	38	-	-	S
Dynamic Characteristics (Note 4)				1		ı
Input Capacitance	Clss		-	4300	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =75V,V <sub>GS</sub> =0V F=1.0MHz	-	130	-	PF
Reverse Transfer Capacitance	Crss		-	111	-	PF
Switching Characteristics (Note 4)	'					
Turn-on Delay Time	t <sub>d(on)</sub>		-	14	-	nS
Turn-on Rise Time	tr	VDD=30V,ID=2A,RL=15Ω	-	12	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> =10V,R <sub>G</sub> =2.5Ω	-	45	-	nS
Turn-Off Fall Time	tr		-	11	-	nS
Total Gate Charge	Qg		-	63.8	-	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> =75V,I <sub>D</sub> =18A V <sub>GS</sub> =4.5V	-	9.8	-	nC
Gate-Drain Charge	Qgd	-	-	26.9	-	nC
Drain-Source Diode Characteristics				<u> </u>	<u> </u>	l
Diode Forward Voltage (Note 3)	Vsp	V <sub>GS</sub> =0V,I <sub>S</sub> =18A	-	_	1.2	V
Diode Forward Current (Note 2)	Is		-	-	40	А
Reverse Recovery Time	trr	T1-25°C 15-40A	-	42	-	nS
Reverse Recovery Charge	Qm	TJ=25°C, IF=18A di/dt=100A/µs (Note 3)		75		nC

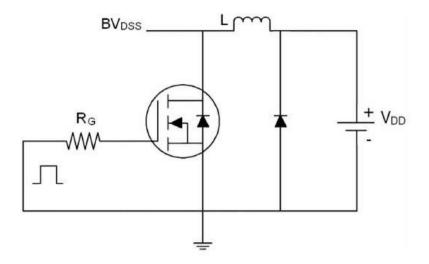
#### Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4 Guaranteed by design, not subject to production

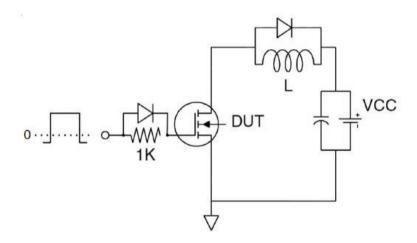




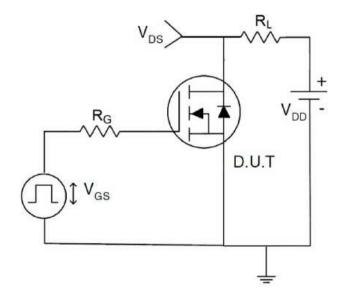
## Test circuit



Eas test Circuit



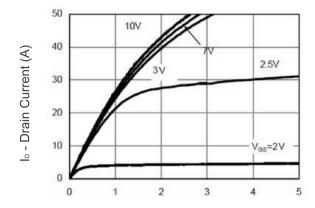
Gate charge test Circuit



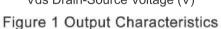
Switch Time Test Circuit

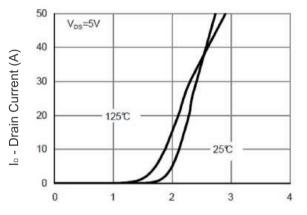


# Typical Electrical and Thermal Characteristics (Curves)



Vds Drain-Source Voltage (V)





Vgs Gate-Source Voltage (V) Figure 2 Transfer Characteristics

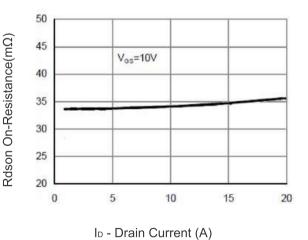
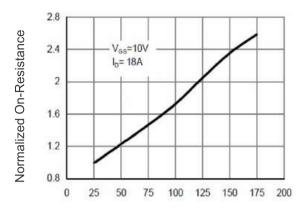
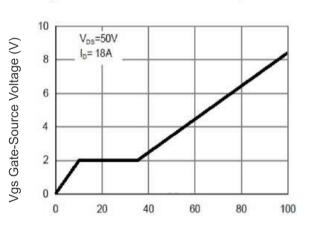


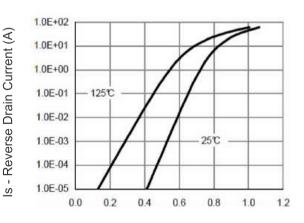
Figure 3 Rdson- Drain Current



T<sub>J</sub> -Junction Temperature(°C) Figure 4 Rdson-Junction Temperature

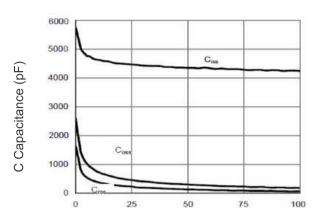


Qg Gate Charge (nC) Figure 5 Gate Charge



Vsd Source-Drain Voltage (V) Figure 6 Source- Drain Diode Forward

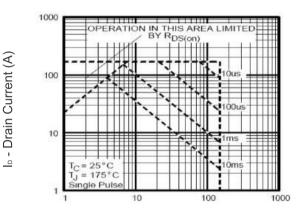


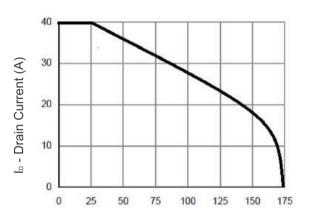


140 120 100 100 80 60 40 20 0 25 50 75 100 125 150 175

Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds

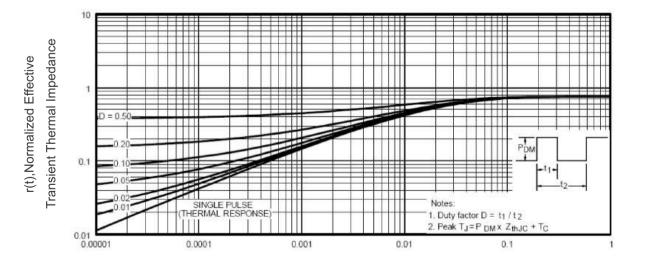
T<sub>J</sub> -Junction Temperature(°C) Figure 9 Power De-rating





Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area

T<sub>J</sub> -Junction Temperature(°C)
Figure 10 Current De-rating



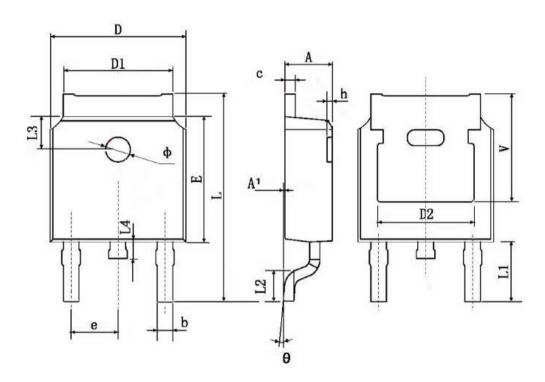
Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance





# TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimension	s In Inches
	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.8	30 TYP.	0.190 TYP.	
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900	TYP.	0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600	TYP.	0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0.	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211	TYP.





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