

MJ N-Channel Enhancement Mode Power MOSFET

Description

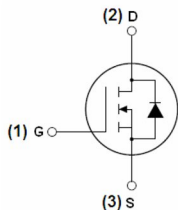
The MJ02H10T uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

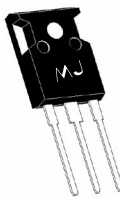
- ◆ $V_{DS}=200V, I_D=100A$
 $R_{DS(ON)}<18m\Omega$ @ $V_{GS}=10V$
- ◆ High density cell design for ultra low R_{dson}
- ◆ Fully characterized avalanche voltage and current
- ◆ Good stability and uniformity with high E_{AS}
- ◆ Excellent package for good heat dissipation
- ◆ Special process technology for high ESD capability

Application

- ◆ Power switching application
- ◆ Hard switched and high frequency circuits
- ◆ Uninterruptible power supply



Schematic diagram



TO-247 top view

100% UIS TESTED! 100% ΔV_{ds} TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MJ02H10T	MJ02H10T	TO-247	-	-	-

Absolute Maximum Ratings ($T_c=25\text{ }^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	200	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	100	A
Drain Current-Continuous($T_c=100^{\circ}\text{C}$)	$I_{D(100^{\circ}\text{C})}$	70.7	A
Pulsed Drain Current	I_{DM}	400	A
Maximum Power Dissipation	P_D	400	W
Single pulse avalanche energy ^(Note 3)	E_{AS}	1369	mJ
Derating factor		2.67	W/ $^{\circ}\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^{\circ}\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 1)	$R_{\theta JA}$	0.38	$^{\circ}\text{C}/\text{W}$
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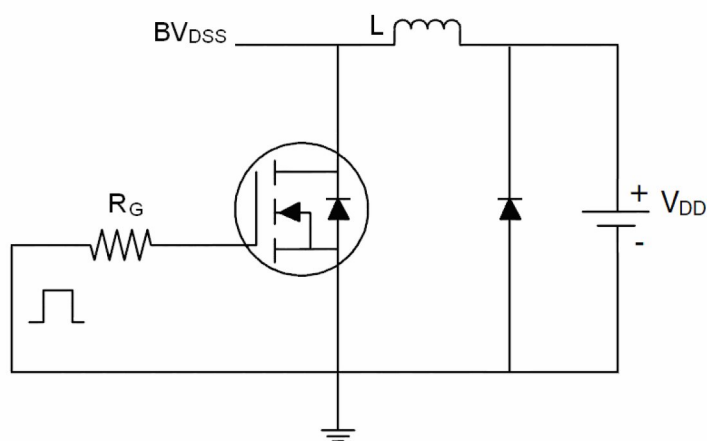
Electrical Characteristics (T_A =25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	200	220	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =200V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{DS} =±20V,V _{GS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =50A	-	13.5	18	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V,I _D =50A	50	-	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C _{iss}	V _{DS} =50V,V _{GS} =0V, F=1.0MHz	-	9382	-	PF
Output Capacitance	C _{oss}		-	529	-	PF
Reverse Transfer Capacitance	C _{rss}		-	206	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =100V, R _L =15Ω V _{GS} =10V,R _G =2.5Ω	-	35	-	nS
Turn-on Rise Time	t _r		-	30	-	nS
Turn-Off Delay Time	t _{d(off)}		-	55	-	nS
Turn-Off Fall Time	t _f		-	25	-	nS
Total Gate Charge	Q _g	V _{DS} =100V,I _D =50A, V _{GS} =10V ^(Note2)	-	150.9	-	nC
Gate-Source Charge	Q _{gs}		-	36.8	-	nC
Gate-Drain Charge	Q _{gd}		-	52.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V,I _S =50A	-	-	1.2	V
Diode Forward Current ^(Note 2)	I _S		-	-	100	A
Reverse Recovery Time	t _{rr}	T _J =25°C, I _F =50A di/dt=100A/μs ^(Note 3)	-	52	-	nS
Reverse Recovery Charge	Q _{rr}		-	80	-	nC
Forward Turn-On Time	ton	Intrinsic turn-on time is negligible(turn-on is dominated by LS+LD)				

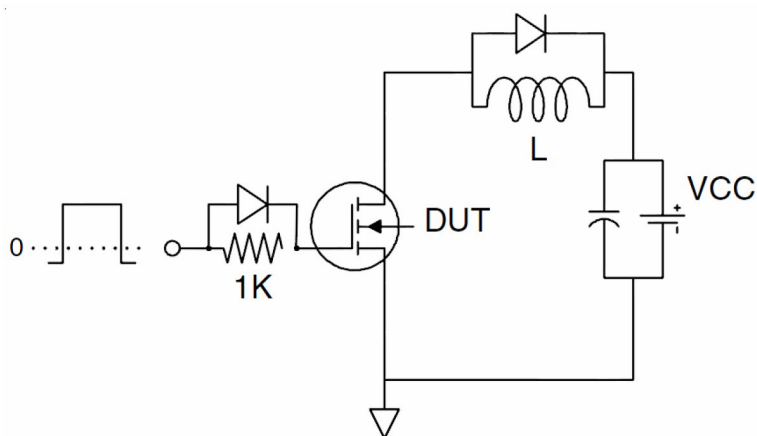
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t≤10sec.
- ③ Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%.
- ④ Guaranteed by design, not subject to production
- ⑤ E_{AS} condition: V_{DD}=50V,V_G=10V,L=0.5mH,R_g=25Ω

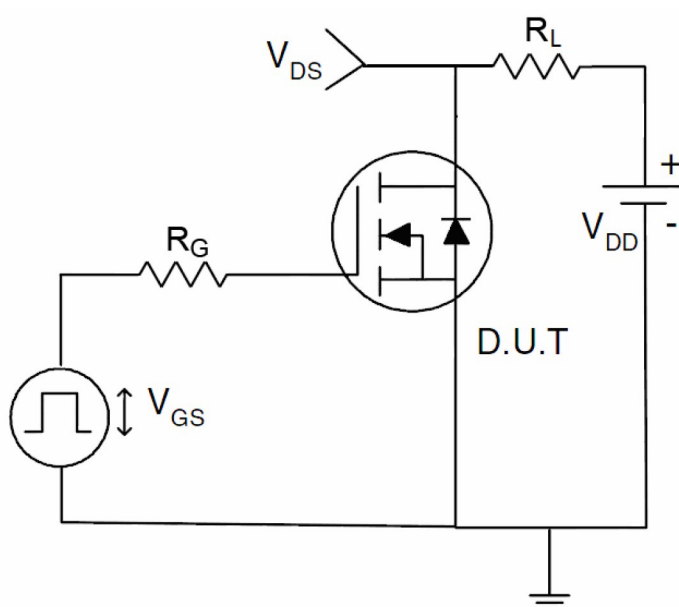
Test circuit



EAS test Circuit



Gate charge test Circuit



Switch Time Test Circuit

Typical Electrical and Thermal Characteristics (Curves)

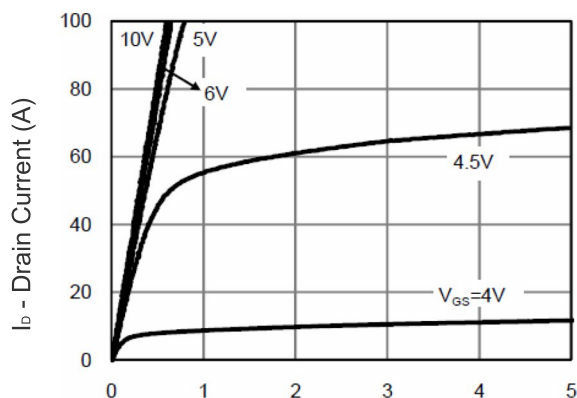


Figure 1 Output Characteristics

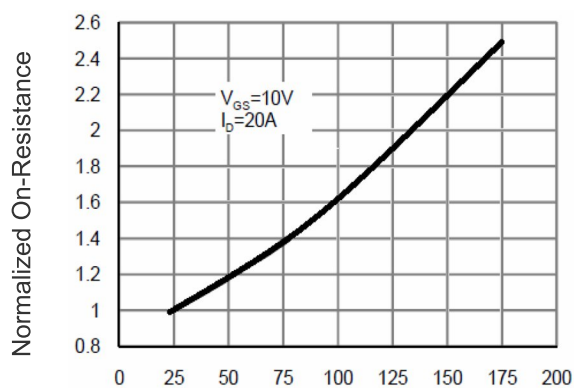


Figure 4 $R_{DS(on)}$ -Junction Temperature

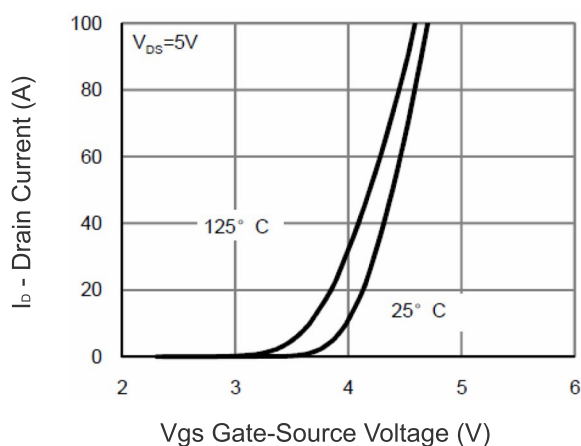


Figure 2 Transfer Characteristics

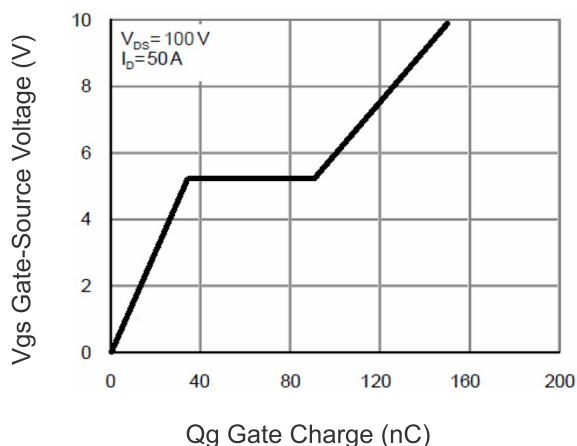


Figure 5 Gate Charge

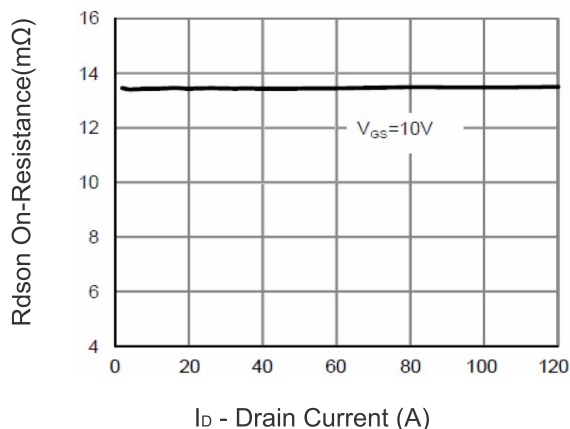


Figure 3 $R_{DS(on)}$ - Drain Current

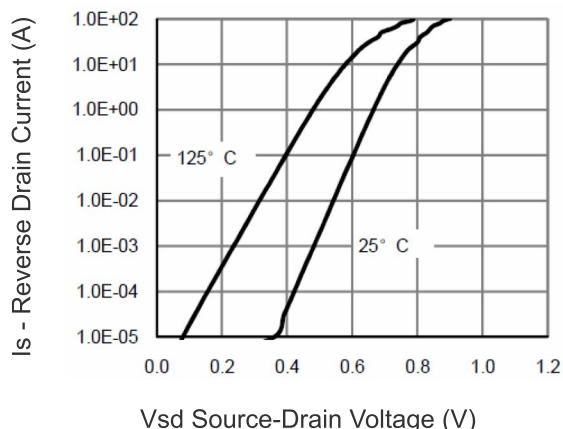
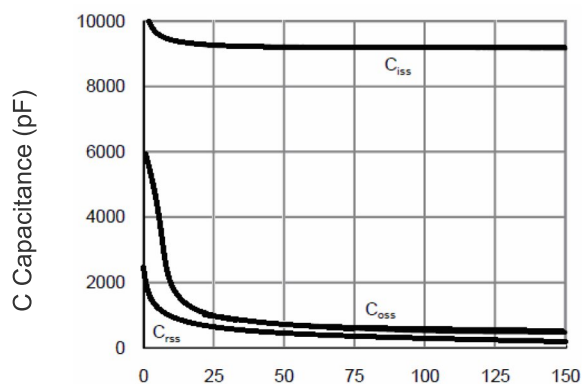
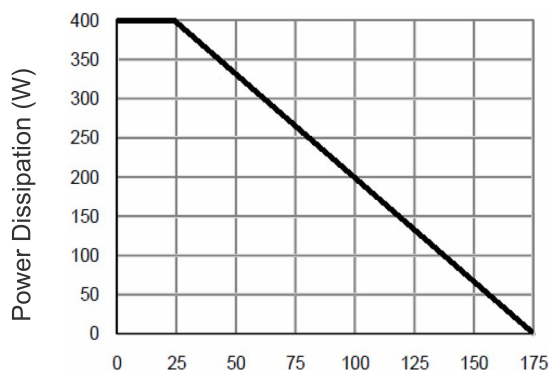


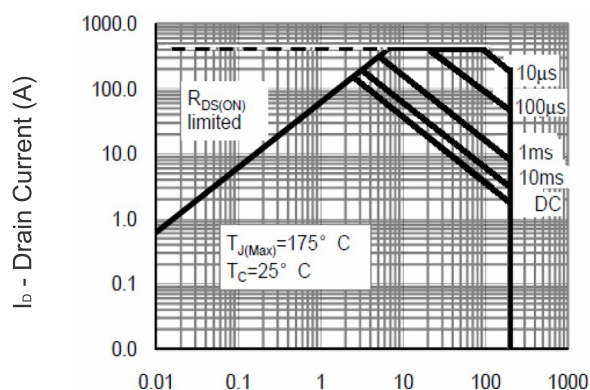
Figure 6 Source- Drain Diode Forward



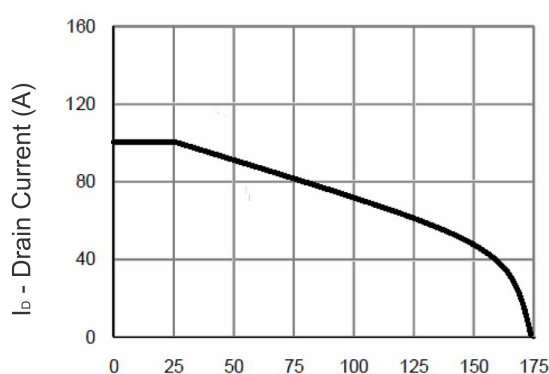
Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds



TJ -Junction Temperature(°C)
Figure 9 Power De-rating



Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area



TJ -Junction Temperature(°C)
Figure 10 Current De-rating

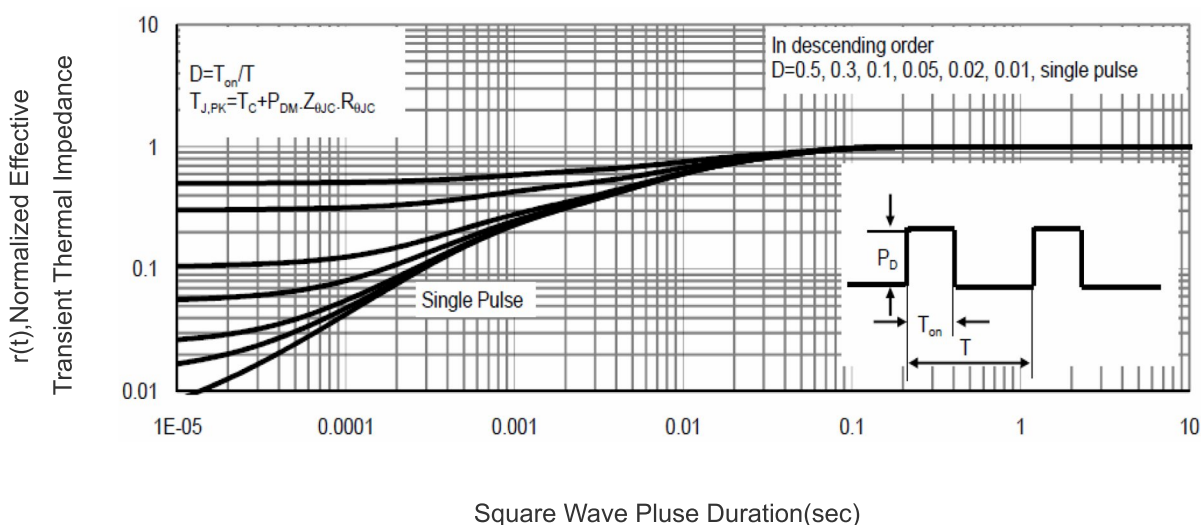
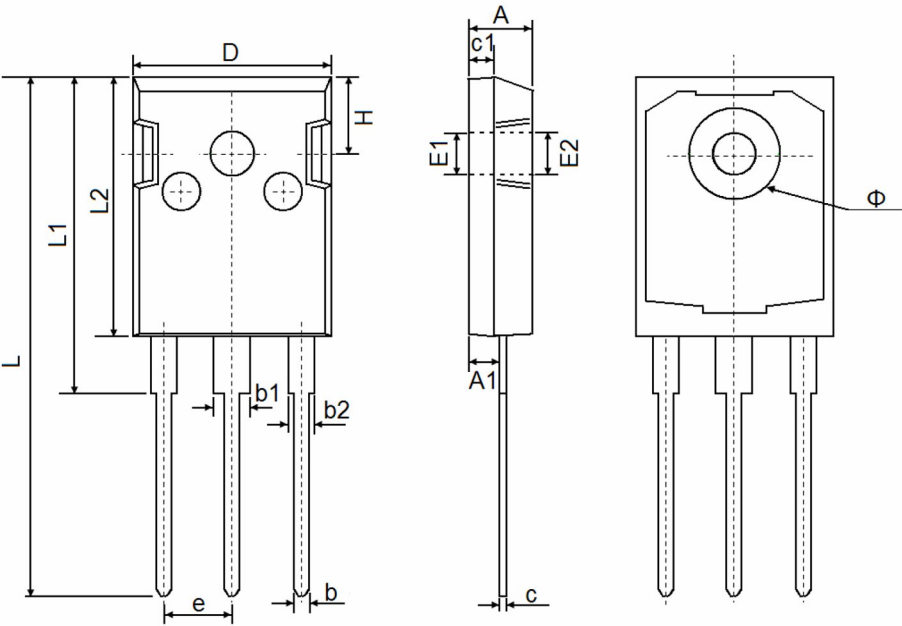


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-247 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087
c	0.500	0.700	0.020	0.028
c1	1.900	2.100	0.075	0.083
D	15.450	15.750	0.608	0.620
E1	3.500 REF		0.138 REF	
E2	3.600 REF		0.142 REF	
L	40.900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Φ	7.100	7.300	0.280	0.287
e	5.450 TYP		0.215 TYP	
H	5.980 REF		0.235 REF	

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