

# MJ N-Channel Enhancement Mode Power MOSFET

## Description

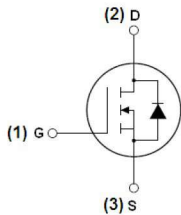
The MJ0208IA uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## General Features

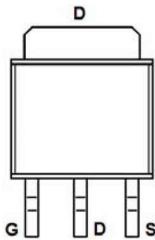
- ◆  $V_{DS} = 200V, I_D = 8A$   
 $R_{DS(ON)} < 300m\Omega$  @  $V_{GS} = 10V$  (Typ: 260m $\Omega$ )
- ◆ High density cell design for ultra low  $R_{dson}$
- ◆ Fully characterized avalanche voltage and current
- ◆ Low gate to drain charge to reduce switching losses

## Application

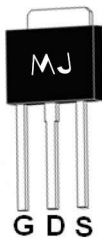
- ◆ Power switching application
- ◆ Hard switched and high frequency circuits
- ◆ Uninterruptible power supply



Schematic diagram



Marking and pin assignment



TO-251 top view

100%  $\Delta V_{ds}$  TESTED!

## Package Marking and Ordering Information

| Device Marking | Device   | Device Package | Reel Size | Tape width | Quantity |
|----------------|----------|----------------|-----------|------------|----------|
| MJ0208IA       | MJ0208IA | TO-251         | -         | -          | -        |

## Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)

| Parameter  | Symbol                | Limit      | Unit |
|--|-----------------------|------------|------|
| Drain-Source Voltage                             | $V_{DS}$              | 200        | V    |
| Gate-Source Voltage                              | $V_{GS}$              | ±20        | V    |
| Drain Current-Continuous                         | $I_D$                 | 8          | A    |
| Drain Current-Continuous(T <sub>c</sub> =100°C)  | $I_{D(100^{\circ}C)}$ | 5.6        | A    |
| Pulsed Drain Current                             | $I_{DM}$              | 20         | A    |
| Maximum Power Dissipation                        | $P_D$                 | 55         | W    |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$        | -55 To 150 | °C   |

## Thermal Characteristic

|   |                 |     |      |
|---|-----------------|-----|------|
| Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup> | $R_{\theta JC}$ | 2.3 | °C/W |
|---|-----------------|-----|------|

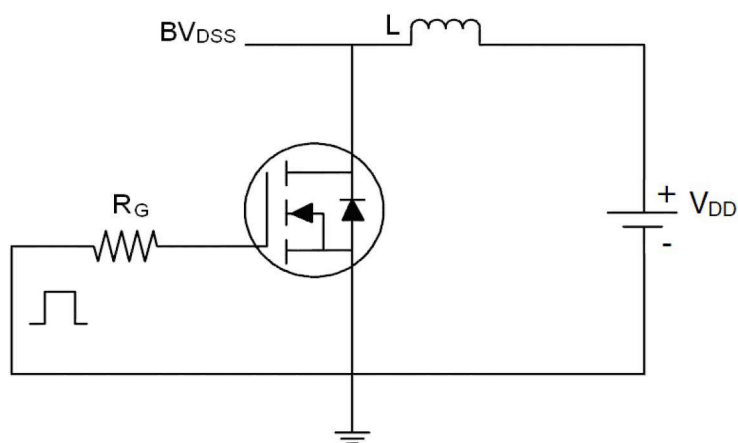
Electrical Characteristics (Tc=25℃ unless otherwise noted)

| Parameter                                     | Symbol              | Condition   | Min | Typ | Max  | Unit |
|---|---------------------|---|-----|-----|------|------|
| Off Characteristics                           |                     |   |     |     |      |      |
| Drain-Source Breakdown Voltage                | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V I <sub>D</sub> =250μA   | 200 | 215 | -    | V    |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>    | V <sub>DS</sub> =200V,V <sub>GS</sub> =0V   | -   | -   | 1    | μA   |
| Gate-Body Leakage Current                     | I <sub>GSS</sub>    | V <sub>DS</sub> =±20V,V <sub>GS</sub> =0V   | -   | -   | ±100 | nA   |
| On Characteristics <sup>(Note 3)</sup>        |                     |   |     |     |      |      |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA                                 | 1   | 1.7 | 2.5  | V    |
| Drain-Source On-State Resistance              | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A  | -   | 260 | 300  | mΩ   |
| Forward Transconductance                      | g <sub>FS</sub>     | V <sub>DS</sub> =25V,I <sub>D</sub> =4.5A   | 3   | -   | -    | S    |
| Dynamic Characteristics <sup>(Note 4)</sup>   |                     |   |     |     |      |      |
| Input Capacitance                             | C <sub>iss</sub>    | V <sub>DS</sub> =25V,V <sub>GS</sub> =0V<br>F=1.0MHz                                    | -   | 540 | -    | PF   |
| Output Capacitance                            | C <sub>oss</sub>    |   | -   | 90  | -    | PF   |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>    |   | -   | 35  | -    | PF   |
| Switching Characteristics <sup>(Note 4)</sup> |                     |   |     |     |      |      |
| Turn-on Delay Time                            | t <sub>d(on)</sub>  | V <sub>DD</sub> =100V,I <sub>D</sub> =4.5A<br>V <sub>GS</sub> =10V,R <sub>GEN</sub> =5Ω | -   | 6.4 | -    | nS   |
| Turn-on Rise Time                             | t <sub>r</sub>      |   | -   | 11  | -    | nS   |
| Turn-Off Delay Time                           | t <sub>d(off)</sub> |   | -   | 20  | -    | nS   |
| Turn-Off Fall Time                            | t <sub>f</sub>      |   | -   | 12  | -    | nS   |
| Total Gate Charge                             | Q <sub>g</sub>      | V <sub>DS</sub> =160V,I <sub>D</sub> =4.5A<br>V <sub>GS</sub> =10V                      | -   | 16  | -    | nC   |
| Gate-Source Charge                            | Q <sub>gs</sub>     |   | -   | 3.4 | -    | nC   |
| Gate-Drain Charge                             | Q <sub>gd</sub>     |   | -   | 5.1 | -    | nC   |
| Drain-Source Diode Characteristics            |                     |   |     |     |      |      |
| Diode Forward Voltage <sup>(Note 3)</sup>     | V <sub>SD</sub>     | V <sub>GS</sub> =0V,I <sub>S</sub> =4.5A  | -   | -   | 1.2  | V    |
| Diode Forward Current <sup>(Note 2)</sup>     | I <sub>S</sub>      |   | -   | -   | 8    | A    |

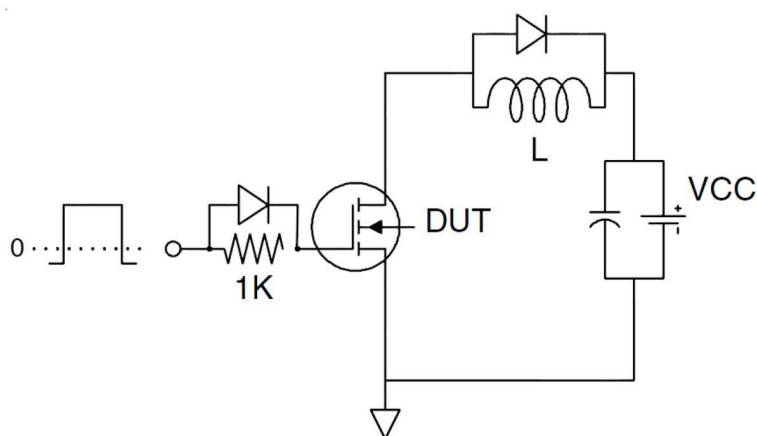
Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③ Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④ Guaranteed by design, not subject to production

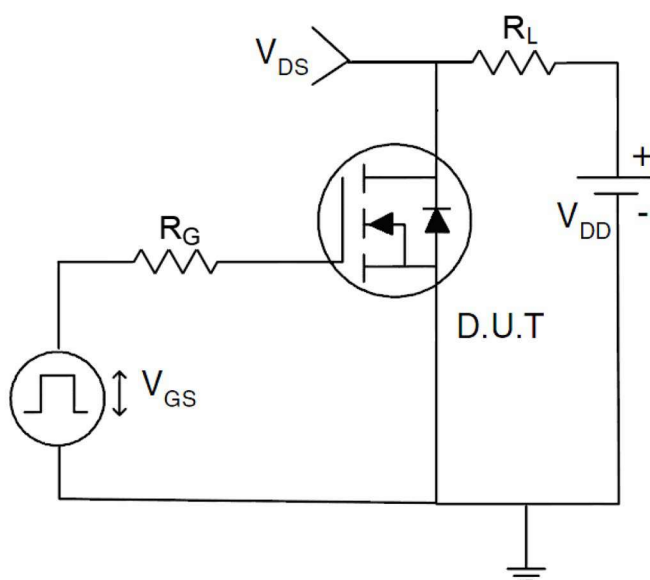
# Test circuit



EAS test Circuit

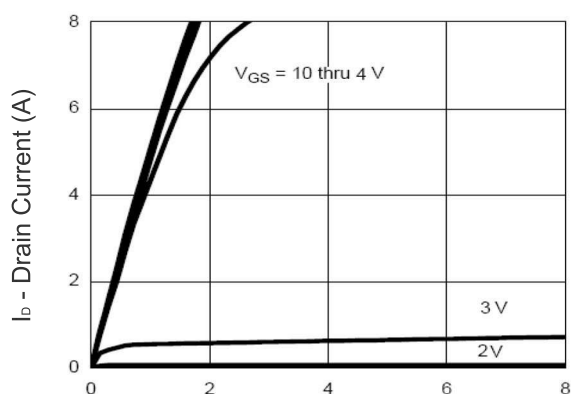


Gate charge test Circuit

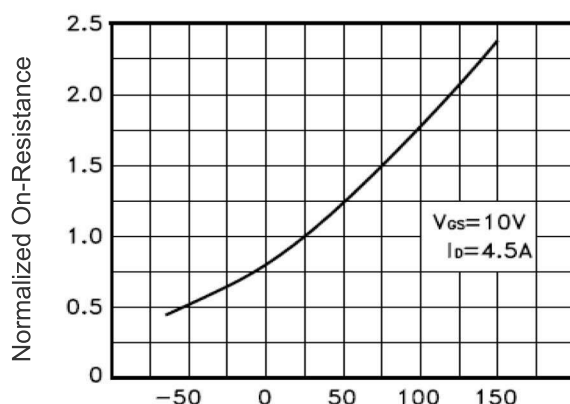


Switch Time Test Circuit

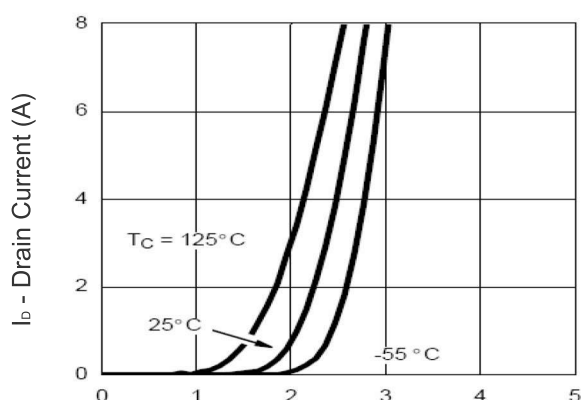
## Typical Electrical and Thermal Characteristics (Curves)



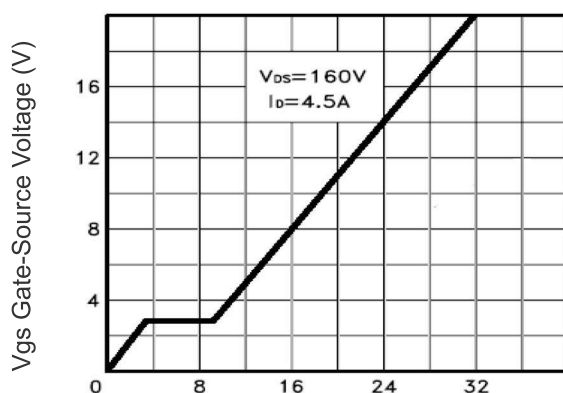
V<sub>DS</sub> Drain-Source Voltage (V)  
Figure 1 Output Characteristics



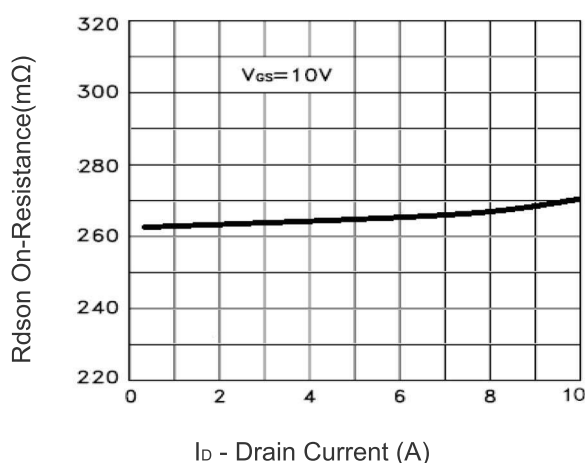
$T_J$  -Junction Temperature( $^{\circ}\text{C}$ )  
Figure 4  $R_{DS(on)}$ -Junction Temperature



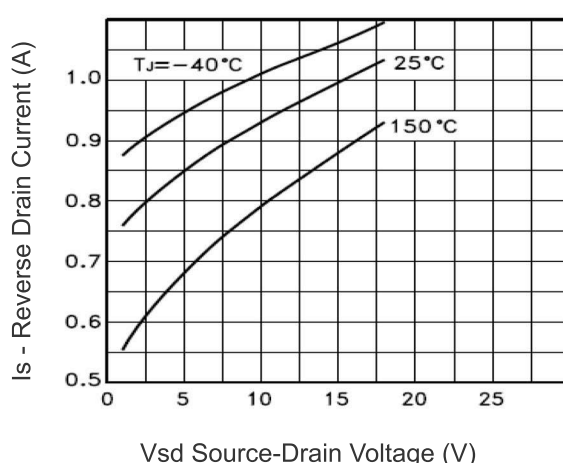
V<sub>GS</sub> Gate-Source Voltage (V)  
Figure 2 Transfer Characteristics



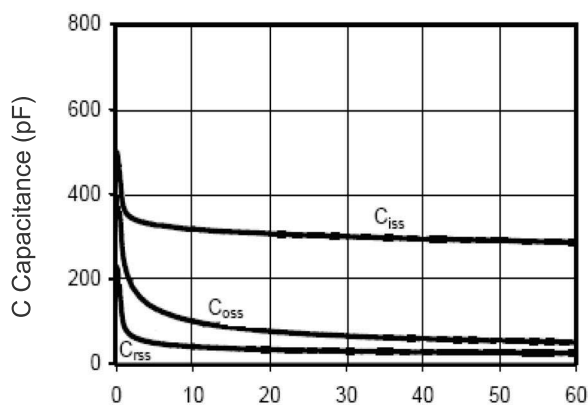
$Q_g$  Gate Charge (nC)  
Figure 5 Gate Charge



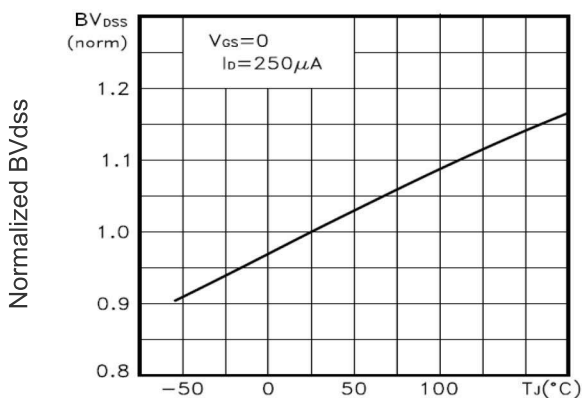
$I_D$  - Drain Current (A)  
Figure 3  $R_{DS(on)}$ - Drain Current



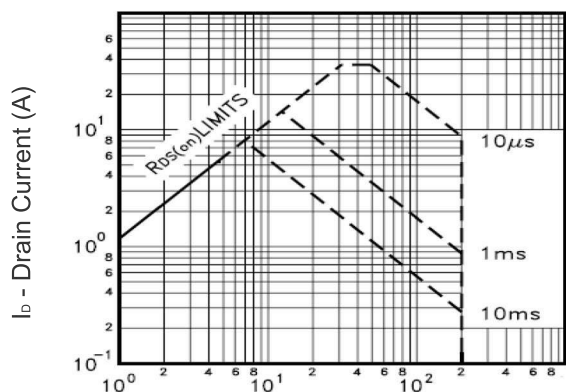
V<sub>SD</sub> Source-Drain Voltage (V)  
Figure 6 Source- Drain Diode Forward



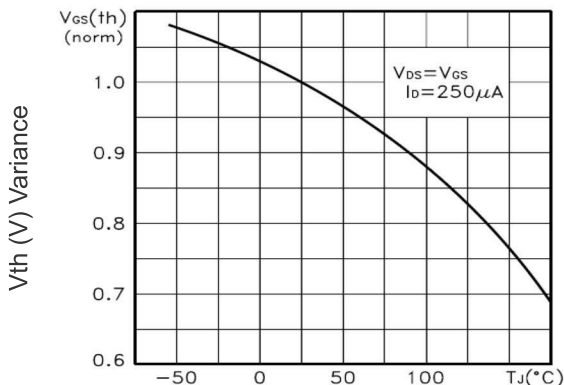
Vds Drain-Source Voltage (V)  
Figure 7 Capacitance vs Vds



Tj -Junction Temperature(°C)  
Figure 9 BV<sub>DSS</sub> vs Junction Temperature



Vds Drain-Source Voltage (V)  
Figure 8 Safe Operation Area



Tj -Junction Temperature(°C)  
Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

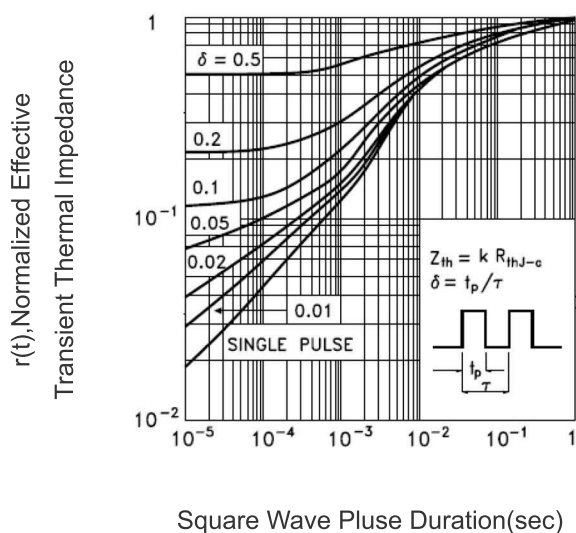
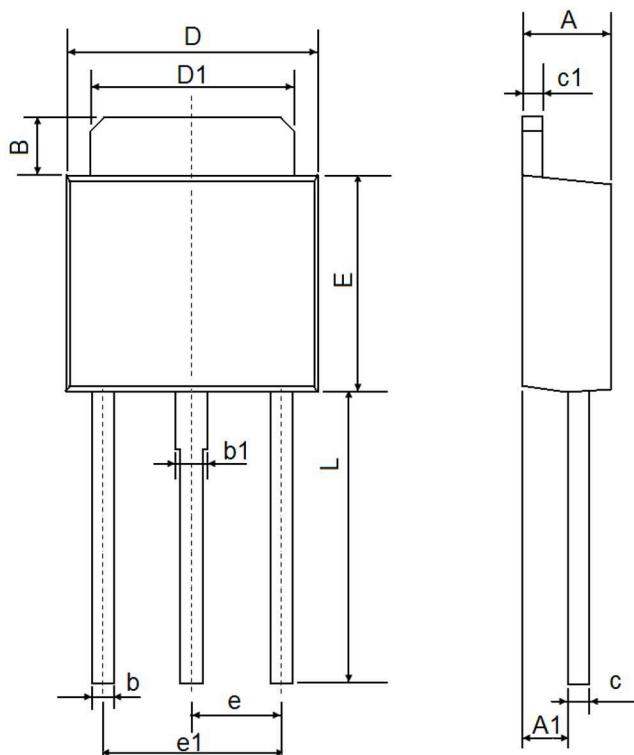


Figure 11 Normalized Maximum Transient Thermal Impedance

# TO-251 Package Information



| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min.                      | Max.  | Min.                 | Max.  |
| A      | 2.200                     | 2.400 | 0.087                | 0.094 |
| A1     | 1.050                     | 1.350 | 0.042                | 0.054 |
| B      | 1.350                     | 1.650 | 0.053                | 0.065 |
| b      | 0.500                     | 0.700 | 0.020                | 0.028 |
| b1     | 0.700                     | 0.900 | 0.028                | 0.035 |
| c      | 0.430                     | 0.580 | 0.017                | 0.023 |
| c1     | 0.430                     | 0.580 | 0.017                | 0.023 |
| D      | 6.350                     | 6.650 | 0.250                | 0.262 |
| D1     | 5.200                     | 5.400 | 0.205                | 0.213 |
| E      | 5.400                     | 5.700 | 0.213                | 0.224 |
| e      | 2.300 TYP                 |       | 0.091 TYP            |       |
| e1     | 4.500                     | 4.700 | 0.177                | 0.185 |
| L      | 7.500                     | 7.900 | 0.295                | 0.311 |

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